

FEATURES

- Double Side Cooling
- High Surge Capability

KEY PARAMETERS

V_{RRM}	6000V
$I_{F(AV)}$	630A
I_{FSM}	10500A

VOLTAGE RATINGS

Part and Ordering Number	Repetitive Peak Voltages V_{RRM} V	Conditions
DRD630G60 DRD630G58 DRD630G56 DRD630G52	6000 5800 5600 5200	$V_{RSM} = V_{RRM} + 100V$

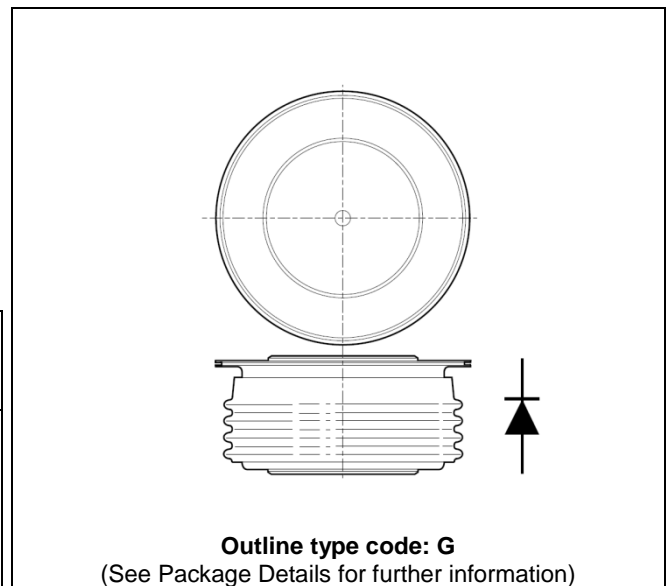


Fig. 1 Package outline

ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

DRD630G56 for a 5600V device

CURRENT RATINGS
T_{case} = 75°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
Double Side Cooled				
I _{F(AV)}	Mean forward current	Half wave resistive load	811	A
I _{F(RMS)}	RMS value	-	1274	A
I _F	Continuous (direct) on-state current	-	1172	A
Single Side Cooled (Anode side)				
I _{F(AV)}	Mean forward current	Half wave resistive load	534	A
I _{F(RMS)}	RMS value	-	839	A
I _F	Continuous (direct) on-state current	-	727	A

T_{case} = 100°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
Double Side Cooled				
I _{F(AV)}	Mean forward current	Half wave resistive load	630	A
I _{F(RMS)}	RMS value	-	990	A
I _F	Continuous (direct) on-state current	-	900	A
Single Side Cooled (Anode side)				
I _{F(AV)}	Mean forward current	Half wave resistive load	410	A
I _{F(RMS)}	RMS value	-	644	A
I _F	Continuous (direct) on-state current	-	550	A

SURGE RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
I_{FSM}	Surge (non-repetitive) on-state current	10ms half sine, $T_{case} = 150^{\circ}C$	8.5	kA
I^2t	I^2t for fusing	$V_R = 50\% V_{RRM} - 1/4$ sine	0.36	MA ² s
I_{FSM}	Surge (non-repetitive) on-state current	10ms half sine, $T_{case} = 150^{\circ}C$	10.5	kA
I^2t	I^2t for fusing	$V_R = 0$	0.565	MA ² s

THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions	Min.	Max.	Units	
$R_{th(j-c)}$	Thermal resistance – junction to case	Double side cooled	DC	-	0.032	$^{\circ}C/W$
		Single side cooled	Anode DC	-	0.064	$^{\circ}C/W$
			Cathode DC	-	0.064	$^{\circ}C/W$
$R_{th(c-h)}$	Thermal resistance – case to heatsink	Clamping force 12kN	Double side	-	0.008	$^{\circ}C/W$
		(with mounting compound)	Single side	-	0.016	$^{\circ}C/W$
T_{vj}	Virtual junction temperature	On-state (conducting)	-	160	$^{\circ}C$	
		Reverse (blocking)	-	150	$^{\circ}C$	
T_{stg}	Storage temperature range		-55	175	$^{\circ}C$	
F_m	Clamping force		11.5	13.5	kN	

CHARACTERISTICS

Symbol	Parameter	Test Conditions	Min.	Max.	Units
V_{FM}	Forward voltage	At 1800A peak, $T_{case} = 25^{\circ}C$	-	2.1	V
I_{RM}	Peak reverse current	At V_{RRM} , $T_{case} = 150^{\circ}C$	-	75	mA
Q_S	Total stored charge	$I_F = 1000A$, $dI_{RR}/dt = 3A/\mu s$	-	3000	μC
I_{rr}	Peak reverse recovery current	$T_{case} = 150^{\circ}C$, $V_R = 100V$	-	90	A
V_{TO}	Threshold voltage	At $T_{vj} = 150^{\circ}C$	-	0.9	V
r_T	Slope resistance	At $T_{vj} = 150^{\circ}C$	-	0.93	$m\Omega$

CURVES

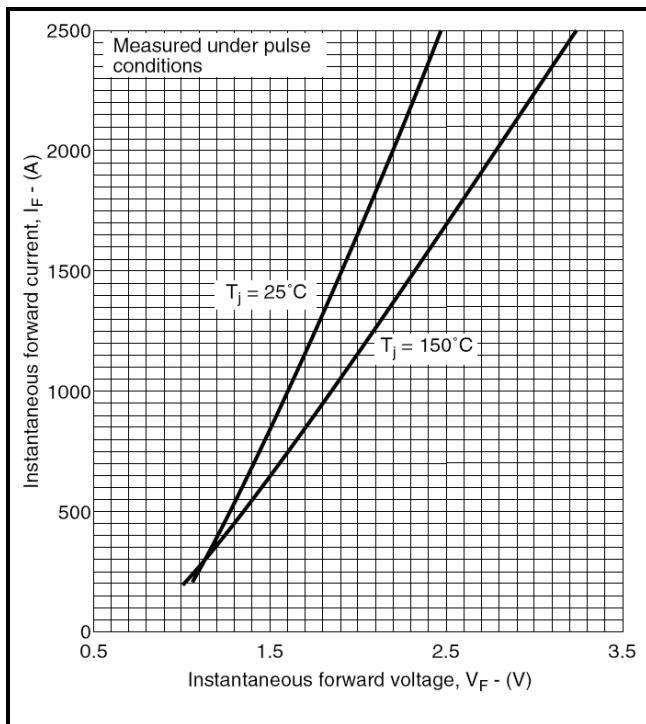


Fig.2 Maximum (limit) on-state characteristics

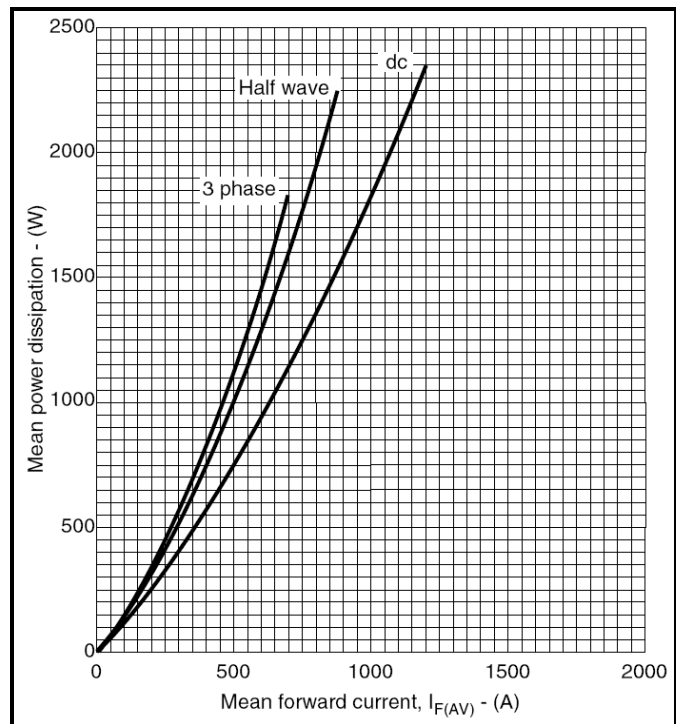


Fig.3 Dissipation curves

V_{TM} EQUATION

$$V_{TM} = A + B \ln(I_T) + C \cdot I_T + D \cdot \sqrt{I_T}$$

Where $A = 1.249986$

$B = -0.17646$

$C = 0.000524$

$D = 0.041024$

these values are valid for $T_j = 150^{\circ}C$ for I_F 500A to 2500A

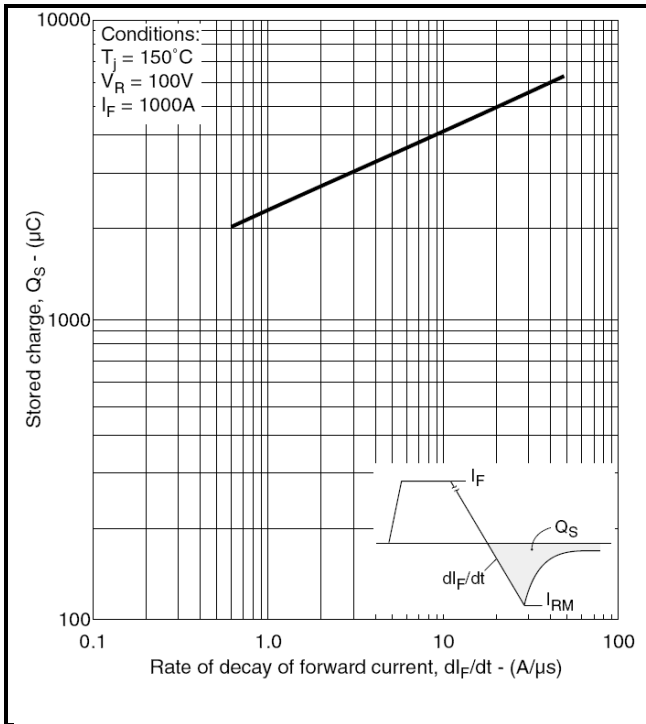


Fig.4 Total stored charge

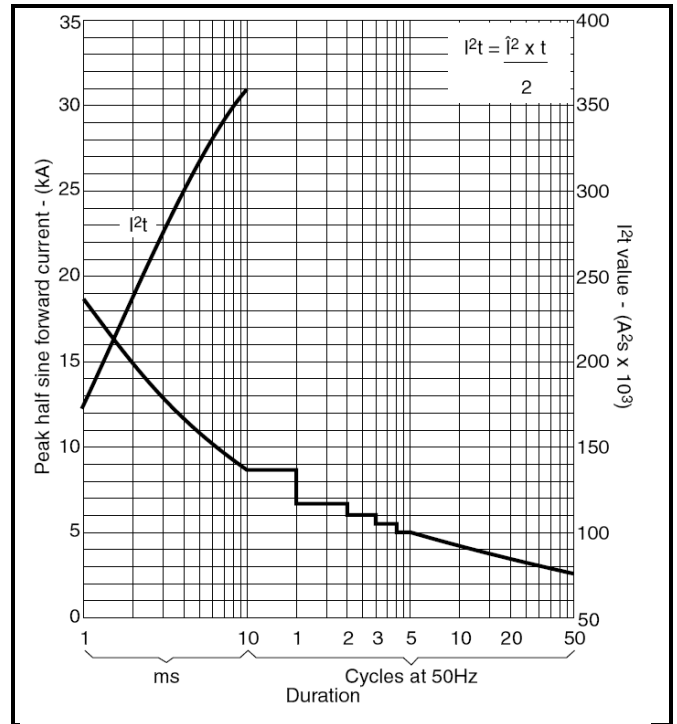


Fig.5 Surge (non-repetitive) forward current vs time (with 50% V_{RRM} at $T_{case} 150^\circ\text{C}$)

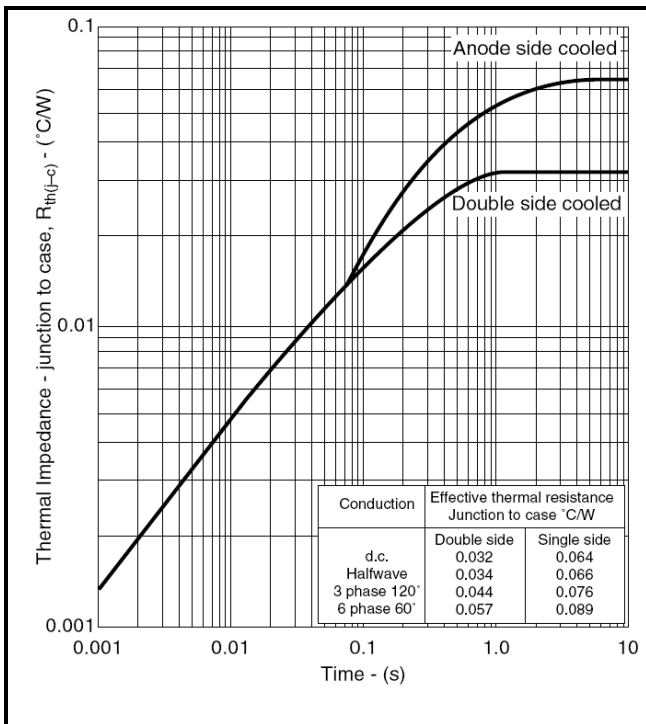
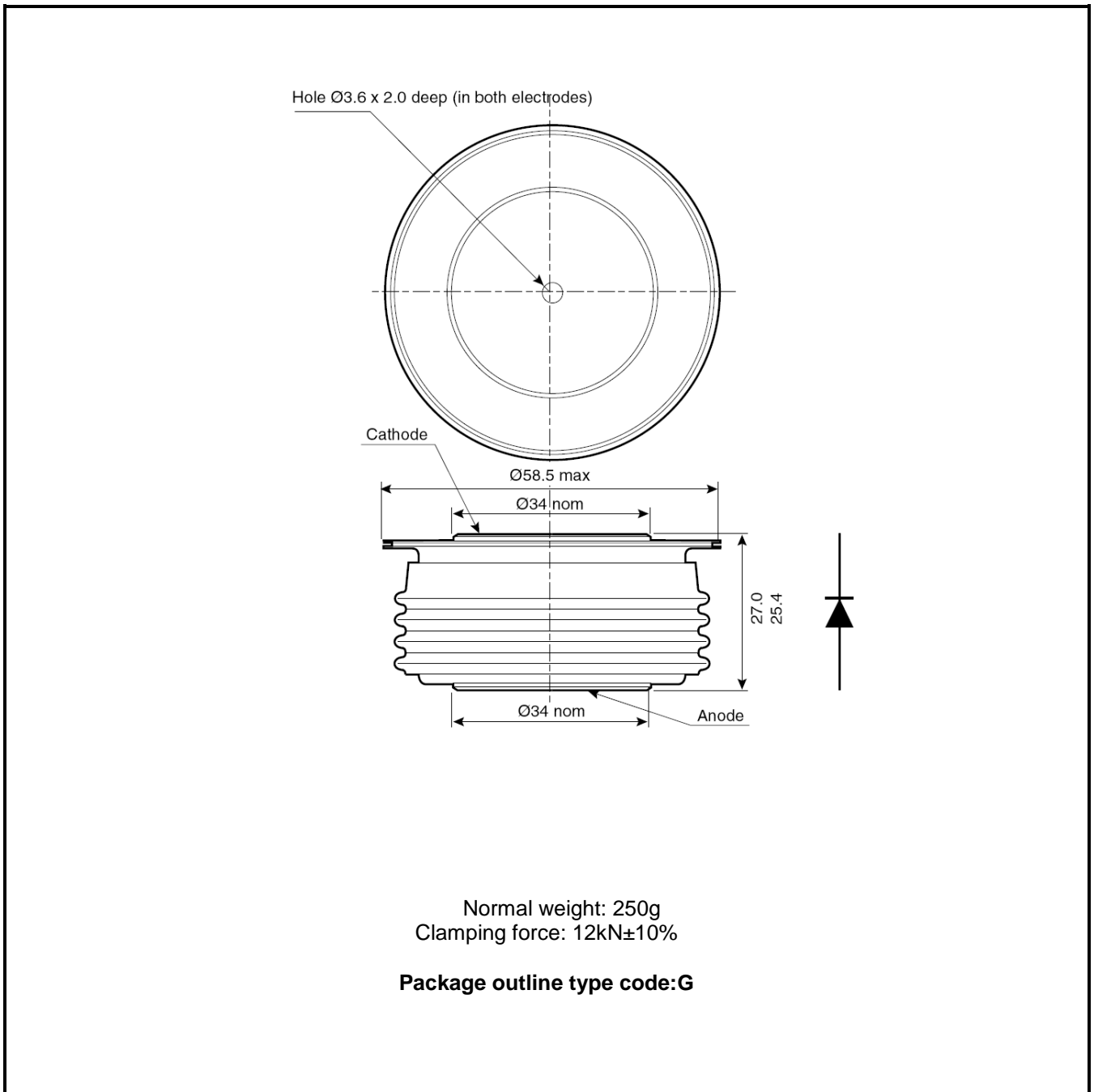


Fig.6 Maximum (limit) transient thermal impedance-junction to case

PACKAGE DETAILS

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.


Note:

Some packages may be supplied with gate and or tags.

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