

**FEATURES**

- Trench Gate IGBT
- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- Low  $V_{ce(sat)}$  Device
- High Current Density
- Isolated AISiC Base with AlN Substrates

**APPLICATIONS**

- High Reliability Inverters
- Motor Controllers
- Smart Grid
- Traction Drives

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM2400ESM17-PT500 is a single switch 1700V, trench gate, insulated gate bipolar transistor (IGBT) module with enhanced field stop and implantation technology. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10µs short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

**ORDERING INFORMATION**

Order As:

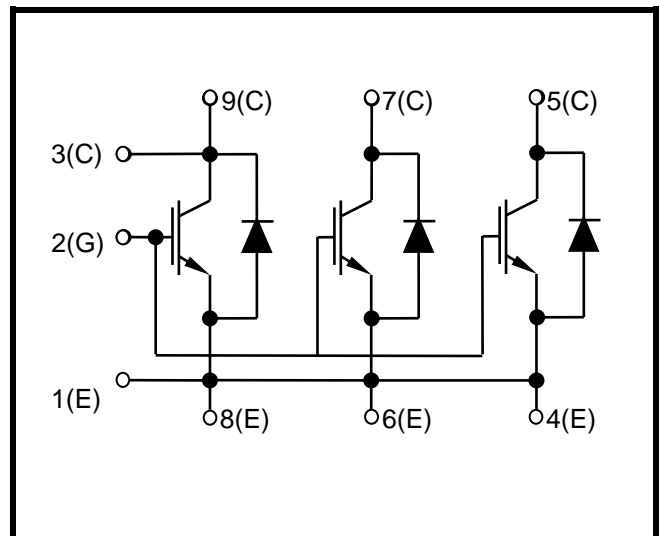
**DIM2400ESM17-PT500**

Note: When ordering, please use the complete part number

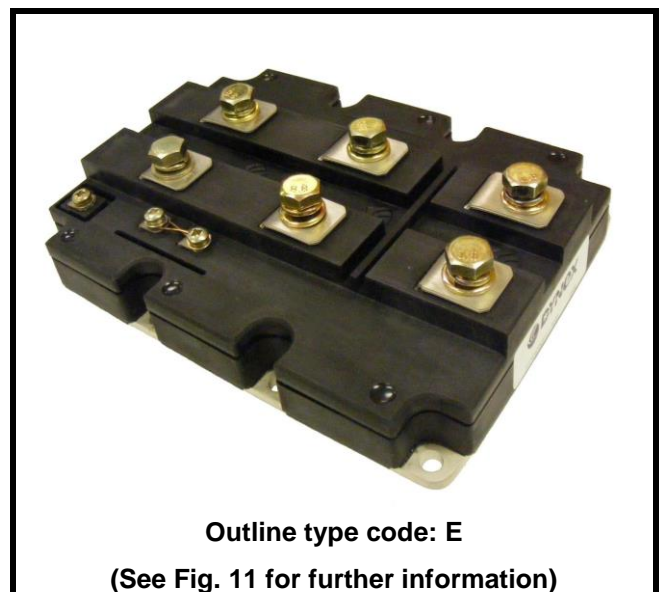
**KEY PARAMETERS**

$V_{CES}$	<b>1700V</b>
$V_{CE(sat)}$ * (typ)	<b>1.75V</b>
$I_C$ (max)	<b>2400A</b>
$I_{C(PK)}$ (max)	<b>4800A</b>

\* Measured at the power busbars, not the auxiliary terminals



**Fig. 1 Circuit configuration**



**Outline type code: E  
(See Fig. 11 for further information)**

**Fig. 2 Package**

**ABSOLUTE MAXIMUM RATINGS**

Stresses above those listed under ‘Absolute Maximum Ratings’ may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

**T<sub>case</sub> = 25°C unless stated otherwise**

Symbol	Parameter	Test Conditions	Max.	Units
V <sub>CES</sub>	Collector-emitter voltage	V <sub>GE</sub> = 0V	1700	V
V <sub>GES</sub>	Gate-emitter voltage		±20	V
I <sub>C</sub>	Continuous collector current	T <sub>case</sub> = 118°C	2400	A
I <sub>C(PK)</sub>	Peak collector current	1ms, T <sub>case</sub> = 140°C	4800	A
P <sub>max</sub>	Max. transistor power dissipation	T <sub>case</sub> = 25°C, T <sub>j</sub> = 150°C	20.8	kW
I <sup>2</sup> t	Diode I <sup>2</sup> t value	V <sub>R</sub> = 0, t <sub>p</sub> = 10ms, T <sub>j</sub> = 150°C	1170	kA <sup>2</sup> s
V <sub>isol</sub>	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	4000	V
Q <sub>PD</sub>	Partial discharge – per module	IEC1287, V <sub>1</sub> = 1800V, V <sub>2</sub> = 1300V, 50Hz RMS	10	pC

**THERMAL AND MECHANICAL RATINGS**

Internal insulation material: AIN  
 Baseplate material: AISiC  
 Creepage distance: 33mm  
 Clearance: 20mm  
 CTI (Comparative Tracking Index): >600

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
R <sub>th(j-c)</sub>	Thermal resistance – transistor	Continuous dissipation - junction to case	-	-	6	°C/kW
R <sub>th(j-c)</sub>	Thermal resistance – diode	Continuous dissipation - junction to case	-	-	12	°C/kW
R <sub>th(c-h)</sub>	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	6	°C/kW
T <sub>j</sub>	Junction temperature	Transistor	-	-	150	°C
		Diode	-	-	150	°C
T <sub>stg</sub>	Storage temperature range	-	-40	-	150	°C
	Screw torque	Mounting – M6	-	-	5	Nm
		Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm

## ELECTRICAL CHARACTERISTICS

$T_{case} = 25^{\circ}C$  unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
$I_{CES}$	Collector cut-off current	$V_{GE} = 0V, V_{CE} = V_{CES}$			1	mA
		$V_{GE} = 0V, V_{CE} = V_{CES}, T_{case} = 125^{\circ}C$			40	mA
$I_{GES}$	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$			60	$\mu A$
$V_{GE(TH)}$	Gate threshold voltage	$I_C = 120mA, V_{GE} = V_{CE}$	5.0	6.0	7.0	V
$V_{CE(sat)}$	Collector-emitter saturation voltage	$V_{GE} = 15V, I_C = 2400A$		1.75		V
		$V_{GE} = 15V, I_C = 2400A, T_j = 125^{\circ}C$		1.95		V
		$V_{GE} = 15V, I_C = 2400A, T_j = 150^{\circ}C$		2.05		
$I_F$	Diode forward current	DC		2400		A
$I_{FM}$	Diode maximum forward current	$t_p = 1ms$		4800		A
$V_F$	Diode forward voltage	$I_F = 2400A$		1.65		V
		$I_F = 2400A, T_j = 125^{\circ}C$		1.75		V
		$I_F = 2400A, T_j = 150^{\circ}C$		1.75		
$C_{ies}$	Input capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		400		nF
$Q_g$	Gate charge	$\pm 15V$		19		$\mu C$
$C_{res}$	Reverse transfer capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		3		nF
$L_M$	Module inductance			10		nH
$R_{INT}$	Internal transistor resistance			110		$\mu\Omega$
$SC_{Data}$	Short circuit current, $I_{SC}$	$T_j = 150^{\circ}C, V_{CC} = 1000V$ $t_p \leq 10\mu s, V_{GE} \leq 15V$ $V_{CE(max)} = V_{CES} - L^* \times di/dt$ IEC 60747-9		12000		A

**Note:**

\* L is the circuit inductance +  $L_M$

**ELECTRICAL CHARACTERISTICS**

**T<sub>case</sub> = 25°C unless stated otherwise**

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units	
t <sub>d(off)</sub>	Turn-off delay time	I <sub>C</sub> = 2400A V <sub>GE</sub> = ±15V V <sub>CE</sub> = 900V R <sub>G(ON)</sub> = 0.5Ω R <sub>G(OFF)</sub> = 0.5Ω L <sub>S</sub> ~ 50nH		2320		ns	
t <sub>f</sub>	Fall time			500		ns	
E <sub>OFF</sub>	Turn-off energy loss				1050		mJ
t <sub>d(on)</sub>	Turn-on delay time				450		ns
t <sub>r</sub>	Rise time				210		ns
E <sub>ON</sub>	Turn-on energy loss				410		mJ
Q <sub>rr</sub>	Diode reverse recovery charge	I <sub>F</sub> = 2400A V <sub>CE</sub> = 900V dI <sub>F</sub> /dt = 10000A/μs		480		μC	
I <sub>rr</sub>	Diode reverse recovery current			1000		A	
E <sub>rec</sub>	Diode reverse recovery energy				320		mJ

**T<sub>case</sub> = 125°C unless stated otherwise**

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units	
t <sub>d(off)</sub>	Turn-off delay time	I <sub>C</sub> = 2400A V <sub>GE</sub> = ±15V V <sub>CE</sub> = 900V R <sub>G(ON)</sub> = 0.5Ω R <sub>G(OFF)</sub> = 0.5Ω L <sub>S</sub> ~ 50nH		2340		ns	
t <sub>f</sub>	Fall time			510		ns	
E <sub>OFF</sub>	Turn-off energy loss				1320		mJ
t <sub>d(on)</sub>	Turn-on delay time				450		ns
t <sub>r</sub>	Rise time				220		ns
E <sub>ON</sub>	Turn-on energy loss				660		mJ
Q <sub>rr</sub>	Diode reverse recovery charge	I <sub>F</sub> = 2400A V <sub>CE</sub> = 900V dI <sub>F</sub> /dt = 10000A/μs		750		μC	
I <sub>rr</sub>	Diode reverse recovery current			1200		A	
E <sub>rec</sub>	Diode reverse recovery energy				550		mJ

**T<sub>case</sub> = 150°C unless stated otherwise**

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units	
t <sub>d(off)</sub>	Turn-off delay time	I <sub>C</sub> = 2400A V <sub>GE</sub> = ±15V V <sub>CE</sub> = 900V R <sub>G(ON)</sub> = 0.5Ω R <sub>G(OFF)</sub> = 0.5Ω L <sub>S</sub> ~ 50nH		2340		ns	
t <sub>f</sub>	Fall time			510		ns	
E <sub>OFF</sub>	Turn-off energy loss				1400		mJ
t <sub>d(on)</sub>	Turn-on delay time				450		ns
t <sub>r</sub>	Rise time				220		ns
E <sub>ON</sub>	Turn-on energy loss				820		mJ
Q <sub>rr</sub>	Diode reverse recovery charge	I <sub>F</sub> = 2400A V <sub>CE</sub> = 900V dI <sub>F</sub> /dt = 12000A/μs		820		μC	
I <sub>rr</sub>	Diode reverse recovery current			1250		A	
E <sub>rec</sub>	Diode reverse recovery energy				620		mJ

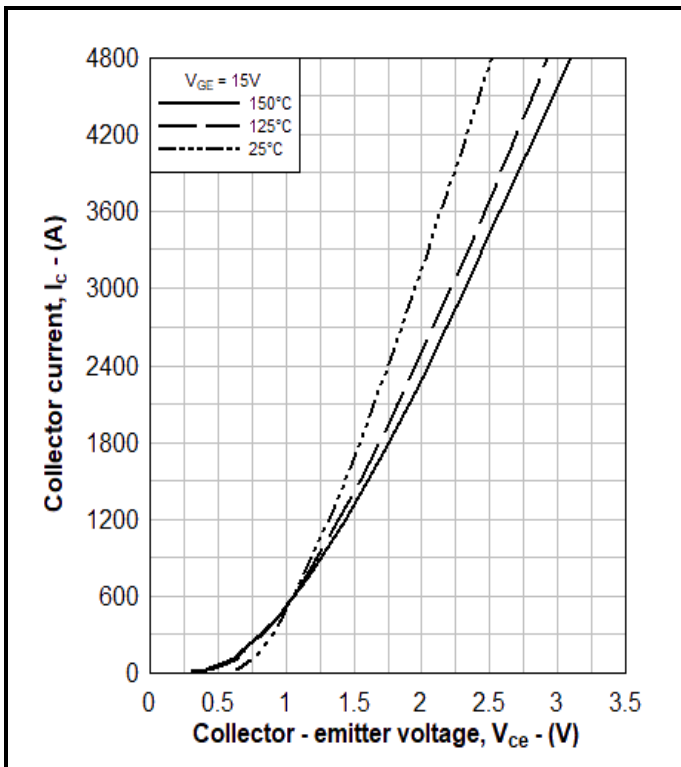


Fig. 3 Typical output characteristics

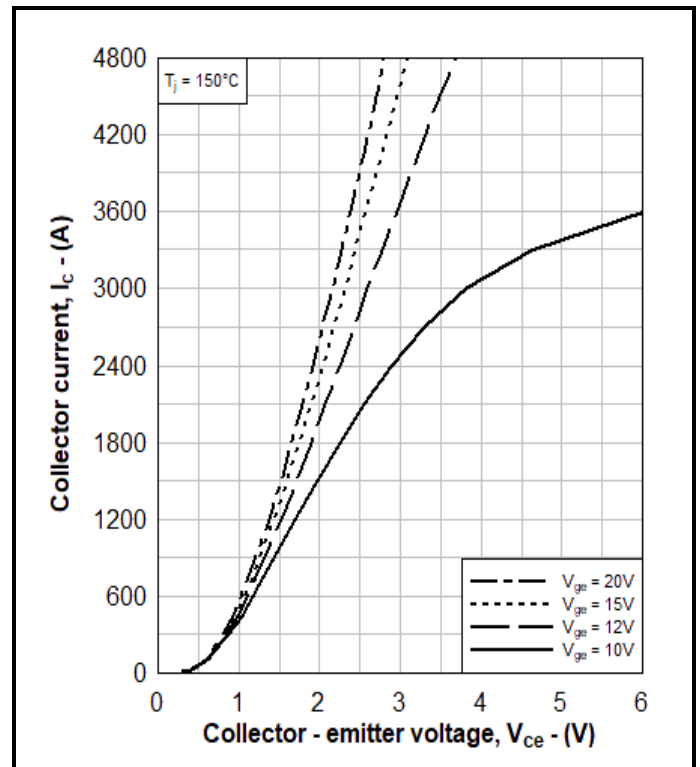


Fig. 4 Typical output characteristics

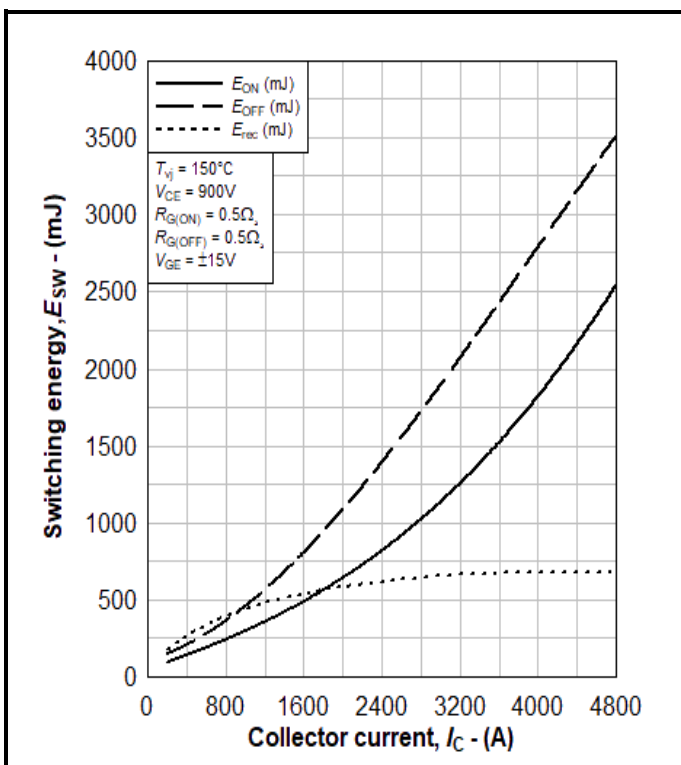


Fig. 5 Typical switching energy vs collector current

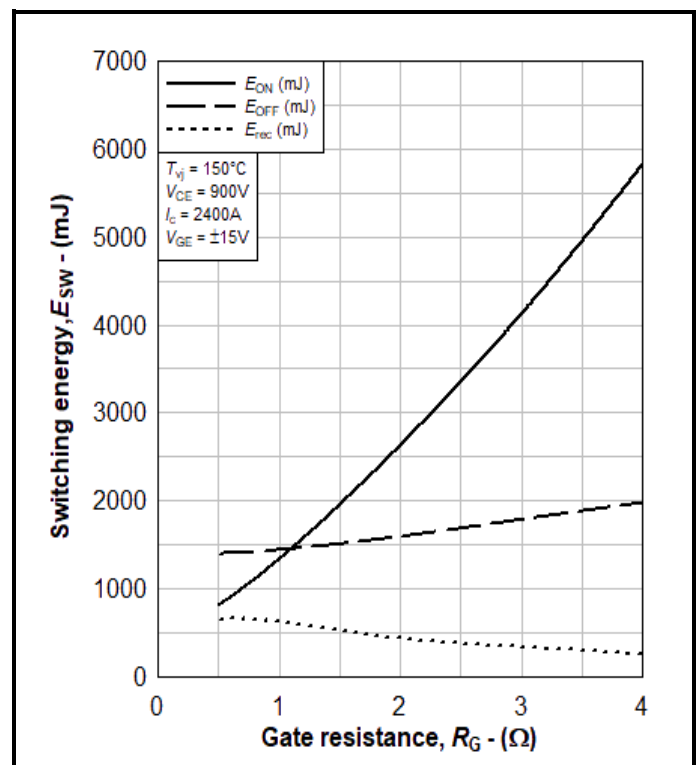


Fig. 6 Typical switching energy vs gate resistance

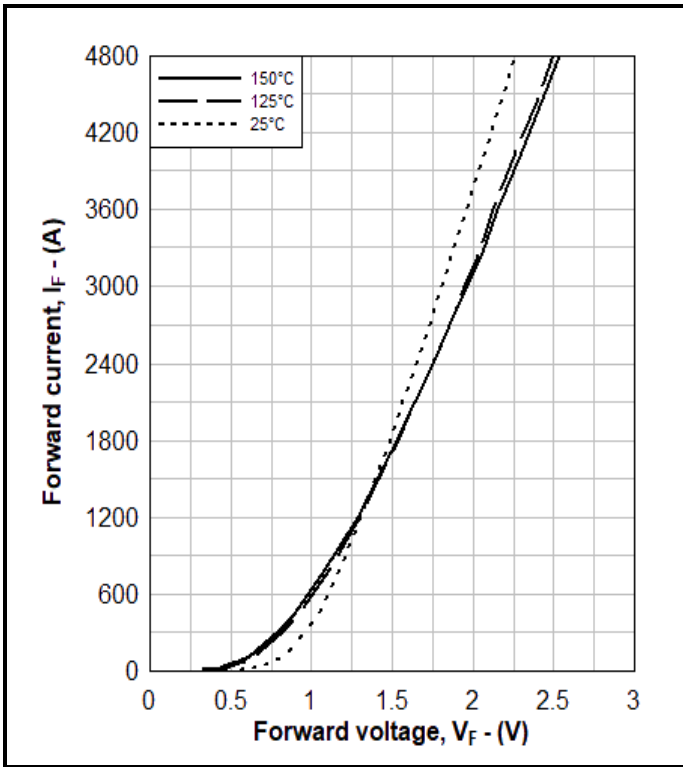


Fig. 7 Diode typical forward characteristics

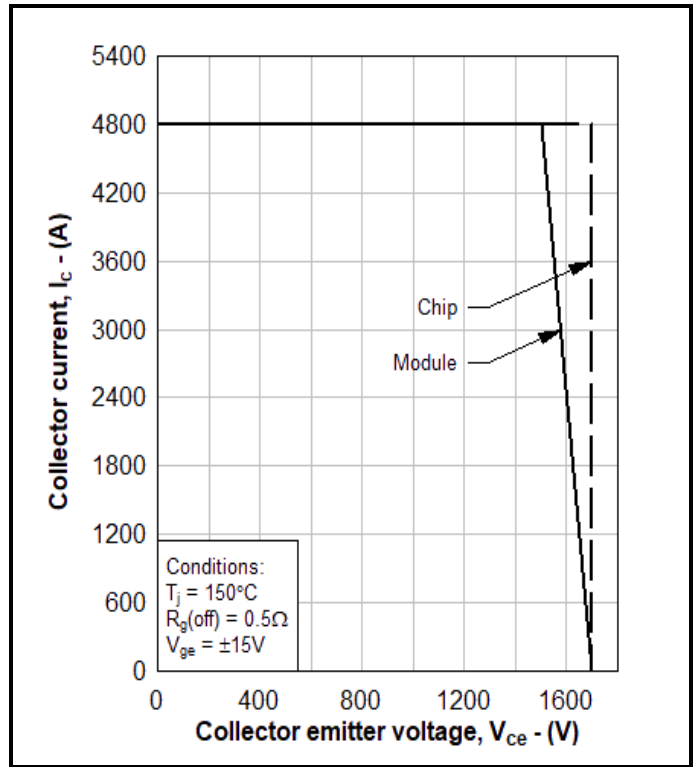


Fig. 8 Reverse bias safe operating area

TBD

Fig. 9 Diode reverse bias safe operating area

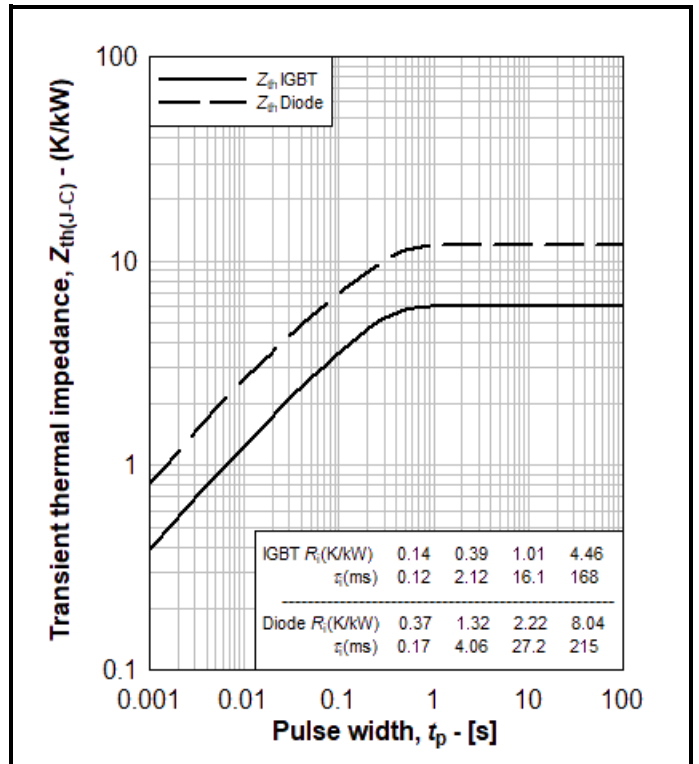
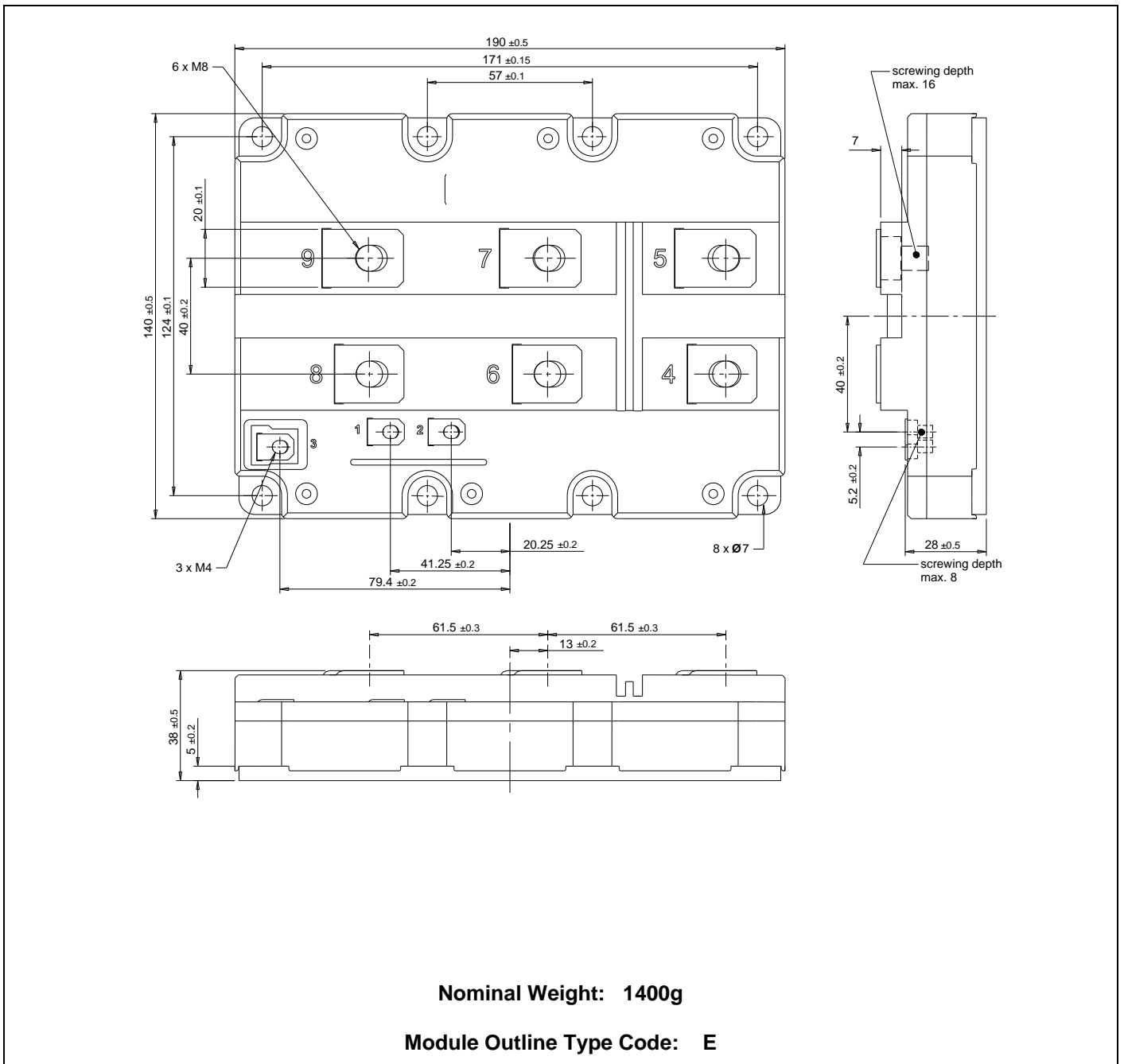


Fig. 10 Transient thermal impedance

## PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services.  
All dimensions in mm, unless stated otherwise.

**DO NOT SCALE.**



**Fig. 11 Module outline drawing**

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