

## FEATURES

- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- High Current Density Enhanced DMOS
- Isolated AISiC Base With AlN Substrates
- Low  $V_{CE(sat)}$  Device

## APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- Smart Grid
- Traction Drives

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM1500ESM33-PS500 is a single switch 3300V, n-channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10µs short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

## ORDERING INFORMATION

Order As:

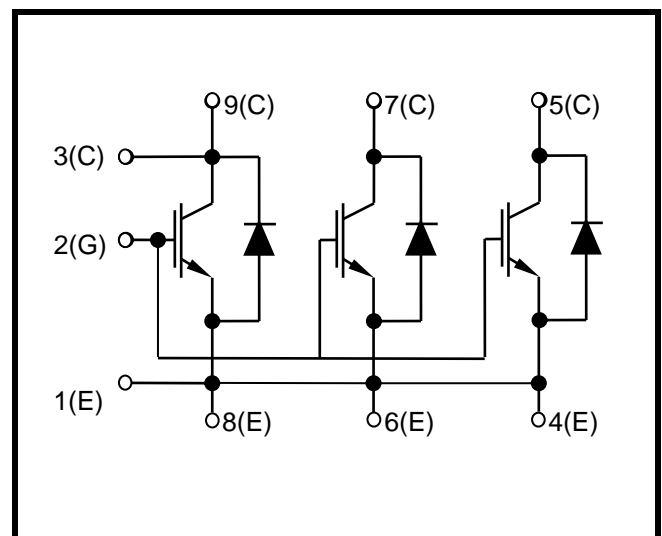
### **DIM1500ESM33-PS500**

Note: When ordering, please use the complete part number

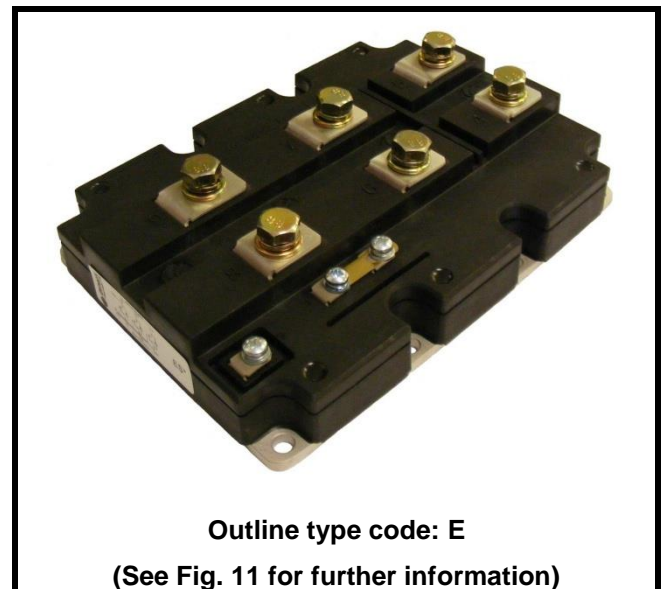
## KEY PARAMETERS

$V_{CES}$	<b>3300V</b>
$V_{CE(sat)}$ * (typ)	<b>2.1V</b>
$I_C$ (max)	<b>1500A</b>
$I_{C(PK)}$ (max)	<b>3000A</b>

\* Measured at the auxiliary terminals



**Fig. 1 Circuit configuration**



**Outline type code: E**  
**(See Fig. 11 for further information)**

**Fig. 2 Package**

**ABSOLUTE MAXIMUM RATINGS**

Stresses above those listed under ‘Absolute Maximum Ratings’ may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

**T<sub>case</sub> = 25°C unless stated otherwise**

Symbol	Parameter	Test Conditions	Max.	Units
V <sub>CES</sub>	Collector-emitter voltage	V <sub>GE</sub> = 0V	3300	V
V <sub>GES</sub>	Gate-emitter voltage		±20	V
I <sub>C</sub>	Continuous collector current	T <sub>case</sub> = 110°C	1500	A
I <sub>C(PK)</sub>	Peak collector current	1ms, T <sub>case</sub> = 140°C	3000	A
P <sub>max</sub>	Max. transistor power dissipation	T <sub>case</sub> = 25°C, T <sub>j</sub> = 150°C	15.6	kW
I <sup>2</sup> t	Diode I <sup>2</sup> t value	V <sub>R</sub> = 0, t <sub>p</sub> = 10ms, T <sub>j</sub> = 150°C	720	kA <sup>2</sup> s
V <sub>isol</sub>	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	6000	V
Q <sub>PD</sub>	Partial discharge – per module	IEC1287, V <sub>1</sub> = 3500V, V <sub>2</sub> = 2600V, 50Hz RMS	10	pC

**THERMAL AND MECHANICAL RATINGS**

Internal insulation material: AIN  
 Baseplate material: AISiC  
 Creepage distance: 33mm  
 Clearance: 20mm  
 CTI (Comparative Tracking Index): >600

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
R <sub>th(j-c)</sub>	Thermal resistance – transistor	Continuous dissipation - junction to case	-	-	8	°C/kW
R <sub>th(j-c)</sub>	Thermal resistance – diode	Continuous dissipation - junction to case	-	-	16	°C/kW
R <sub>th(c-h)</sub>	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	6	°C/kW
T <sub>j</sub>	Junction temperature	Transistor	-	-	150	°C
		Diode	-	-	150	°C
T <sub>stg</sub>	Storage temperature range	-	-40	-	150	°C
	Screw torque	Mounting – M6	-	-	5	Nm
		Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm

**ELECTRICAL CHARACTERISTICS**

**T<sub>case</sub> = 25°C unless stated otherwise.**

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
I <sub>CES</sub>	Collector cut-off current	V <sub>GE</sub> = 0V, V <sub>CE</sub> = V <sub>CES</sub>			1	mA
		V <sub>GE</sub> = 0V, V <sub>CE</sub> = V <sub>CES</sub> , T <sub>case</sub> = 125°C			90	mA
		V <sub>GE</sub> = 0V, V <sub>CE</sub> = V <sub>CES</sub> , T <sub>case</sub> = 150°C			150	mA
I <sub>GES</sub>	Gate leakage current	V <sub>GE</sub> = ± 20V, V <sub>CE</sub> = 0V			1	µA
V <sub>GE(TH)</sub>	Gate threshold voltage	I <sub>C</sub> = 120mA, V <sub>GE</sub> = V <sub>CE</sub>	5.0	5.7	6.5	V
V <sub>CE(sat)</sub>	Collector-emitter saturation voltage	V <sub>GE</sub> = 15V, I <sub>C</sub> = 1500A		2.1	2.5	V
		V <sub>GE</sub> = 15V, I <sub>C</sub> = 1500A, T <sub>j</sub> = 125°C		2.6	3.0	V
		V <sub>GE</sub> = 15V, I <sub>C</sub> = 1500A, T <sub>j</sub> = 150°C		2.9	3.2	V
I <sub>F</sub>	Diode forward current	DC		1500		A
I <sub>FM</sub>	Diode maximum forward current	t <sub>p</sub> = 1ms		3000		A
V <sub>F</sub>	Diode forward voltage	I <sub>F</sub> = 1500A		2.0	2.5	V
		I <sub>F</sub> = 1500A, T <sub>j</sub> = 125°C		2.2	2.6	V
		I <sub>F</sub> = 1500A, T <sub>j</sub> = 150°C		2.2	2.6	V
C <sub>ies</sub>	Input capacitance	V <sub>CE</sub> = 25V, V <sub>GE</sub> = 0V, f = 1MHz		260		nF
Q <sub>g</sub>	Gate charge	±15V Including external C <sub>ge</sub>		25		µC
C <sub>res</sub>	Reverse transfer capacitance	V <sub>CE</sub> = 25V, V <sub>GE</sub> = 0V, f = 1MHz		6		nF
L <sub>M</sub>	Module inductance			10		nH
R <sub>INT</sub>	Internal transistor resistance			110		µΩ
SC <sub>Data</sub>	Short circuit current, I <sub>SC</sub>	T <sub>j</sub> = 150°C, V <sub>CC</sub> = 2500V t <sub>p</sub> ≤ 10µs, V <sub>GE</sub> ≤ 15V V <sub>CE(max)</sub> = V <sub>CES</sub> - L* x dl/dt IEC 60747-9		6300		A

**Note:**

\* L is the circuit inductance + L<sub>M</sub>

**ELECTRICAL CHARACTERISTICS**

**T<sub>case</sub> = 25°C unless stated otherwise**

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units	
t <sub>d(off)</sub>	Turn-off delay time	I <sub>C</sub> = 1500A V <sub>GE</sub> = ±15V V <sub>CE</sub> = 1800V R <sub>g(ON)</sub> = 1.0Ω R <sub>g(OFF)</sub> = 1.5Ω C <sub>GE</sub> = 330nF L <sub>S</sub> ~ 150nH		2700		ns	
t <sub>f</sub>	Fall time				600		ns
E <sub>OFF</sub>	Turn-off energy loss				3600		mJ
t <sub>d(on)</sub>	Turn-on delay time				890		ns
t <sub>r</sub>	Rise time				340		ns
E <sub>ON</sub>	Turn-on energy loss				1400		mJ
Q <sub>rr</sub>	Diode reverse recovery charge	I <sub>F</sub> = 1500A V <sub>CE</sub> = 1800V dI <sub>F</sub> /dt = 5000A/μs		1100		μC	
I <sub>rr</sub>	Diode reverse recovery current				1340		A
E <sub>rec</sub>	Diode reverse recovery energy				1600		mJ

**T<sub>case</sub> = 125°C unless stated otherwise**

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units	
t <sub>d(off)</sub>	Turn-off delay time	I <sub>C</sub> = 1500A V <sub>GE</sub> = ±15V V <sub>CE</sub> = 1800V R <sub>g(ON)</sub> = 1.0Ω R <sub>g(OFF)</sub> = 1.5Ω C <sub>GE</sub> = 330nF L <sub>S</sub> ~ 150nH		2800		ns	
t <sub>f</sub>	Fall time				640		ns
E <sub>OFF</sub>	Turn-off energy loss				4100		mJ
t <sub>d(on)</sub>	Turn-on delay time				890		ns
t <sub>r</sub>	Rise time				350		ns
E <sub>ON</sub>	Turn-on energy loss				1910		mJ
Q <sub>rr</sub>	Diode reverse recovery charge	I <sub>F</sub> = 1500A V <sub>CE</sub> = 1800V dI <sub>F</sub> /dt = 5000A/μs		1750		μC	
I <sub>rr</sub>	Diode reverse recovery current				1520		A
E <sub>rec</sub>	Diode reverse recovery energy				2400		mJ

**T<sub>case</sub> = 150°C unless stated otherwise**

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units	
t <sub>d(off)</sub>	Turn-off delay time	I <sub>C</sub> = 1500A V <sub>GE</sub> = ±15V V <sub>CE</sub> = 1800V R <sub>g(ON)</sub> = 1.0Ω R <sub>g(OFF)</sub> = 1.5Ω C <sub>GE</sub> = 330nF L <sub>S</sub> ~ 150nH		2800		ns	
t <sub>f</sub>	Fall time				650		ns
E <sub>OFF</sub>	Turn-off energy loss				4300		mJ
t <sub>d(on)</sub>	Turn-on delay time				880		ns
t <sub>r</sub>	Rise time				360		ns
E <sub>ON</sub>	Turn-on energy loss				2100		mJ
Q <sub>rr</sub>	Diode reverse recovery charge	I <sub>F</sub> = 1500A V <sub>CE</sub> = 1800V dI <sub>F</sub> /dt = 5000A/μs		2100		μC	
I <sub>rr</sub>	Diode reverse recovery current				1590		A
E <sub>rec</sub>	Diode reverse recovery energy				2900		mJ

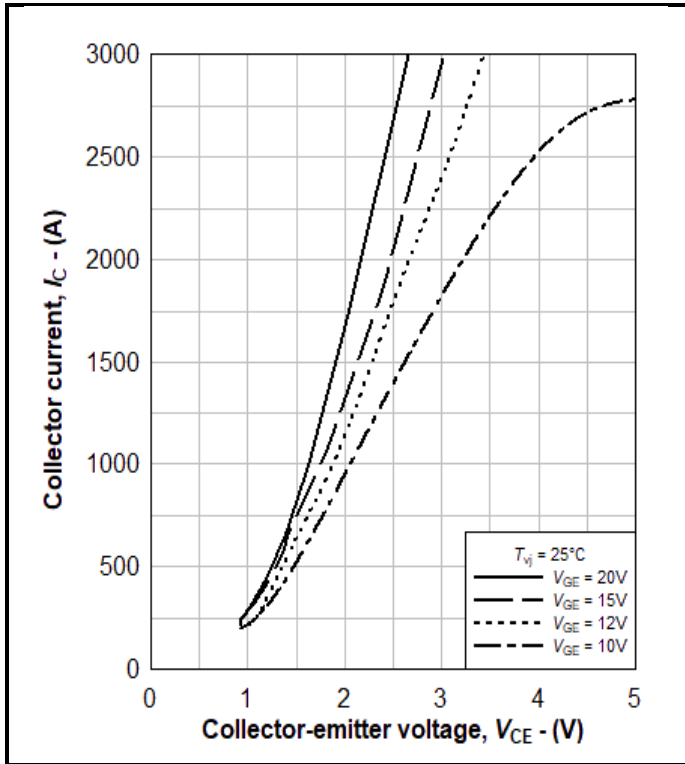


Fig. 3 Typical output characteristics

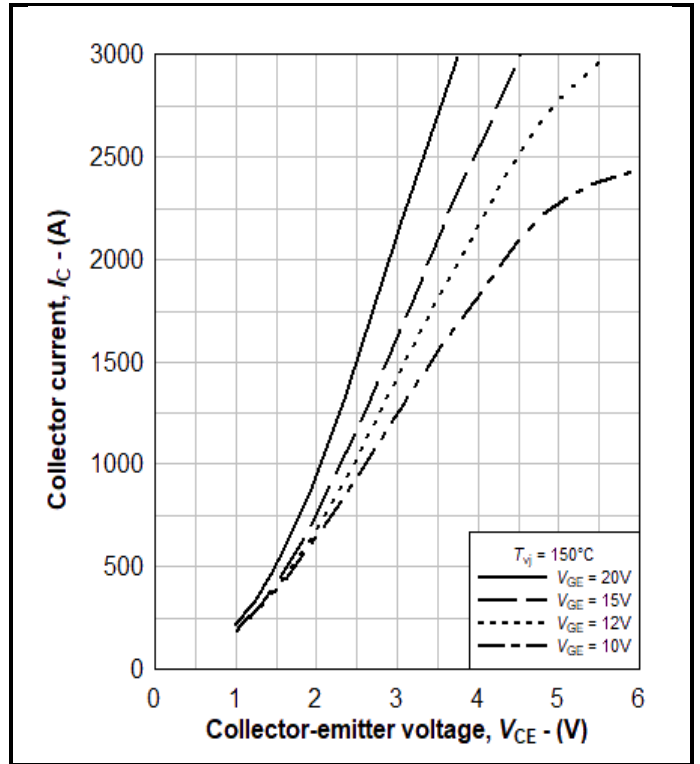


Fig. 4 Typical output characteristics

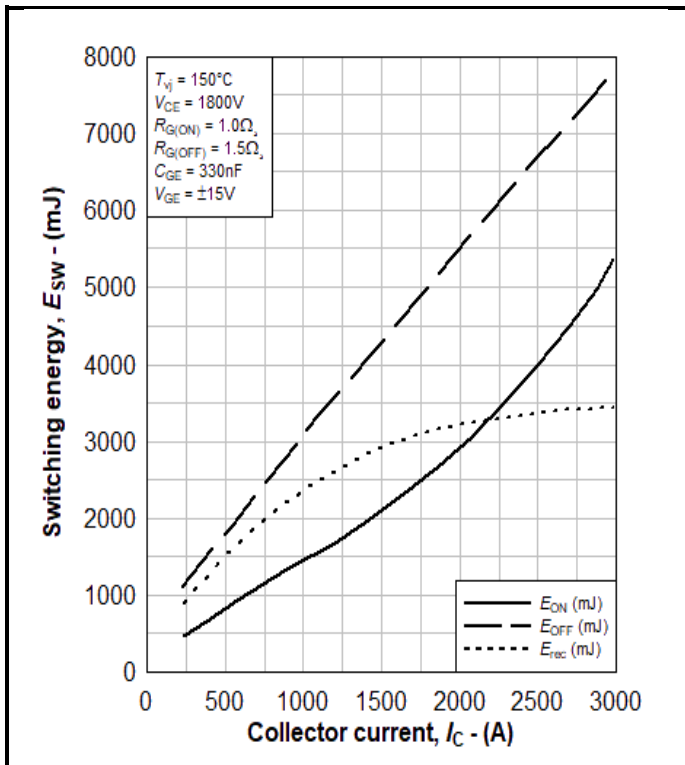


Fig. 5 Typical switching energy vs collector current

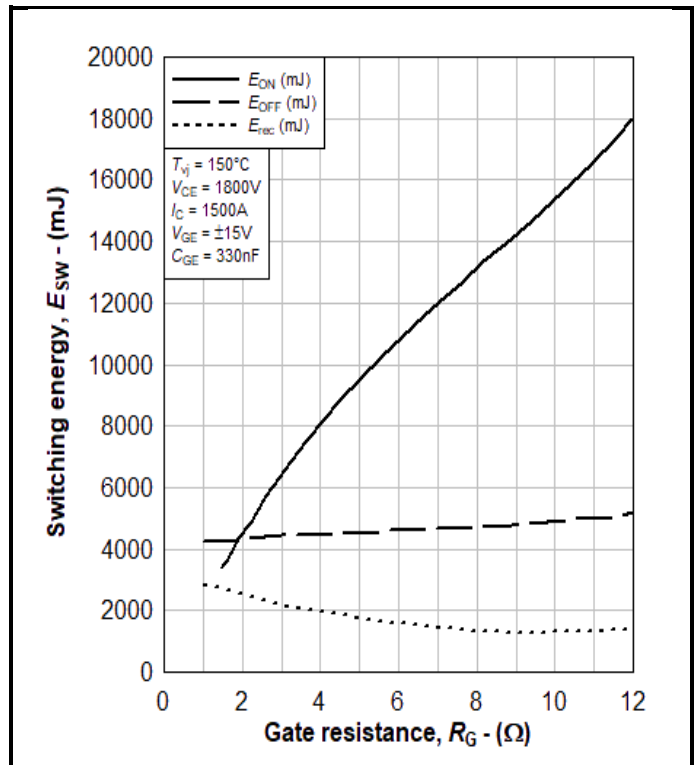


Fig. 6 Typical switching energy vs gate resistance

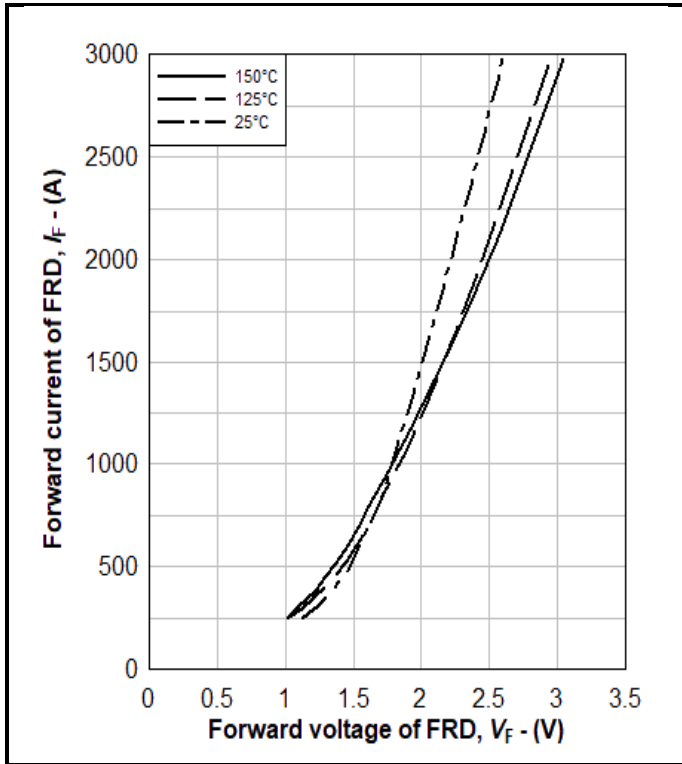


Fig. 7 Diode typical forward characteristics

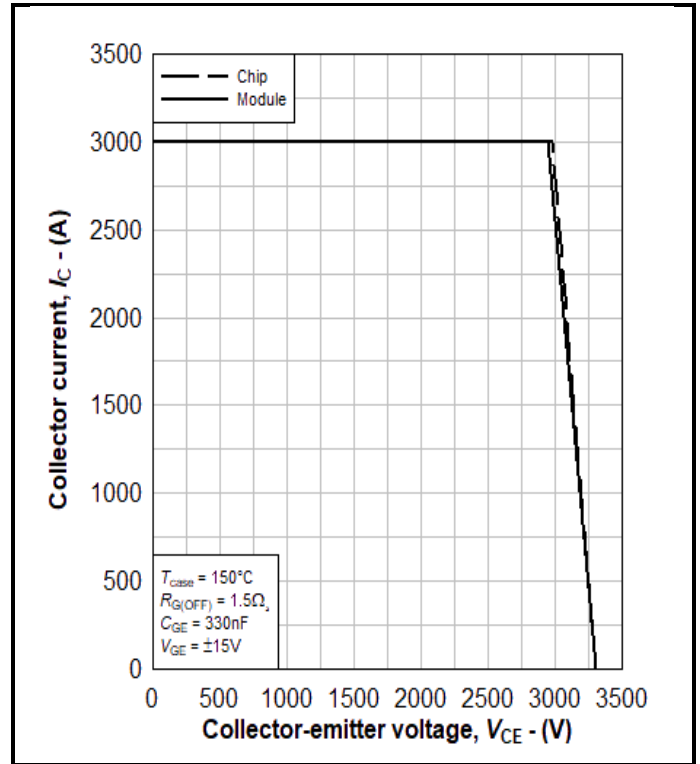


Fig. 8 Reverse bias safe operating area

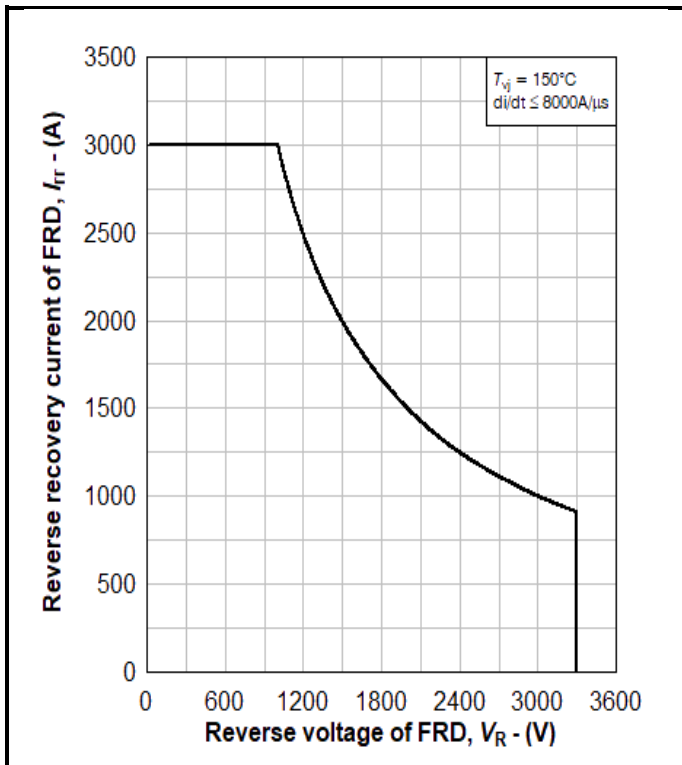


Fig. 9 Diode reverse bias safe operating area

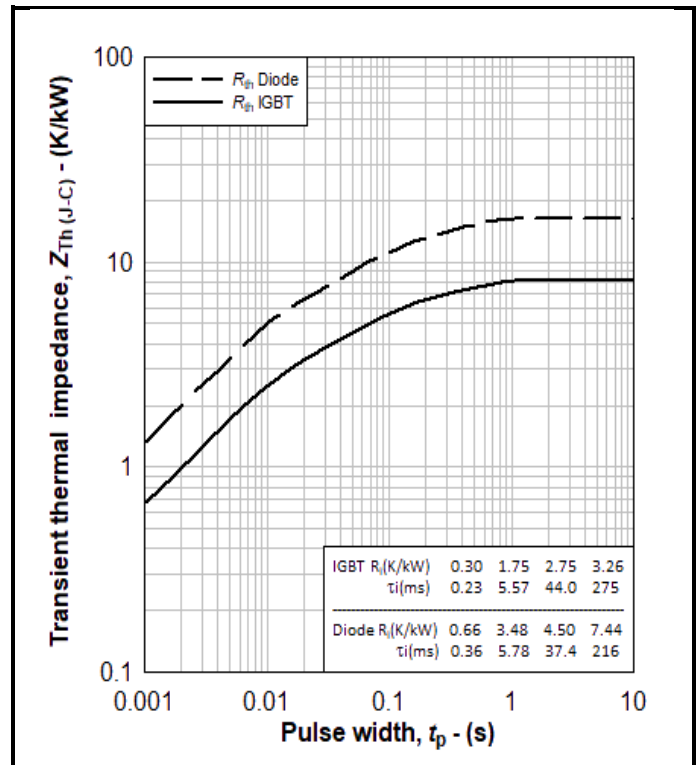
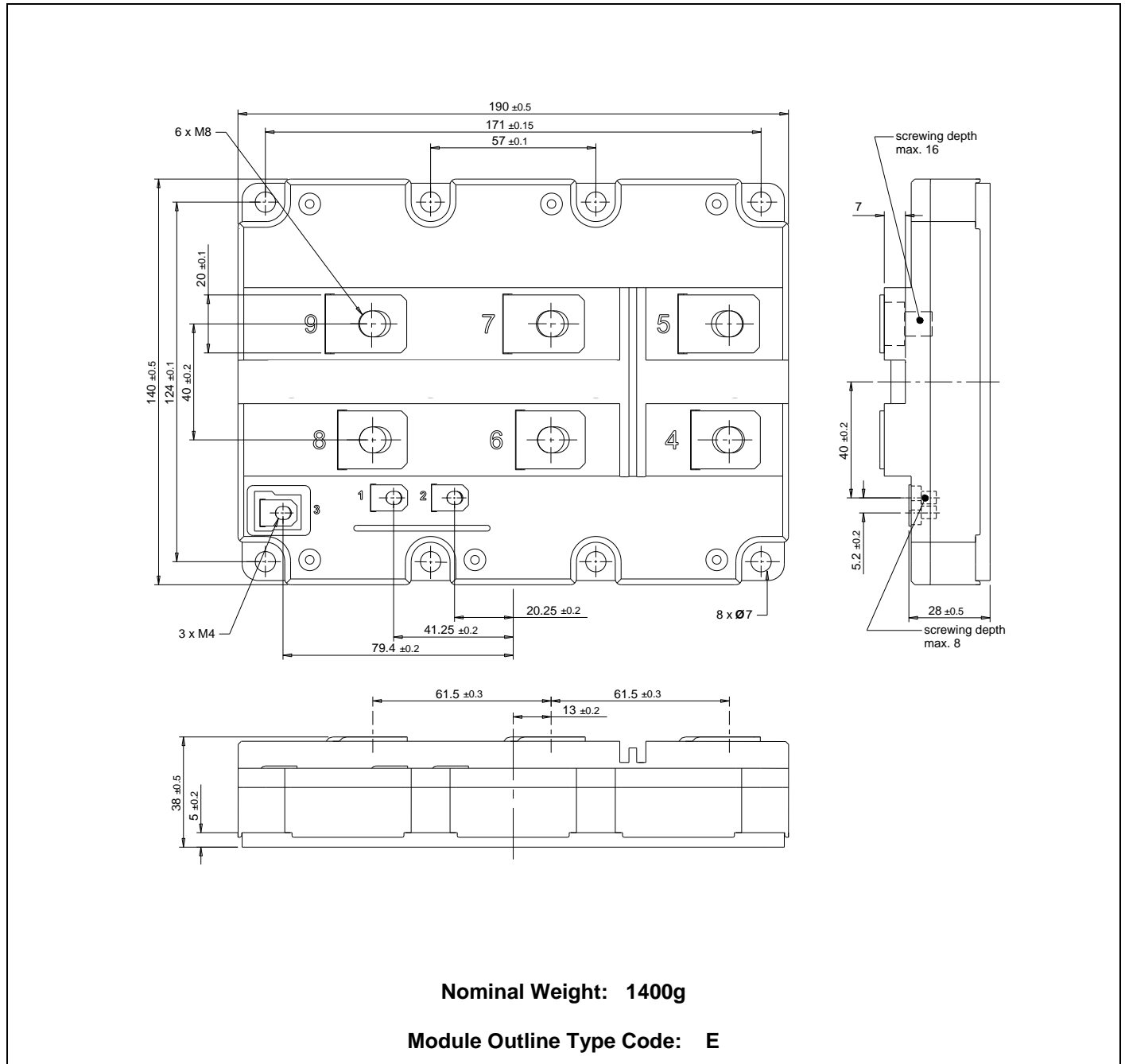


Fig. 10 Transient thermal impedance

**PACKAGE DETAILS**

For further package information, please visit our website or contact Customer Services.  
 All dimensions in mm, unless stated otherwise.  
**DO NOT SCALE.**



**Fig. 11 Module outline drawing**

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### HEADQUARTERS OPERATIONS

#### DYNEX SEMICONDUCTOR LTD

Doddington Road, Lincoln, Lincolnshire, LN6 3LF,  
United Kingdom

Tel: +44(0)1522 500500

Web: <http://www.dynexsemi.com>

### CUSTOMER SERVICE

#### DYNEX SEMICONDUCTOR LTD

Doddington Road, Lincoln, Lincolnshire, LN6 3LF,  
United Kingdom

Tel: +44(0)1522 502753 / 502901

Email: [powersolutions@dynexsemi.com](mailto:powersolutions@dynexsemi.com)