

# **DRD870G40**

## **Rectifier Diode**

DS5986-1 January 2011 (LN28007)

## **FEATURES**

- Double Side Cooling
- High Surge Capability

## **KEY PARAMETERS**

$V_{RRM}$	4000V
I <sub>F(AV)</sub>	870A
I <sub>FSM</sub>	15000A

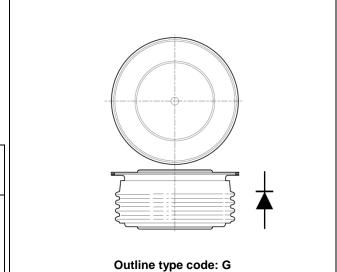


Fig. 1 Package outline

(See Package Details for further information)

### **VOLTAGE RATINGS**

Part and Ordering Number	Repetitive Peak Voltages V <sub>RRM</sub> V	Conditions
DRD870G40 DRD870G38 DRD870G36 DRD870G34	4000 3800 3600 3400	$V_{RSM} = V_{RRM} + 100V$

## **ORDERING INFORMATION**

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

DRD870G36 for a 3600V device

## **CURRENT RATINGS**

 $T_{\text{case}} = 75^{\circ}\text{C}$  unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units		
Double Si	Double Side Cooled					
$I_{F(AV)}$	Mean forward current	Half wave resistive load	1121	А		
I <sub>F(RMS)</sub>	RMS value	-	1761	Α		
I <sub>F</sub>	Continuous (direct) on-state current	-	1608	А		
Single Side Cooled (Anode side)						
I <sub>F(AV)</sub>	Mean forward current	Half wave resistive load	734	Α		
I <sub>F(RMS)</sub>	RMS value	-	1154	Α		
l <sub>F</sub>	Continuous (direct) on-state current	-	989	Α		

## $T_{\text{case}}$ = 100°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units			
Double Si	Double Side Cooled						
$I_{F(AV)}$	Mean forward current	Half wave resistive load	870	А			
I <sub>F(RMS)</sub>	RMS value	-	1366	Α			
I <sub>F</sub>	Continuous (direct) on-state current	-	1280	Α			
Single Side Cooled (Anode side)							
I <sub>F(AV)</sub>	Mean forward current	Half wave resistive load	550	Α			
I <sub>F(RMS)</sub>	RMS value	-	863	Α			
I <sub>F</sub>	Continuous (direct) on-state current	-	740	А			

## **SURGE RATINGS**

Symbol	Parameter	Test Conditions		Units
I <sub>FSM</sub>	Surge (non-repetitive) on-state current	10ms half sine, T <sub>case</sub> = 150°C	12	kA
l <sup>2</sup> t	I <sup>2</sup> t for fusing	$V_R = 50\% V_{RRM} - \frac{1}{4}$ sine	0.72	MA <sup>2</sup> s
I <sub>FSM</sub>	Surge (non-repetitive) on-state current	10ms half sine, T <sub>case</sub> = 150°C	15	kA
l <sup>2</sup> t	I <sup>2</sup> t for fusing	$V_R = 0$	1.125	MA <sup>2</sup> s

## THERMAL AND MECHANICAL RATINGS

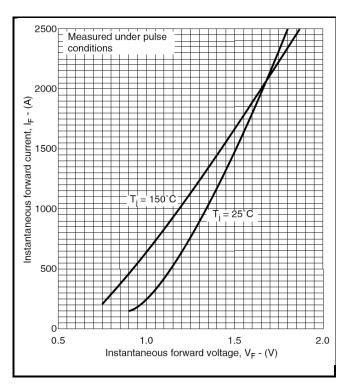
Symbol	Parameter	Test Conditions		Min.	Max.	Units
R <sub>th(j-c)</sub>	Thermal resistance – junction to case	Double side cooled	DC	-	0.032	°C/W
		Single side cooled	Anode DC	-	0.064	°C/W
			Cathode DC	-	0.064	°C/W
$R_{th(c-h)}$	Thermal resistance – case to heatsink	Clamping force 12kN	Double side		0.008	°C/W
		(with mounting compound)	Single side	-	0.016	°C/W
T <sub>vj</sub>	Virtual junction temperature	On-state (conducting)		-	160	°C
		Reverse (blocking)			150	°C
T <sub>stg</sub>	Storage temperature range			-55	175	°C
F <sub>m</sub>	Clamping force			11.5	13.5	kN

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## **CHARACTERISTICS**

Symbol	Parameter	Test Conditions	Min.	Max.	Units
V <sub>FM</sub>	Forward voltage	At 1800A peak, T <sub>case</sub> = 25°C	-	1.6	V
I <sub>RM</sub>	Peak reverse current	At V <sub>RRM</sub> , T <sub>case</sub> = 150°C	-	50	mA
Qs	Total stored charge	$I_F = 1000A$ , $dI_{RR}/dt = 3A/\mu s$	-	2000	μC
Irr	Peak reverse recovery current	$T_{case} = 150$ °C, $V_R = 100$ V	-	80	А
V <sub>TO</sub>	Threshold voltage	At T <sub>vj</sub> = 150°C	-	0.75	V
r <sub>T</sub>	Slope resistance	At T <sub>vj</sub> = 150°C	-	0.44	mΩ

## **CURVES**



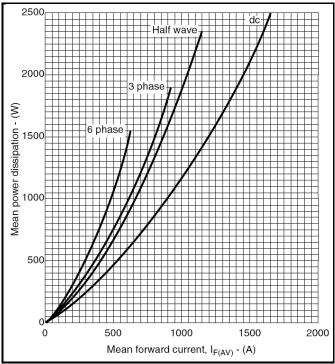


Fig.2 Maximum (limit) on-state characteristics

Fig.3 Dissipation curves

 $V_{\text{TM}}$  EQUATION

 $V_{TM} = A + Bln (I_T) + C.I_T + D.\sqrt{I_T}$ 

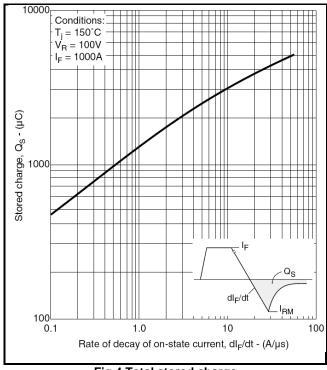
Where A = 0.616461

B = -0.01452

C = 0.000349

D = 0.009952

these values are valid for  $T_i = 150$ °C for  $I_F 500$ A to 2500A



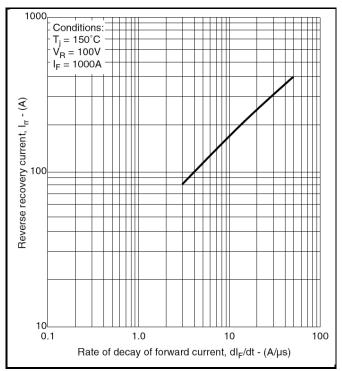
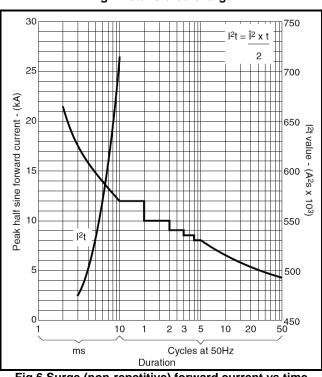
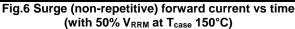


Fig.4 Total stored charge







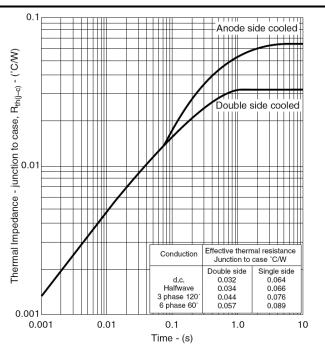
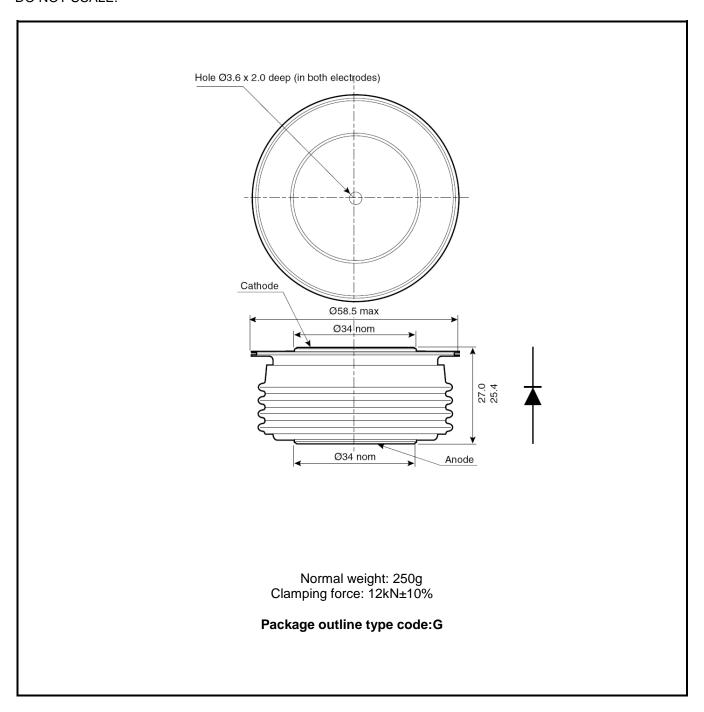


Fig.7 Maximum (limit) transient thermal impedancejunction to case

## **PACKAGE DETAILS**

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



### Note:

Some packages may be supplied with gate and or tags.

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