



# DIM800XSM45-TS001

Replaces DS6090-9

Single Switch IGBT Module

DS6090-10 August 2023 (LN42742)

# FEATURES

- 10.2kV Isolation
- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- High Current Density Enhanced DMOS SPT
- Isolated AISiC Base with AIN Substrates
- Lead Free Construction

# **APPLICATIONS**

- High Reliability Inverters
- Motor Controllers
- Traction Drives
- Choppers

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM800XSM45-TS001 is a single switch 4500V, n-channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10µs short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

# **ORDERING INFORMATION**

Order As:

# DIM800XSM45-TS001

Note: When ordering, please use the complete part number

# **KEY PARAMETERS**

VCES		4500V
V <sub>CE(sat)</sub>	* (typ)	2.5V
lc	(max)	800A
I <sub>C(РК)</sub>	(max)	1600A

\* Measured at the auxiliary terminals

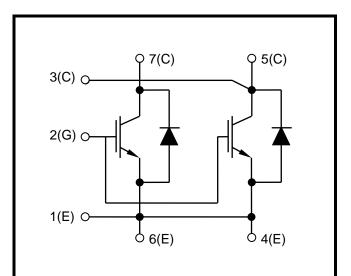


Fig. 1 Circuit configuration



# **ABSOLUTE MAXIMUM RATINGS**

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

#### T<sub>case</sub> = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
Vces	Collector-emitter voltage	V <sub>GE</sub> = 0V	4500	V
V <sub>GES</sub>	Gate-emitter voltage		±20	V
lc	Continuous collector current	T <sub>case</sub> = 90°C	800	А
I <sub>C(PK)</sub>	Peak collector current	1ms, $T_{case} = 115^{\circ}C$	1600	А
P <sub>max</sub>	Max. transistor power dissipation	$T_{case} = 25^{\circ}C, T_j = 125^{\circ}C$	8.3	kW
l²t	Diode I <sup>2</sup> t value	$V_R = 0, t_p = 10ms, T_j = 125^{\circ}C$	205	kA <sup>2</sup> s
Visol	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	10.2	kV
Q <sub>PD</sub>	Partial discharge – per module	IEC1287, $V_1 = 6900V$ , $V_2 = 5100V$ , 50Hz RMS	10	рС

# THERMAL AND MECHANICAL RATINGS

Internal insulation material:	AIN
Baseplate material:	AISiC
Creepage distance:	56mm
Clearance:	26mm
CTI (Comparative Tracking Index):	>600

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
R <sub>th(j-c)</sub>	Thermal resistance – transistor	Continuous dissipation - junction to case	-	-	12	°C/kW
R <sub>th(j-c)</sub>	Thermal resistance – diode	Continuous dissipation - junction to case	-	-	24	°C/kW
Rth(c-h)	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	8	°C/kW
Tj	Junction temperature	Transistor	-	-	125	°C
		Diode	-	-	125	°C
T <sub>stg</sub>	Storage temperature range	-	-40	-	125	°C
		Mounting – M6	-	-	5	Nm
	Screw torque	Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm

# **ELECTRICAL CHARACTERISTICS**

## T<sub>case</sub> = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Тур	Мах	Units
	0	$V_{GE} = 0V, V_{CE} = V_{CES}$			1	mA
ICES Collector cut-off current		V <sub>GE</sub> = 0V, V <sub>CE</sub> = V <sub>CES</sub> , T <sub>case</sub> = 125°C			40	mA
IGES	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$			1	μA
V <sub>GE(TH)</sub>	Gate threshold voltage	$I_{C} = 80 \text{mA}, V_{GE} = V_{CE}$		5.8		V
N	Collector-emitter	V <sub>GE</sub> = 15V, I <sub>C</sub> = 800A		2.5		V
V <sub>CE(sat)</sub>	saturation voltage	$V_{GE} = 15V, I_C = 800A, T_j = 125^{\circ}C$		3.2		V
lF	Diode forward current	DC		800		А
Іғм	Diode maximum forward current	t <sub>p</sub> = 1ms		1600		А
		IF = 800A		2.9		V
Vf	Diode forward voltage	I <sub>F</sub> = 800A, T <sub>j</sub> = 125°C		3.1		V
Cies	Input capacitance	V <sub>CE</sub> = 25V, V <sub>GE</sub> = 0V, f = 1MHz		100		nF
Qg	Gate charge	±15V		15		μC
Cres	Reverse transfer capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		8		nF
Lм	Module inductance			15		nH
RINT	Internal transistor resistance			135		μΩ
SC <sub>Data</sub>	Short circuit current, I <sub>SC</sub>	$\begin{split} T_{j} &= 125^{\circ}C, \ V_{CC} &= 3400V \\ t_{p} &\leq 10 \mu s, \ V_{GE} &\leq 15V \\ V_{CE \ (max)} &= V_{CES} - L^{*} \ x \ dI/dt \\ IEC \ 60747-9 \end{split}$		3200		A

#### Note:

 $^{\ast}\,$  L is the circuit inductance +  $L_{M}$ 

# **ELECTRICAL CHARACTERISTICS**

#### T<sub>case</sub> = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t <sub>d(off)</sub>	Turn-off delay time			2800		ns
t <sub>f</sub>	Fall time	$I_{C} = 800A$ $V_{GE} = \pm 15V$		530		ns
Eoff	Turn-off energy loss	V <sub>CE</sub> = 2800V		2500		mJ
t <sub>d(on)</sub>	Turn-on delay time	$R_{G(ON)} = 3.9\Omega$ $R_{G(OFF)} = 3.9\Omega$		1000		ns
tr	Rise time	C <sub>ge</sub> = 150nF		160		ns
Eon	Turn-on energy loss	Ls ~ 165nH		3500		mJ
Qrr	Diode reverse recovery charge	IF = 800A		880		μC
Irr	Diode reverse recovery current	$V_{CE} = 2800V$		680		А
E <sub>rec</sub>	Diode reverse recovery energy	dl⊧/dt = 1900/µs		1100		mJ

## T<sub>case</sub> = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t <sub>d(off)</sub>	Turn-off delay time			2900		ns
t <sub>f</sub>	Fall time	Ic = 800A V <sub>GE</sub> = ±15V		490		ns
E <sub>OFF</sub>	Turn-off energy loss	V <sub>CE</sub> = 2800V		2600		mJ
t <sub>d(on)</sub>	Turn-on delay time	$R_{G(ON)} = 3.9\Omega$ $R_{G(OFF)} = 3.9\Omega$		1000		ns
tr	Rise time	$C_{ge} = 150 nF$		170		ns
Eon	Turn-on energy loss	L <sub>s</sub> ~ 165nH		4800		mJ
Qrr	Diode reverse recovery charge	IF = 800A		1450		μC
Irr	Diode reverse recovery current	$V_{CE} = 2800V$		750		А
Erec	Diode reverse recovery energy	dl <sub>F</sub> /dt = 2000A/µs		2000		mJ

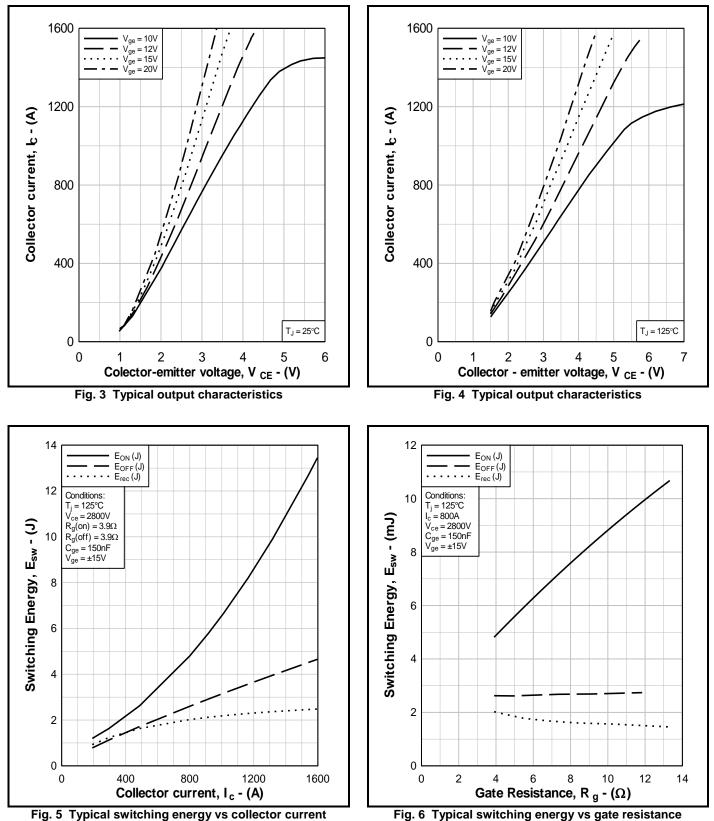
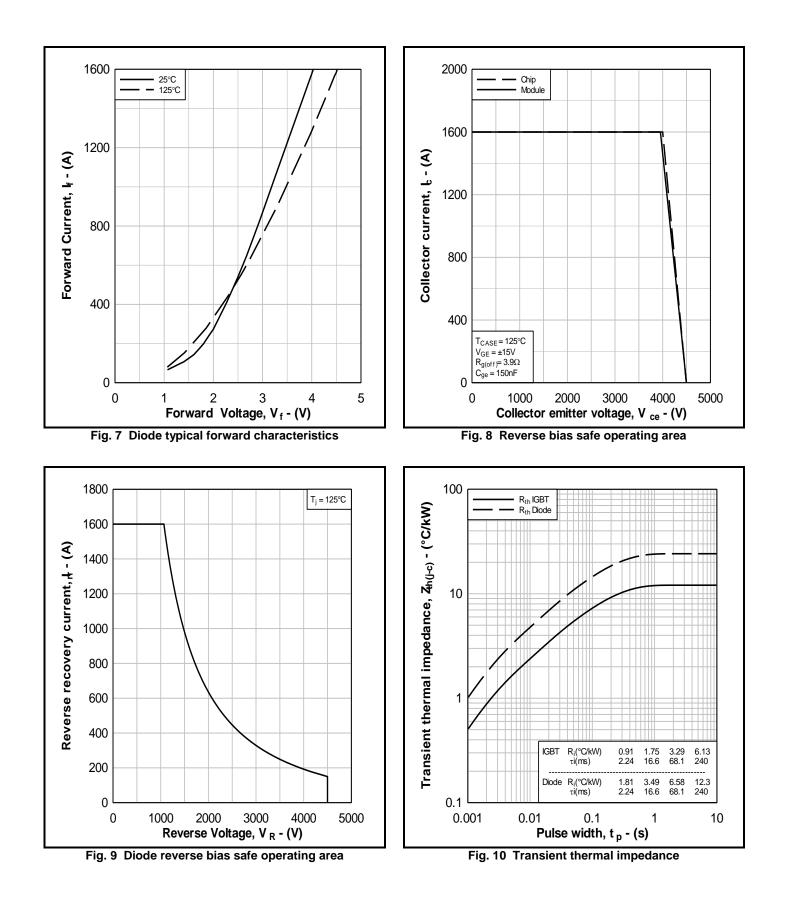
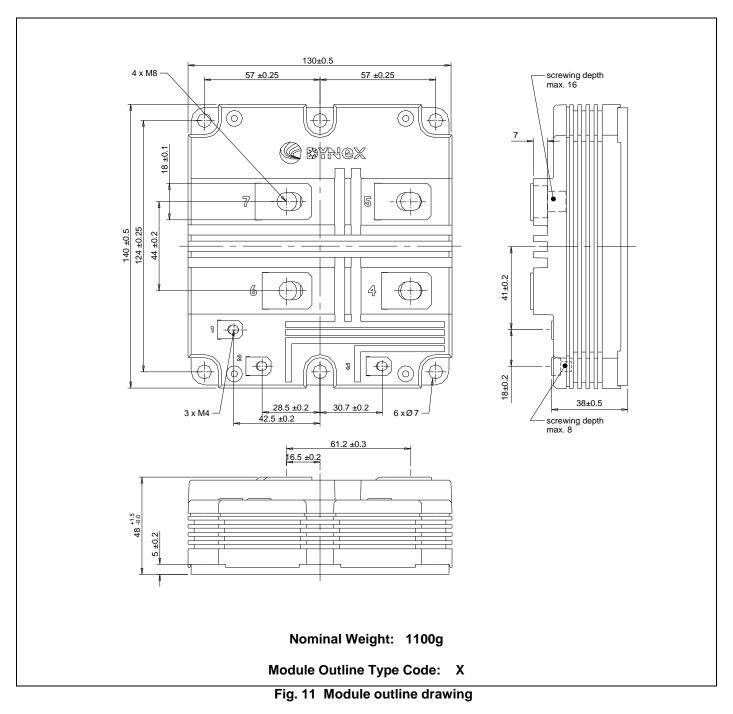


Fig. 6 Typical switching energy vs gate resistance



## PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise. **DO NOT SCALE.** 



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#### DYNEX SEMICONDUCTOR LTD

Doddington Road, Lincoln, Lincolnshire, LN6 3LF, United Kingdom Fax: +44(0)1522 500550 Tel: +44(0)1522 500500 Web: <u>http://www.dynexsemi.com</u>

# CUSTOMER SERVICE

#### DYNEX SEMICONDUCTOR LTD

Doddington Road, Lincoln, Lincolnshire, LN6 3LF, United Kingdom

Tel: +44(0)1522 502753 / 502901 Email: <u>powersolutions@dynexsemi.com</u>

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