

TRENCH Gen7 STMOS

# DIM800M1HS17-PG500

# **Half Bridge IGBT Module**

DS6429-1 November 2023 (LN42926)

### **FEATURES**

- Ultra-fine Trench Gate IGBT
- Cu Base with Enhanced Si<sub>3</sub>N<sub>4</sub> Substrates
- High Thermal Cycling Capability

#### **APPLICATIONS**

- Wind Turbines
- Power Charging Equipment
- Smart Grid
- High Reliability Inverters

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM800M1HS17-PG500 is a half bridge 1700V, trench gate, insulated gate bipolar transistor (IGBT) module with enhanced field stop. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 8µs short circuit withstand.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

### **ORDERING INFORMATION**

Order As:

### DIM800M1HS17-PG500

Note: When ordering, please use the complete part number

#### **KEY PARAMETERS**

V <sub>CES</sub>		1700V
V <sub>CE(sat)</sub>	* (typ)	1.65V
Ic	(max)	A008
I <sub>C(PK)</sub>	(max)	1600A

<sup>\*</sup> Measured at the auxiliary terminals

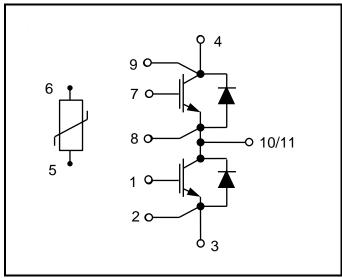


Fig. 1 Circuit configuration



Fig. 2 Package

### **ABSOLUTE MAXIMUM RATINGS**

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T<sub>case</sub> = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions		Units
Vces	Collector-emitter voltage	V <sub>GE</sub> = 0V, T <sub>C</sub> = 25°C	1700	V
V <sub>GES</sub>	Gate-emitter voltage	T <sub>C</sub> = 25°C	±20	V
lc	Continuous collector current	Tc = 80°C, T <sub>vj</sub> = 175°C	800	Α
I <sub>C(PK)</sub>	Peak collector current	t <sub>P</sub> = 1ms	1600	Α
P <sub>max</sub>	Max. transistor power dissipation	Tc = 25°C, T <sub>vj</sub> = 175°C	4	kW
l²t	Diode I <sup>2</sup> t value	$V_R = 0$ , $t_p = 10$ ms, $T_{vj} = 175$ °C	37	kA <sup>2</sup> s
Visol	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	3400	V

#### THERMAL AND MECHANICAL RATINGS

Internal insulation material: Si<sub>3</sub>N<sub>4</sub>

Baseplate material: Cu

Creepage distance – Terminal to heatsink: 14.5mm

Creepage distance – Terminal to terminal: 13.0mm

Clearance – Terminal to heatsink: 12.5mm

Clearance – Terminal to terminal: 10mm

CTI (Comparative Tracking Index): >200

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
R <sub>th(j-c)</sub>	Thermal resistance – IGBT	Continuous dissipation –	-	-	37	°C/kW
R <sub>th(j-c)</sub>	Thermal resistance – diode	junction to case	-	-	63	°C/kW
R <sub>th(c-h)</sub>	Thermal resistance – case to heatsink (IGBT)	Mounting torque 5Nm	-	28	-	°C/kW
R <sub>th(c-h)</sub>	Thermal resistance – case to heatsink (Diode)	(with mounting grease 1W/m°C)	-	38	-	°C/kW
-	Junction temperature – under switching conditions	IGBT	-40	-	175	°C
Tj		Diode	-40	-	175	°C
T <sub>stg</sub>	Storage temperature range	-	-40	-	125	°C
	Screw torque	Mounting – M5	3	-	6	Nm
		Electrical connections – M6	3	-	6	Nm

# **ELECTRICAL CHARACTERISTICS**

 $T_{case}$  = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
	Collector cut-off current	VGE = 0V, VCE = VCES			1	mA
I <sub>CES</sub>		$V_{GE} = 0V$ , $V_{CE} = V_{CES}$ , $T_C = 150$ °C			20	mA
		$V_{GE} = 0V$ , $V_{CE} = V_{CES}$ , $T_C = 175$ °C			30	mA
I <sub>GES</sub>	Gate leakage current	V <sub>GE</sub> = ± 20V, V <sub>CE</sub> = 0V			0.5	μA
V <sub>GE(TH)</sub>	Gate threshold voltage	Ic = 15mA, V <sub>GE</sub> = V <sub>CE</sub>	5.40	6.00	6.60	V
		V <sub>GE</sub> = 15V, I <sub>C</sub> = 800A		1.65	2.05	V
$V_{\text{CE(sat)}}$	Collector-emitter saturation voltage	V <sub>GE</sub> = 15V, I <sub>C</sub> = 800A, T <sub>j</sub> = 150°C		2.05		V
	Vollago	V <sub>GE</sub> = 15V, I <sub>C</sub> = 800A, T <sub>j</sub> = 175°C		2.10		V
l <sub>F</sub>	Diode forward current	DC		800		Α
I <sub>FM</sub>	Diode maximum forward current	$t_p = 1 ms$		1600		Α
	Diode forward voltage	I <sub>F</sub> = 800A		1.85	2.25	V
$V_{F}$		I <sub>F</sub> = 800A, T <sub>j</sub> = 150°C		2.05		V
		I <sub>F</sub> = 800A, T <sub>j</sub> = 175°C		2.05		V
Cies	Input capacitance	V <sub>CE</sub> = 25V, V <sub>GE</sub> = 0V, f = 100kHz		185		nF
Qg	Gate charge	±15V		8.6		μC
Cres	Reverse transfer capacitance	V <sub>CE</sub> = 25V, V <sub>GE</sub> = 0V, f = 100kHz		0.4		nF
L <sub>M</sub>	Module inductance			28		nΗ
Rcc'+EE'	Module lead resistance, Terminal - chip			1		mΩ
R <sub>Gint</sub>	Internal gate resistor			0.6		Ω
SC <sub>Data</sub>	Short circuit current, Isc	$T_{j} = 175^{\circ}C$ , $V_{CC} = 1000V$ $t_{p} \le 8\mu s$ , $V_{GE} \le 15V$ $V_{CE (max)} = V_{CES} - L^{*} x di/dt$ IEC 60747-9		3000		А

### Note:

# **NTC-Thermistor Data**

Symbol	Parameter	Test Conditions		Тур	Max	Units
R <sub>25</sub>	Rated resistance	Tc = 25°C		5		kΩ
Δ <i>R</i> /R	Deviation of R100	$T_{\rm C} = 100^{\circ}{\rm C},  {\rm R}_{100} = 493\Omega$	-5		5	%
P <sub>25</sub>	Power dissipation	Tc = 25°C			20	m/W
<b>B</b> 25/50		$R_2 = R_{25} exp [B_{25/50}(1/T2 - 1/(298.15K))]$		3375		K
B <sub>25/80</sub>	B-value	$R_2 = R_{25} exp [B_{25/80}(1/T2 - 1/(298.15K))]$		3411		K
B <sub>25/100</sub>		$R_2 = R_{25} exp [B_{25/100}(1/T2 - 1/(298.15K))]$		3433		K

 $<sup>^{\</sup>star}\,$  L is the circuit inductance + L<sub>M</sub>

# **ELECTRICAL CHARACTERISTICS**

T<sub>case</sub> = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions		Min	Тур.	Max	Units
t <sub>d(off)</sub>	Turn-off delay time		<i>dv/dt</i> = 3500V/μs		1320		ns
t <sub>f</sub>	Fall time	$ \begin{array}{c} I_{C} = 800A \\ V_{CE} = 900V \\ V_{GE} = \pm 15V \\ R_{G(OFF)} = 6.8\Omega \\ R_{G(ON)} = 0.5\Omega \\ L_{S} = 35 nH \end{array} $			195		ns
Eoff	Turn-off energy loss				217		mJ
t <sub>d(on)</sub>	Turn-on delay time		<i>di/dt</i> = 6400A/μs		270		ns
tr	Rise time				85		ns
Eon	Turn-on energy loss				98		mJ
Qrr	Diode reverse recovery charge	IF =800A			150		μC
Irr	Diode reverse recovery current	V <sub>CE</sub> = 900V		670		Α	
Erec	Diode reverse recovery energy	di/dt = 6	6400A/µs		109		mJ

# T<sub>case</sub> = 150°C unless stated otherwise

Symbol	Parameter	Test Conditions		Min	Тур.	Max	Units
t <sub>d(off)</sub>	Turn-off delay time	$\begin{array}{c} I_{C} = 800A \\ V_{CE} = 900V \\ V_{GE} = \pm 15V \\ R_{G(OFF)} = 6.8\Omega \\ R_{G(ON)} = 0.5\Omega \\ L_{S} = 35 nH \end{array}$	<i>dv/dt</i> = 3500V/μs		1520		ns
t <sub>f</sub>	Fall time				385		ns
Eoff	Turn-off energy loss				288		mJ
t <sub>d(on)</sub>	Turn-on delay time		di/dt = 6400A/µs		294		ns
t <sub>r</sub>	Rise time				100		ns
Eon	Turn-on energy loss				214		mJ
Qrr	Diode reverse recovery charge	I <sub>F</sub> =800A			270		μC
Irr	Diode reverse recovery current	V <sub>CE</sub> = 900V		588		Α	
Erec	Diode reverse recovery energy	<i>di/dt</i> = 6	6400A/µs		174		mJ

# T<sub>case</sub> = 175°C unless stated otherwise

Symbol	Parameter	Test Conditions		Min	Тур.	Max	Units
t <sub>d(off)</sub>	Turn-off delay time		<i>dv/dt</i> = 3500V/μs		1565		ns
t <sub>f</sub>	Fall time	$\begin{array}{c} I_{C} = 800A \\ V_{CE} = 900V \\ V_{GE} = \pm 15V \\ R_{G(OFF)} = 6.8\Omega \\ R_{G(ON)} = 0.5\Omega \\ L_{S} = 35 nH \end{array}$			410		ns
Eoff	Turn-off energy loss				298		mJ
t <sub>d(on)</sub>	Turn-on delay time		<i>di/dt</i> = 6400A/μs		300		ns
tr	Rise time				110		ns
Eon	Turn-on energy loss				246		mJ
Qrr	Diode reverse recovery charge	I <sub>F</sub> =800A			305		μC
Irr	Diode reverse recovery current	V <sub>CE</sub> = 900V		560		Α	
Erec	Diode reverse recovery energy	$di/dt = 6400$ A/ $\mu$ s			194		mJ

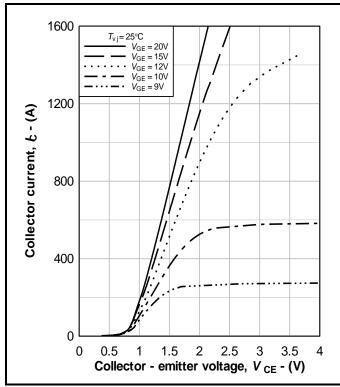


Fig. 3 Typical IGBT output characteristics,  $I_C = f(V_{CE})$ 

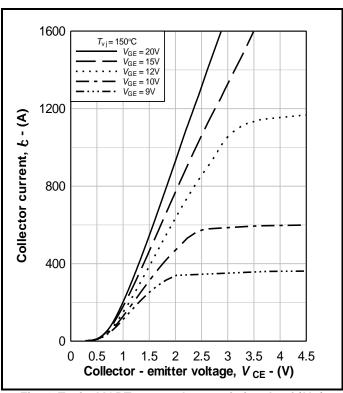


Fig. 4 Typical IGBT output characteristics,  $I_C = f(V_{CE})$ 

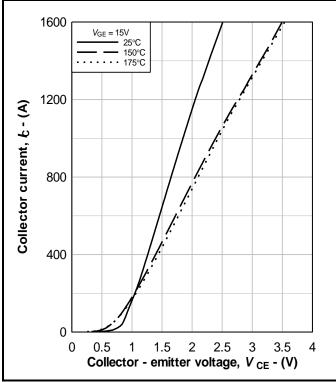


Fig. 5 Typical IGBT output characteristics,  $I_C = f(V_{CE})$ 

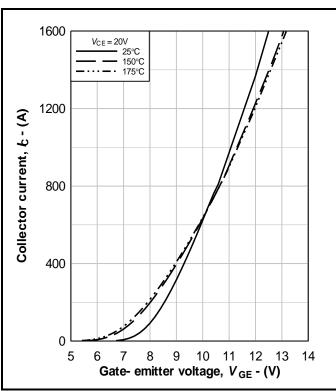


Fig. 6 Typical IGBT transfer characteristics,  $I_C = f(V_{GE})$ 

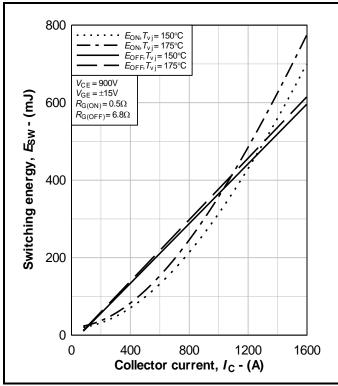


Fig. 7 Typical IGBT switching energy,  $E_{ON} = f(I_C)$ ,  $E_{OFF} = f(I_C)$ 

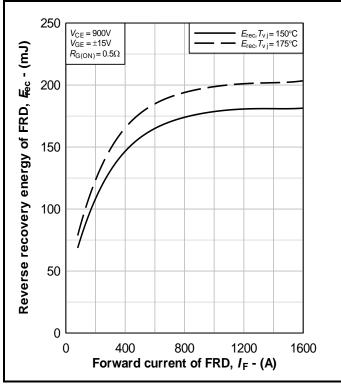


Fig. 9 Typical FRD  $E_{rec}$ ,  $E_{rec} = f(I_F)$ 

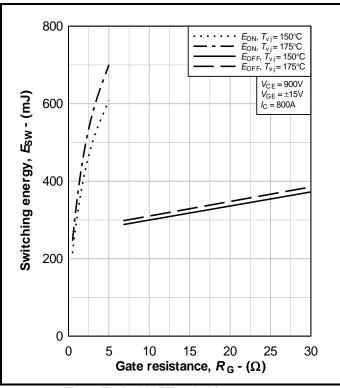


Fig. 8 Typical IGBT switching energy,  $E_{ON} = f(R_G)$ ,  $E_{OFF} = f(R_G)$ 

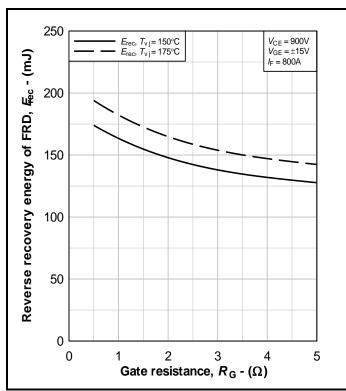


Fig. 10 Typical FRD  $E_{rec}$ ,  $E_{rec} = f(R_G)$ 

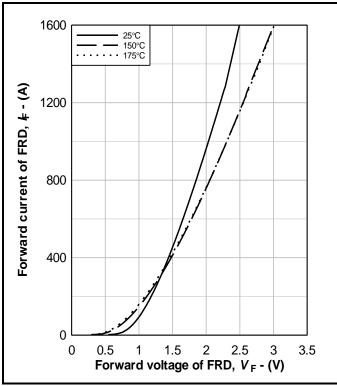


Fig. 11 Diode typical forward characteristics,  $I_F = f(V_F)$ 

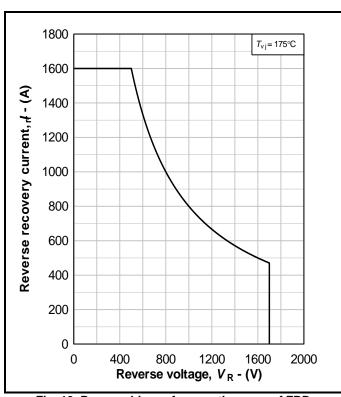


Fig. 13 Reverse bias safe operating area of FRD,  $I_{rr} = f(V_R)$ 

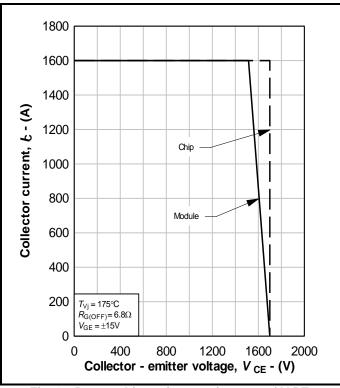


Fig. 12 Reverse bias safe operating area of IGBT,  $I_C = f(V_{CE})$ 

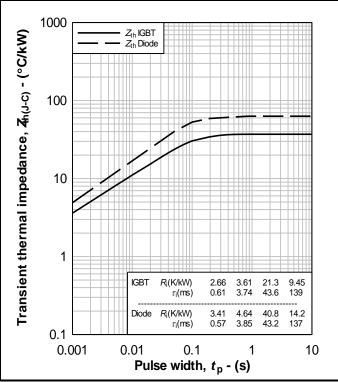


Fig. 14 Transient thermal impedance,  $Z_{th}(J-c) = f(t_p)$ 

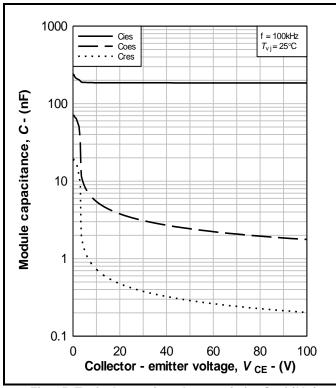


Fig. 15 Typical capacitor characteristic,  $C = f(V_{CE})$ 

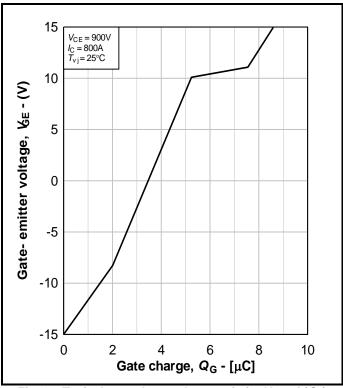


Fig. 16 Typical gate charge characteristic,  $V_{GE} = f(Q_G)$ 

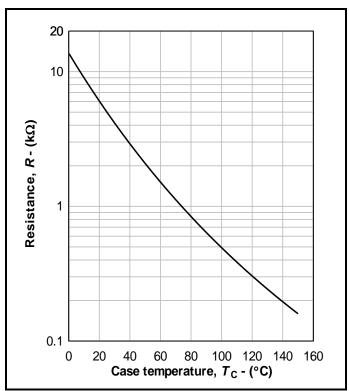


Fig. 17 Typical NTC thermistor characteristic,  $R = f(T_c)$ 

### **PACKAGE DETAILS**

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise.

#### DO NOT SCALE.

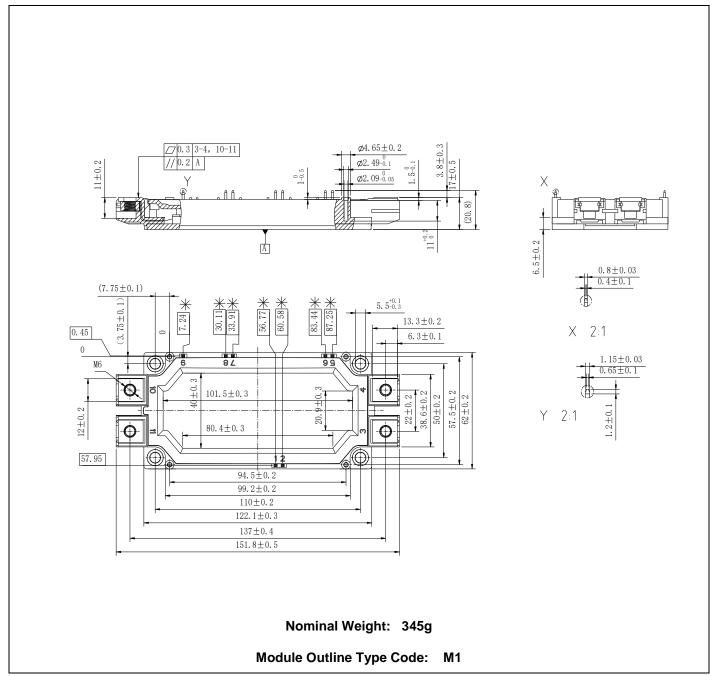


Fig. 18 Module outline drawing

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The products must not be touched when operating because there is a danger of electrocution or severe burning. Always use protective safety equipment such as appropriate shields for the product and wear safety glasses. Even when disconnected any electric charge remaining in the product must be discharged and allowed to cool before safe handling using protective gloves.

Extended exposure to conditions outside the product ratings may affect reliability leading to premature product failure. Use outside the product ratings is likely to cause permanent damage to the product. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture, a large current to flow or high voltage arcing, resulting in fire or explosion. Appropriate application design and safety precautions should always be followed to protect persons and property.

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