

### FEATURES

- 10 $\mu$ s Short Circuit Withstand
- High Thermal Cycling Capability
- Soft Punch Through Silicon
- Isolated AlSiC Base with AlN Substrates
- Lead Free construction

### APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- Traction Drives
- Choppers

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 600V to 6500V and currents up to 2400A.

The DIM750ASM65-TS000 is a single switch 6500V, soft punch through n-channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10 $\mu$ s short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

### ORDERING INFORMATION

Order As:

#### DIM750ASM65-TS000

Note: When ordering, please use the complete part number

### KEY PARAMETERS

$V_{CES}$	<b>6500V</b>
$V_{CE(sat)}$ * (typ)	<b>2.8V</b>
$I_C$ (max)	<b>750A</b>
$I_{C(PK)}$ (max)	<b>1500A</b>

\* Measured at the auxiliary terminals

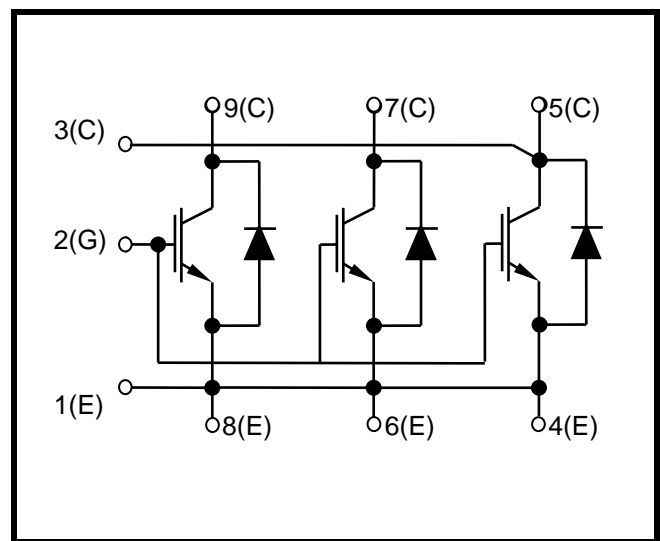


Fig. 1 Circuit configuration



Outline type code: A  
(See Fig. 11 for further information)

Fig. 2 Package

## ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

$T_{case} = 25^{\circ}\text{C}$  unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
$V_{CES}$	Collector-emitter voltage	$V_{GE} = 0\text{V}, T_j = 125^{\circ}\text{C}$	6500	V
		$V_{GE} = 0\text{V}, T_j = 25^{\circ}\text{C}$	6500	V
		$V_{GE} = 0\text{V}, T_j = -40^{\circ}\text{C}$	6000	V
$V_{GES}$	Gate-emitter voltage		$\pm 20$	V
$I_C$	Continuous collector current	$T_{case} = 100^{\circ}\text{C}$	750	A
$I_{C(PK)}$	Peak collector current	1ms, $T_{case} = 115^{\circ}\text{C}$	1500	A
$P_{max}$	Max. transistor power dissipation	$T_{case} = 25^{\circ}\text{C}, T_j = 125^{\circ}\text{C}$	11.1	kW
$I^2t$	Diode $I^2t$ value	$V_R = 0, t_p = 10\text{ms}, T_j = 125^{\circ}\text{C}$	200	$\text{kA}^2\text{s}$
$V_{isol}$	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	10.2	kV
$Q_{PD}$	Partial discharge – per module	IEC1287, $V_1 = 6900\text{V}, V_2 = 5100\text{V}, 50\text{Hz RMS}$	10	pC

## THERMAL AND MECHANICAL RATINGS

Internal insulation material:	AlN
Baseplate material:	AlSiC
Creepage distance:	56mm
Clearance:	26mm
CTI (Comparative Tracking Index):	>600

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
$R_{th(j-c)}$	Thermal resistance – transistor	Continuous dissipation – junction to case			9	$^{\circ}\text{C}/\text{kW}$
$R_{th(j-c)}$	Thermal resistance – diode	Continuous dissipation – junction to case			18	$^{\circ}\text{C}/\text{kW}$
$R_{th(c-h)}$	Thermal resistance – case to heatsink	Mounting torque 5Nm (with mounting grease)			6	$^{\circ}\text{C}/\text{kW}$
$T_j$	Junction temperature	Transistor			125	$^{\circ}\text{C}$
		Diode			125	$^{\circ}\text{C}$
$T_{stg}$	Storage temperature range		-40		125	$^{\circ}\text{C}$
	Screw torque	Mounting – M6			5	Nm
		Electrical connections – M4			2	Nm
		Electrical connections – M8			10	Nm

**ELECTRICAL CHARACTERISTICS**
**T<sub>case</sub> = 25°C unless stated otherwise.**

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
I <sub>CES</sub>	Collector cut-off current	V <sub>GE</sub> = 0V, V <sub>CE</sub> = V <sub>CES</sub>			4	mA
		V <sub>GE</sub> = 0V, V <sub>CE</sub> = V <sub>CES</sub> , T <sub>case</sub> = 125°C			90	mA
I <sub>GES</sub>	Gate leakage current	V <sub>GE</sub> = ± 20V, V <sub>CE</sub> = 0V			1	μA
V <sub>GE(TH)</sub>	Gate threshold voltage	I <sub>C</sub> = 120mA, V <sub>GE</sub> = V <sub>CE</sub>	5.5	6.5	7.5	V
V <sub>CE(sat)</sub> †	Collector-emitter saturation voltage	V <sub>GE</sub> = 15V, I <sub>C</sub> = 750A		2.8		V
		V <sub>GE</sub> = 15V, I <sub>C</sub> = 750A, T <sub>j</sub> = 125°C		3.7		V
I <sub>F</sub>	Diode forward current	DC			750	A
I <sub>FM</sub>	Diode maximum forward current	t <sub>p</sub> = 1ms			1500	A
V <sub>F</sub> †	Diode forward voltage	I <sub>F</sub> = 750A		3.3		V
		I <sub>F</sub> = 750A, T <sub>j</sub> = 125°C		3.7		V
C <sub>ies</sub>	Input capacitance	V <sub>CE</sub> = 25V, V <sub>GE</sub> = 0V, f = 1MHz		120		nF
Q <sub>g</sub>	Gate charge	±15V		10		μC
C <sub>res</sub>	Reverse transfer capacitance	V <sub>CE</sub> = 25V, V <sub>GE</sub> = 0V, f = 1MHz		2.3		nF
L <sub>M</sub>	Module inductance			10		nH
R <sub>INT</sub>	Internal resistance			90		μΩ
SC <sub>Data</sub>	Short circuit current, I <sub>SC</sub>	T <sub>j</sub> = 125°C, V <sub>CC</sub> = 4400V t <sub>p</sub> ≤ 10μs, V <sub>GE</sub> ≤ 15V V <sub>CE(max)</sub> = V <sub>CES</sub> - L* x di/dt IEC 60747-9		3700		A

**Note:**

† Measured at the auxiliary terminals

 \* L is the circuit inductance + L<sub>M</sub>

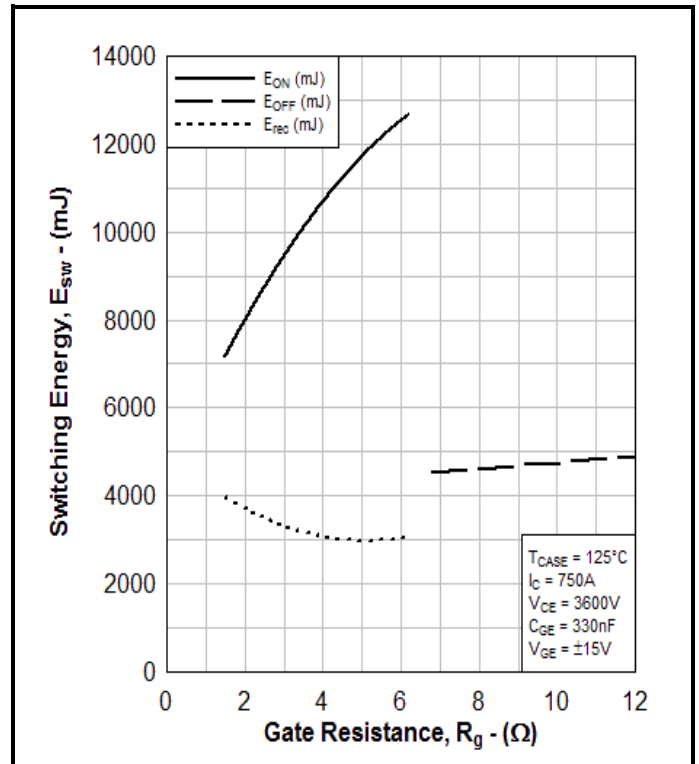
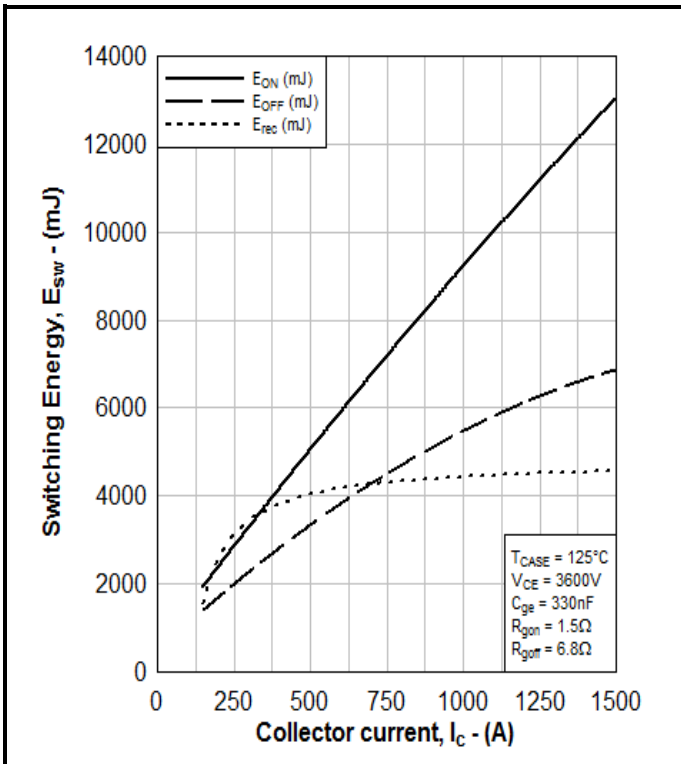
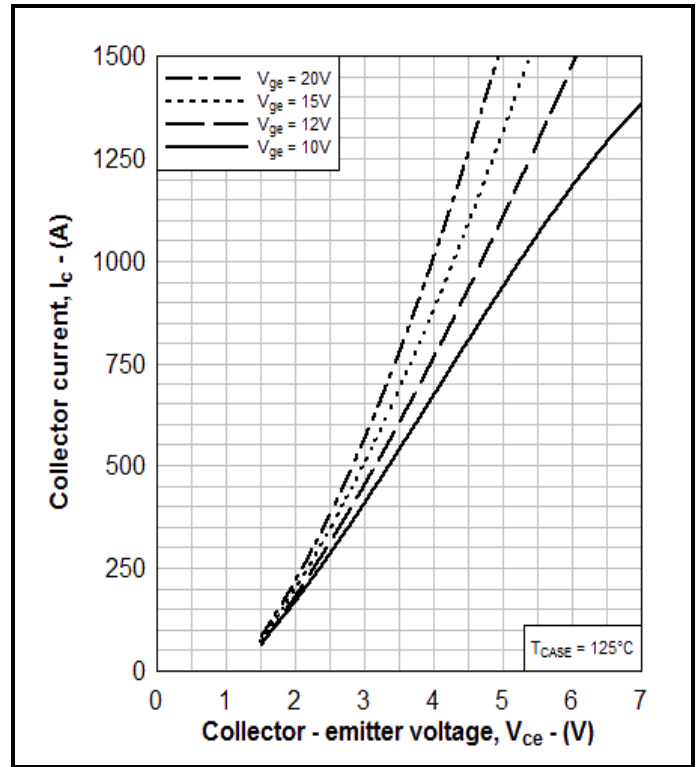
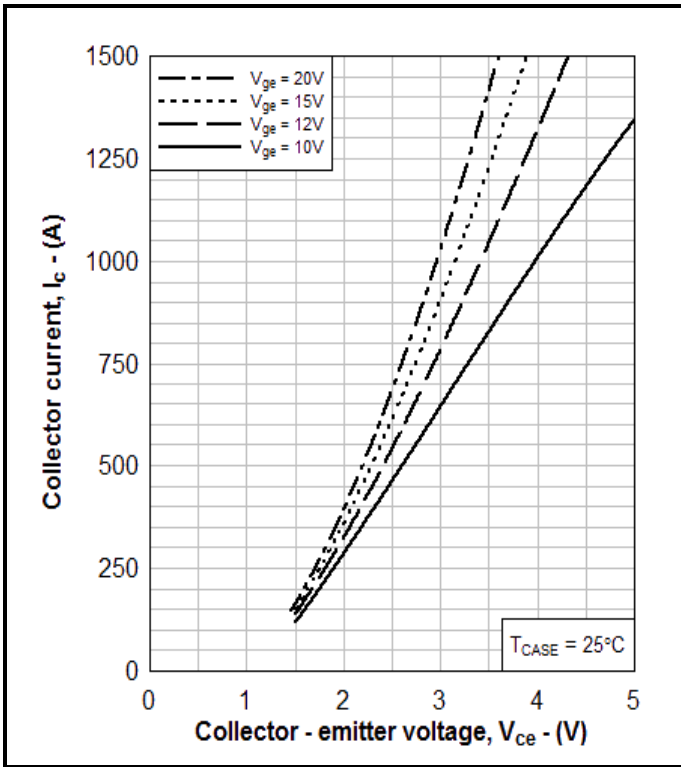
## ELECTRICAL CHARACTERISTICS

$T_{case} = 25^{\circ}\text{C}$  unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
$t_{d(off)}$	Turn-off delay time	$I_C = 750\text{A}$ $V_{GE} = \pm 15\text{V}$ $V_{CE} = 3600\text{V}$ $R_{G(ON)} = 1.5\Omega$ $R_{G(OFF)} = 6.8\Omega$ $C_{ge} = 330\text{nF}$ $L_S \sim 200\text{nH}$		3.7		$\mu\text{s}$
$t_f$	Fall time			450		ns
$E_{OFF}$	Turn-off energy loss			4400		mJ
$t_{d(on)}$	Turn-on delay time			620		ns
$t_r$	Rise time			340		ns
$E_{ON}$	Turn-on energy loss			5850		mJ
$Q_{rr}$	Diode reverse recovery charge	<b>Diode arm</b>		1500		$\mu\text{C}$
$I_{rr}$	Diode reverse recovery current	$I_F = 750\text{A}$ $V_{CE} = 3600\text{V}$		1550		A
$E_{rec}$	Diode reverse recovery energy	$dI_F/dt = 4000\text{A}/\mu\text{s}$		2700		mJ

$T_{case} = 125^{\circ}\text{C}$  unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
$t_{d(off)}$	Turn-off delay time	$I_C = 750\text{A}$ $V_{GE} = \pm 15\text{V}$ $V_{CE} = 3600\text{V}$ $R_{G(ON)} = 1.5\Omega$ $R_{G(OFF)} = 6.8\Omega$ $C_{ge} = 330\text{nF}$ $L_S \sim 200\text{nH}$		3.9		$\mu\text{s}$
$t_f$	Fall time			450		ns
$E_{OFF}$	Turn-off energy loss			4500		mJ
$t_{d(on)}$	Turn-on delay time			550		ns
$t_r$	Rise time			300		ns
$E_{ON}$	Turn-on energy loss			7100		mJ
$Q_{rr}$	Diode reverse recovery charge	<b>Diode arm</b>		2500		$\mu\text{C}$
$I_{rr}$	Diode reverse recovery current	$I_F = 750\text{A}$ $V_{CE} = 3600\text{V}$		2350		A
$E_{rec}$	Diode reverse recovery energy	$dI_F/dt = 4200\text{A}/\mu\text{s}$		4300		mJ



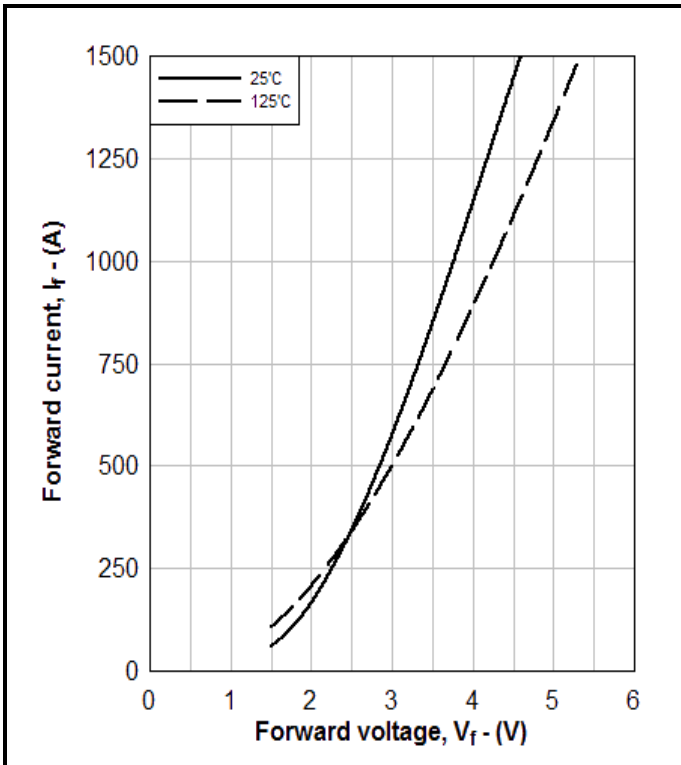


Fig. 7 Diode typical forward characteristics

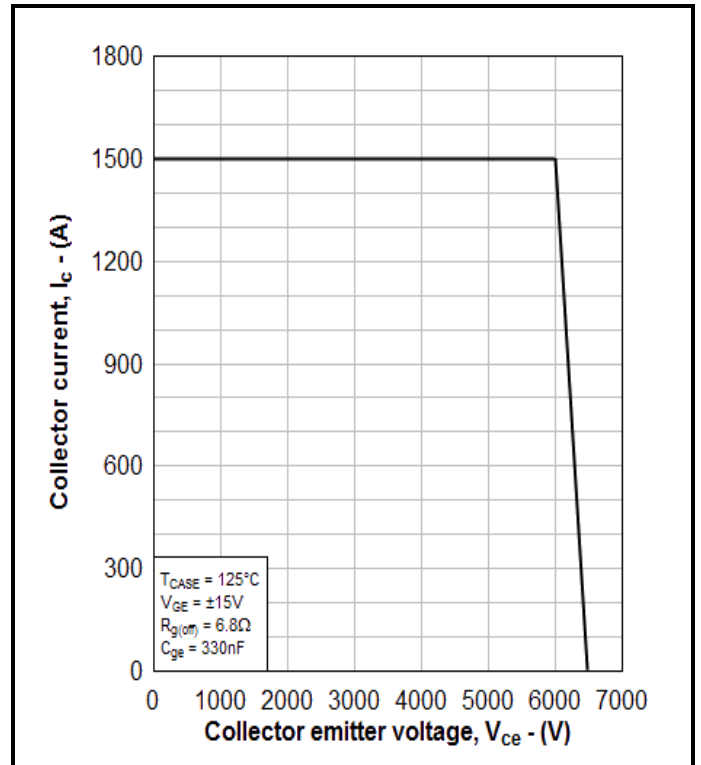


Fig. 8 Reverse bias safe operating area

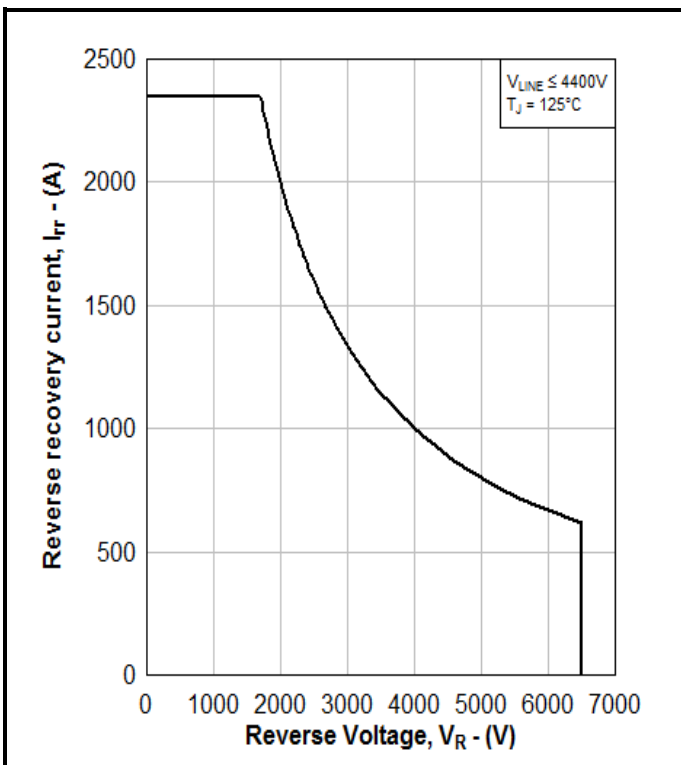


Fig. 9 Diode reverse bias safe operating area

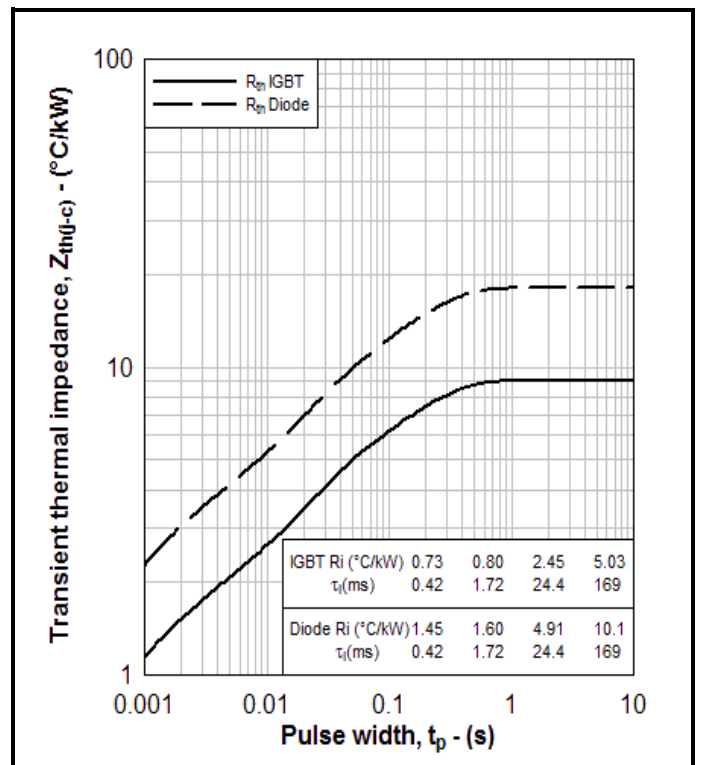
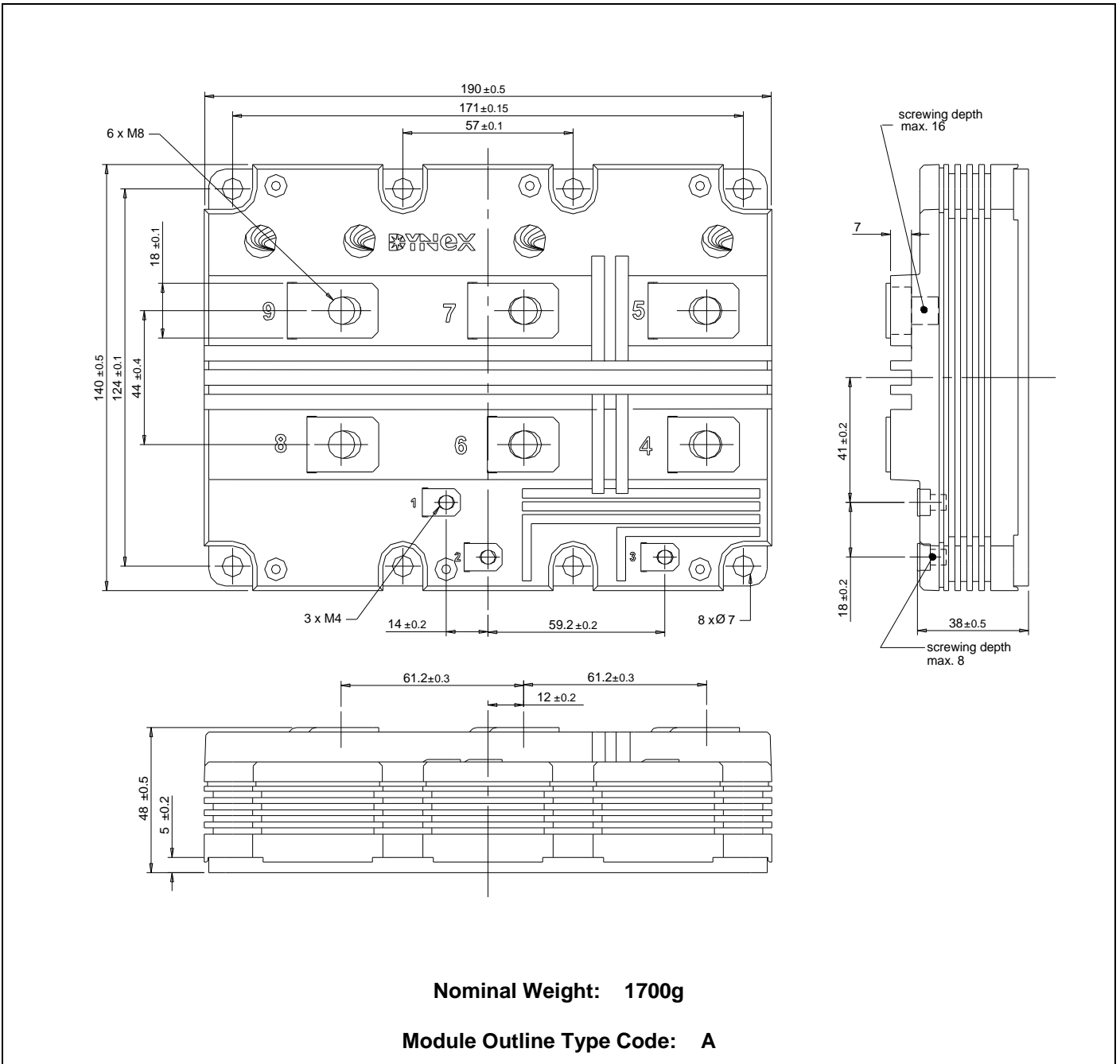


Fig. 10 Transient thermal impedance

**PACKAGE DETAILS**

For further package information, please visit our website or contact Customer Services.  
 All dimensions in mm, unless stated otherwise.  
**DO NOT SCALE.**



**Fig. 11 Module outline drawing**

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