

TRENCH **TSPT**

DIM670ACM65-UF000

IGBT Chopper Module

DS6398-1 January 2022 (LN41435)

FEATURES

- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- Trench Gate Soft Punch Through IGBT
- Isolated AISiC Base with AIN Substrates
- Lead Free construction

APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- **Traction Drives**
- Choppers

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 600V to 6500V and currents up to 2400A.

The DIM670ACM65-UF000 is a 6500V, soft punch through n-channel enhancement mode, insulated gate bipolar transistor (IGBT) chopper module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10µs short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM670ACM65-UF000

Note: When ordering, please use the complete part number

KEY PARAMETERS

V _{CES}		6500V
V _{CE(sat)}	* (typ)	3.6V
Ic	(max)	670A
$I_{C(PK)}$	(max)	1340A

^{*} Measured at the auxiliary terminals

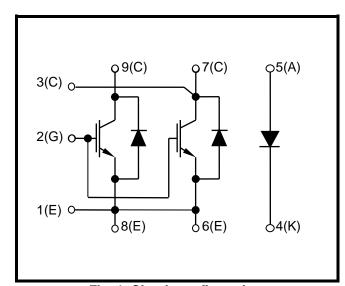


Fig. 1 Circuit configuration



Fig. 2 Package

ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
		V _{GE} = 0V, T _j = 150°C	6500	V
Vces	Collector-emitter voltage	$V_{GE} = 0V$, $T_j = 25$ °C	6300	V
		$V_{GE} = 0V, T_j = -50^{\circ}C$	5700	V
V _{GES}	Gate-emitter voltage		±20	V
Ic	Continuous collector current	T _{case} = 112°C	670	Α
I _{C(PK)}	Peak collector current	1ms, T _{case} = 148°C	1340	Α
P _{max}	Max. transistor power dissipation	$T_{case} = 25^{\circ}C, T_{j} = 150^{\circ}C$	8.9	kW
124	Diode I ² t value (IGBT arm)	V 0.4 40ma T 450°C	208	kA ² s
l ² t	Diode I ² t value (Diode arm)	$V_R = 0$, $t_p = 10$ ms, $T_j = 150$ °C		kA ² s
V _{isol}	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	10.2	kV
Q _{PD}	Partial discharge – per module	IEC1287, V ₁ = 6900V, V ₂ = 5100V, 50Hz RMS	10	рС

THERMAL AND MECHANICAL RATINGS

Internal insulation material:

Baseplate material:

Creepage distance:

Clearance:

CTI (Comparative Tracking Index):

AIN

AISiC

56mm

26mm

>600

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
R _{th(j-c)}	Thermal resistance – transistor	Continuous dissipation – junction to case			14	°C/kW
R _{th(j-c)}	Thermal resistance – diode (IGBT arm)	Continuous dissipation – junction to case			27	°C/kW
R _{th(j-c)}	Thermal resistance – diode (Diode arm)	Continuous dissipation – junction to case			27	°C/kW
R _{th(c-h)}	Thermal resistance – case to heatsink	Mounting torque 5Nm (with mounting grease)			8	°C/kW
т.	Junction temperature	Transistor			150	°C
Tj		Diode			150	°C
T _{stg}	Storage temperature range		-40		125	°C
		Mounting – M6			5	Nm
	Screw torque	Electrical connections – M4			2	Nm
		Electrical connections – M8			10	Nm

ELECTRICAL CHARACTERISTICS

 T_{case} = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
	Callegates and off annually	V _{GE} = 0V, V _{CE} = V _{CES}			4	mA
I _{CES}	Collector cut-off current	V _{GE} = 0V, V _{CE} = V _{CES} , T _{case} = 150°C			135	mA
I _{GES}	Gate leakage current	V _{GE} = ± 20V, V _{CE} = 0V			1	μΑ
V _{GE(TH)}	Gate threshold voltage	Ic = 120mA, V _{GE} = V _{CE}	6.5	6.75	7.3	V
		V _{GE} = 15V, I _C = 670A		3.6		V
V _{CE(sat)}	Collector-emitter saturation voltage	V _{GE} = 15V, I _C = 670A, T _j = 125°C		4.0		V
	Tomage	V _{GE} = 15V, I _C = 670A, T _j = 150°C		4.1		V
l _F	Diode forward current	DC			670	Α
I _{FM}	Diode maximum forward current	$t_p = 1 ms$			1340	Α
		I _F = 670A		3.8		V
VF	Diode forward voltage	I _F = 670A, T _j = 125°C		4.15		V
		$I_F = 670A, T_j = 150^{\circ}C$		4.2		V
Cies	Input capacitance	V _{CE} = 25V, V _{GE} = 0V, f = 100kHz		117		nF
Qg	Gate charge	±15V		10		μC
Cres	Reverse transfer capacitance	V _{CE} = 25V, V _{GE} = 0V, f = 100kHz		4		nF
		IGBT		15		nΗ
L _M	Module inductance	Diode		15		nΗ
		IGBT		135		μΩ
R _{INT}	Internal resistance	Diode		270		μΩ
SC _{Data}	Short circuit current, Isc	$T_{j} = 125$ °C, $V_{CC} = 4400V$ $t_{p} \le 10 \mu s$, $V_{GE} \le 15V$ $V_{CE (max)} = V_{CES} - L^{*} x dI/dt$ IEC 60747-9		3000		А

Note:

 $^{^{\}star}$ L is the circuit inductance + L_M

ELECTRICAL CHARACTERISTICS

 T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time	Ic = 670A		4.5		μs
t _f	Fall time	V _{GE} = ±15V		400		ns
Eoff	Turn-off energy loss	V _{CE} = 3600V		3200		mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = 2.2\Omega$ $R_{G(OFF)} = 22\Omega$ $C_{ge} = 100nF$ $L_{S} \sim 280nH$		720		ns
tr	Rise time			290		ns
Eon	Turn-on energy loss			5000		mJ
Qrr	Diode reverse recovery charge	I _F = 670A V _{CE} = 3600V dI _F /dt = 2700A/μs		1100		μC
Irr	Diode reverse recovery current			1300		Α
Erec	Diode reverse recovery energy			1800		mJ

T_{case} = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time	Ic = 670A		5		μs
t f	Fall time	$V_{GE} = \pm 15V$		500		ns
E _{OFF}	Turn-off energy loss	V _{CE} = 3600V		3700		mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = 2.2\Omega$ $R_{G(OFF)} = 22\Omega$ $C_{ge} = 100 nF$ $L_{S} \sim 280 nH$		740		ns
tr	Rise time			250		ns
Eon	Turn-on energy loss			6400		mJ
Qrr	Diode reverse recovery charge	I _F = 670A V _{CE} = 3600V dI _F /dt = 2700A/μs		1910		μC
I _{rr}	Diode reverse recovery current			1570		Α
E _{rec}	Diode reverse recovery energy			3200		mJ

T_{case} = 150°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time	Ic = 670A		5		μs
t f	Fall time	V _{GE} = ±15V		520		ns
E _{OFF}	Turn-off energy loss	V _{CE} = 3600V		3800		mJ
t _{d(on)}	Turn-on delay time	$\begin{array}{c} R_{G(ON)} = 2.2\Omega \\ R_{G(OFF)} = 22\Omega \\ C_{ge} = 100 nF \\ L_{S} \sim 280 nH \end{array}$		750		ns
tr	Rise time			230		ns
Eon	Turn-on energy loss			6700		mJ
Qrr	Diode reverse recovery charge	I _F = 670A V _{CE} = 3600V dI _F /dt = 2700A/μs		1910		μC
Irr	Diode reverse recovery current			1570		Α
E _{rec}	Diode reverse recovery energy			3400		mJ

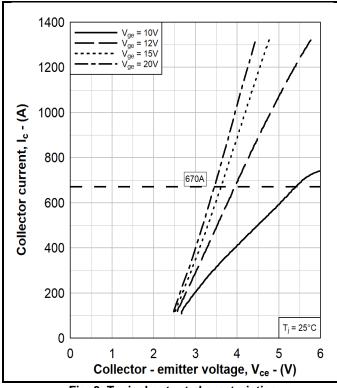


Fig. 3 Typical output characteristics

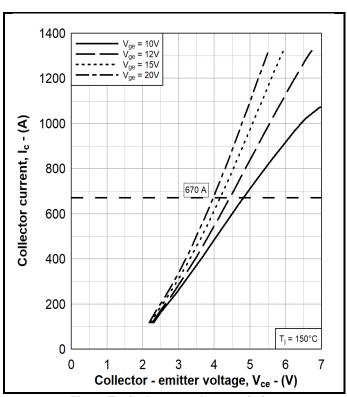


Fig. 4 Typical output characteristics

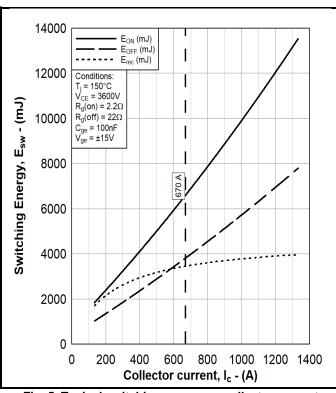


Fig. 5 Typical switching energy vs collector current

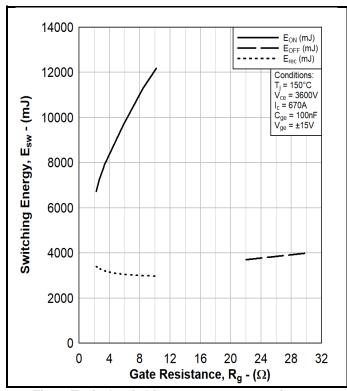


Fig. 6 Typical switching energy vs gate resistance

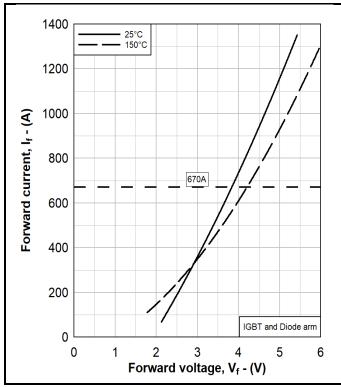


Fig. 7 Diode typical forward characteristics

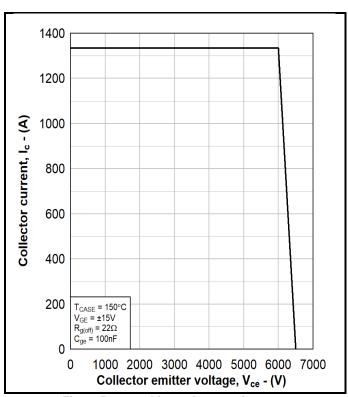


Fig. 8 Reverse bias safe operating area

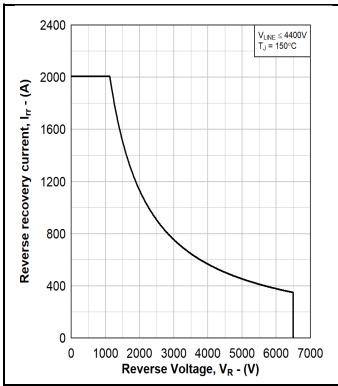


Fig. 9 Diode reverse bias safe operating area

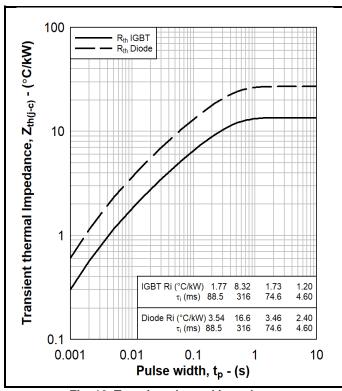


Fig. 10 Transient thermal impedance

PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise.

DO NOT SCALE.

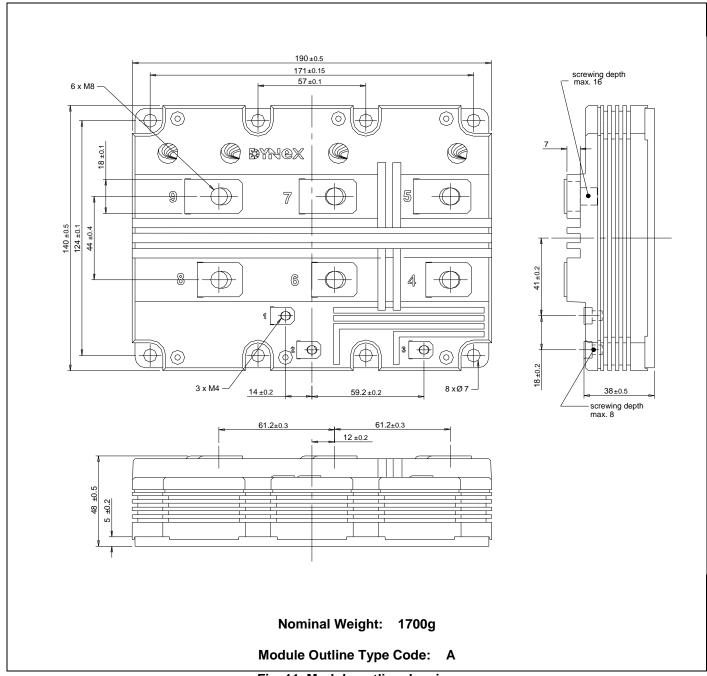


Fig. 11 Module outline drawing

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