

FEATURES

- 10 μ s Short Circuit Withstand
- High Thermal Cycling Capability
- High Current Density Enhanced DMOS SPT
- Isolated AlSiC Base with AlN Substrates

APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- Traction Drives
- Choppers

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM500GDM33-TS000 is a single switch 3300V, n-channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10 μ s short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM500GDM33-TS000

Note: When ordering, please use the complete part number

KEY PARAMETERS

V_{CES}	3300V
$V_{CE(sat)}$ * (typ)	2.2V
I_C (max)	500A
$I_{C(PK)}$ (max)	1000A

* Measured at the auxiliary terminals

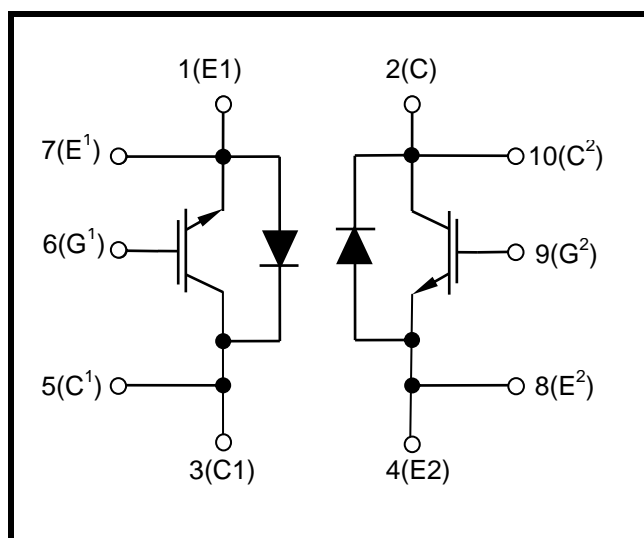


Fig. 1 Circuit configuration



Outline type code: G

(See Fig. 11 for further information)

Fig. 2 Package

ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V _{CES}	Collector-emitter voltage	V _{GE} = 0V	3300	V
V _{GES}	Gate-emitter voltage		±20	V
I _C	Continuous collector current	T _{case} = 110°C	500	A
I _{C(PK)}	Peak collector current	1ms, T _{case} = 140°C	1000	A
P _{max}	Max. transistor power dissipation	T _{case} = 25°C, T _j = 150°C	5.2	kW
I ² t	Diode I ² t value	V _R = 0, t _p = 10ms, T _j = 125°C	80	kA ² s
V _{isol}	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	6000	V
Q _{PD}	Partial discharge – per module	IEC1287, V ₁ = 3500V, V ₂ = 2600V, 50Hz RMS	10	pC

THERMAL AND MECHANICAL RATINGS

Internal insulation material:	AlN
Baseplate material:	AlSiC
Creepage distance:	33mm
Clearance:	20mm
CTI (Comparative Tracking Index):	>600

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
R _{th(j-c)}	Thermal resistance – transistor (per switch)	Continuous dissipation - junction to case	-	-	24	°C/kW
R _{th(j-c)}	Thermal resistance – diode (per switch)	Continuous dissipation - junction to case	-	-	48	°C/kW
R _{th(c-h)}	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	8	°C/kW
T _j	Junction temperature	Transistor	-	-	150	°C
		Diode	-	-	150	°C
T _{stg}	Storage temperature range	-	-40	-	125	°C
	Screw torque	Mounting – M6	-	-	5	Nm
		Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm

ELECTRICAL CHARACTERISTICS

$T_{case} = 25^{\circ}\text{C}$ unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
I_{CES}	Collector cut-off current	$V_{GE} = 0\text{V}, V_{CE} = V_{CES}$			2	mA
		$V_{GE} = 0\text{V}, V_{CE} = V_{CES}, T_{case} = 125^{\circ}\text{C}$			30	mA
		$V_{GE} = 0\text{V}, V_{CE} = V_{CES}, T_{case} = 150^{\circ}\text{C}$			50	mA
I_{GES}	Gate leakage current	$V_{GE} = \pm 20\text{V}, V_{CE} = 0\text{V}$			1	μA
$V_{GE(TH)}$	Gate threshold voltage	$I_C = 120\text{mA}, V_{GE} = V_{CE}$		5.7		V
$V_{CE(sat)}^{\dagger}$	Collector-emitter saturation voltage	$V_{GE} = 15\text{V}, I_C = 500\text{A}$		2.2		V
		$V_{GE} = 15\text{V}, I_C = 500\text{A}, T_j = 125^{\circ}\text{C}$		2.8		V
		$V_{GE} = 15\text{V}, I_C = 500\text{A}, T_j = 150^{\circ}\text{C}$		3.0		V
I_F	Diode forward current	DC		500		A
I_{FM}	Diode maximum forward current	$t_p = 1\text{ms}$		1000		A
V_F^{\dagger}	Diode forward voltage	$I_F = 500\text{A}$		2.4		V
		$I_F = 500\text{A}, T_j = 125^{\circ}\text{C}$		2.5		V
		$I_F = 500\text{A}, T_j = 150^{\circ}\text{C}$		2.4		V
C_{ies}	Input capacitance	$V_{CE} = 25\text{V}, V_{GE} = 0\text{V}, f = 1\text{MHz}$		90		nF
Q_g	Gate charge	$\pm 15\text{V}$		10		μC
C_{res}	Reverse transfer capacitance	$V_{CE} = 25\text{V}, V_{GE} = 0\text{V}, f = 1\text{MHz}$		2		nF
L_M	Module inductance			25		nH
R_{INT}	Internal transistor resistance			260		$\mu\Omega$
SC_{Data}	Short circuit current, I_{SC}	$T_j = 150^{\circ}\text{C}, V_{CC} = 2500\text{V}$ $t_p \leq 10\mu\text{s}, V_{GE} \leq 15\text{V}$ $V_{CE(max)} = V_{CES} - L^* \times dl/dt$ IEC 60747-9		1850		A

Note:

\dagger Measured at the power busbars, not the auxiliary terminals

* L is the circuit inductance + L_M

ELECTRICAL CHARACTERISTICS

 $T_{\text{case}} = 25^{\circ}\text{C}$ unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
$t_{d(\text{off})}$	Turn-off delay time	$I_C = 500\text{A}$ $V_{GE} = \pm 15\text{V}$ $V_{CE} = 1800\text{V}$ $R_{G(\text{ON})} = 4.7\Omega$ $R_{G(\text{OFF})} = 4.7\Omega$ $C_{ge} = 100\text{nF}$ $L_S \sim 100\text{nH}$		2700		ns
t_f	Fall time			520		ns
E_{OFF}	Turn-off energy loss			1000		mJ
$t_{d(\text{on})}$	Turn-on delay time			1000		ns
t_r	Rise time			400		ns
E_{ON}	Turn-on energy loss			650		mJ
Q_{rr}	Diode reverse recovery charge	$I_F = 500\text{A}$ $V_{CE} = 1800\text{V}$ $dl_F/dt = 1400\text{A}/\mu\text{s}$		285		μC
I_{rr}	Diode reverse recovery current			310		A
E_{rec}	Diode reverse recovery energy			350		mJ

 $T_{\text{case}} = 125^{\circ}\text{C}$ unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
$t_{d(\text{off})}$	Turn-off delay time	$I_C = 500\text{A}$ $V_{GE} = \pm 15\text{V}$ $V_{CE} = 1800\text{V}$ $R_{G(\text{ON})} = 4.7\Omega$ $R_{G(\text{OFF})} = 4.7\Omega$ $C_{ge} = 100\text{nF}$ $L_S \sim 100\text{nH}$		2750		ns
t_f	Fall time			570		ns
E_{OFF}	Turn-off energy loss			1100		mJ
$t_{d(\text{on})}$	Turn-on delay time			1020		ns
t_r	Rise time			420		ns
E_{ON}	Turn-on energy loss			850		mJ
Q_{rr}	Diode reverse recovery charge	$I_F = 500\text{A}$ $V_{CE} = 1800\text{V}$ $dl_F/dt = 1400\text{A}/\mu\text{s}$		470		μC
I_{rr}	Diode reverse recovery current			390		A
E_{rec}	Diode reverse recovery energy			600		mJ

 $T_{\text{case}} = 150^{\circ}\text{C}$ unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
$t_{d(\text{off})}$	Turn-off delay time	$I_C = 500\text{A}$ $V_{GE} = \pm 15\text{V}$ $V_{CE} = 1800\text{V}$ $R_{G(\text{ON})} = 4.7\Omega$ $R_{G(\text{OFF})} = 4.7\Omega$ $C_{ge} = 100\text{nF}$ $L_S \sim 100\text{nH}$		2800		ns
t_f	Fall time			550		ns
E_{OFF}	Turn-off energy loss			1150		mJ
$t_{d(\text{on})}$	Turn-on delay time			1030		ns
t_r	Rise time			430		ns
E_{ON}	Turn-on energy loss			950		mJ
Q_{rr}	Diode reverse recovery charge	$I_F = 500\text{A}$ $V_{CE} = 1800\text{V}$ $dl_F/dt = 1400\text{A}/\mu\text{s}$		535		μC
I_{rr}	Diode reverse recovery current			400		A
E_{rec}	Diode reverse recovery energy			650		mJ

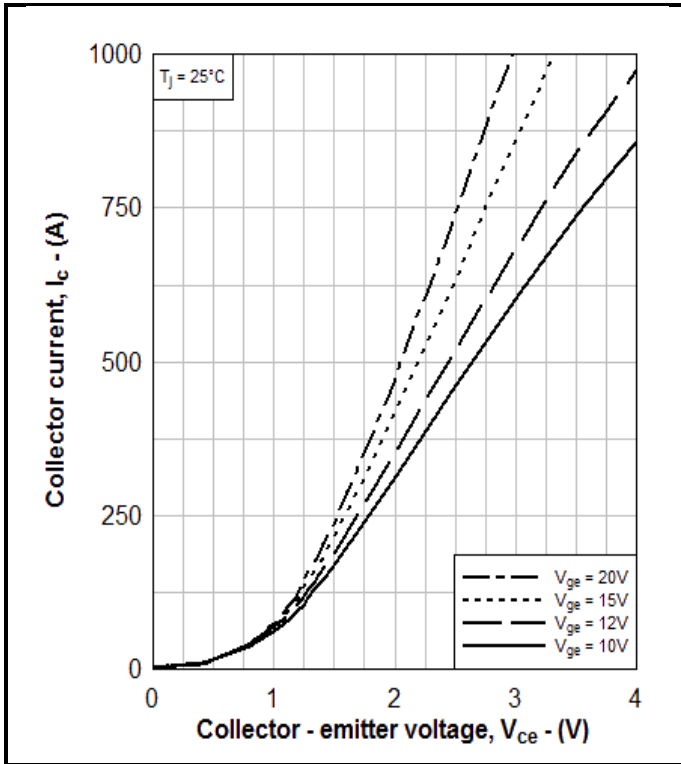


Fig. 3 Typical output characteristics

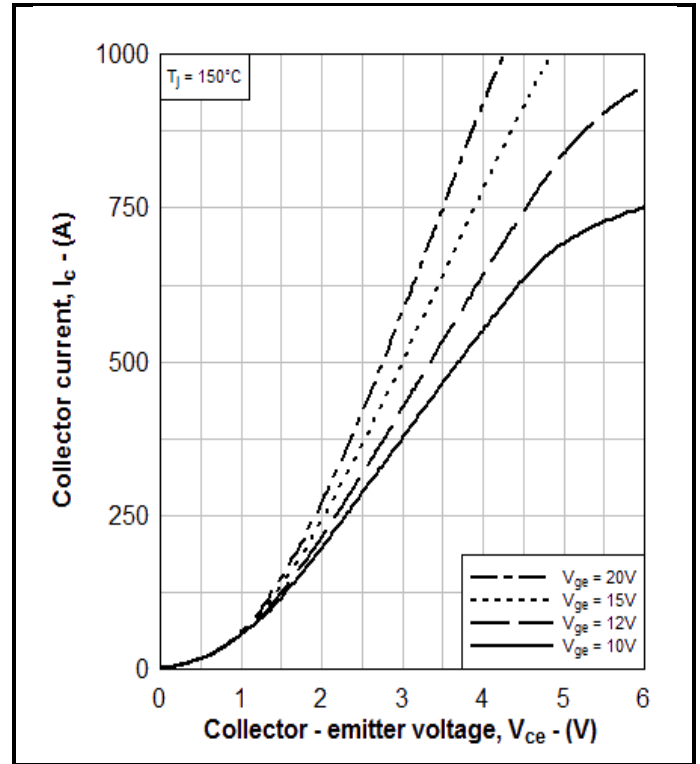


Fig. 4 Typical output characteristics

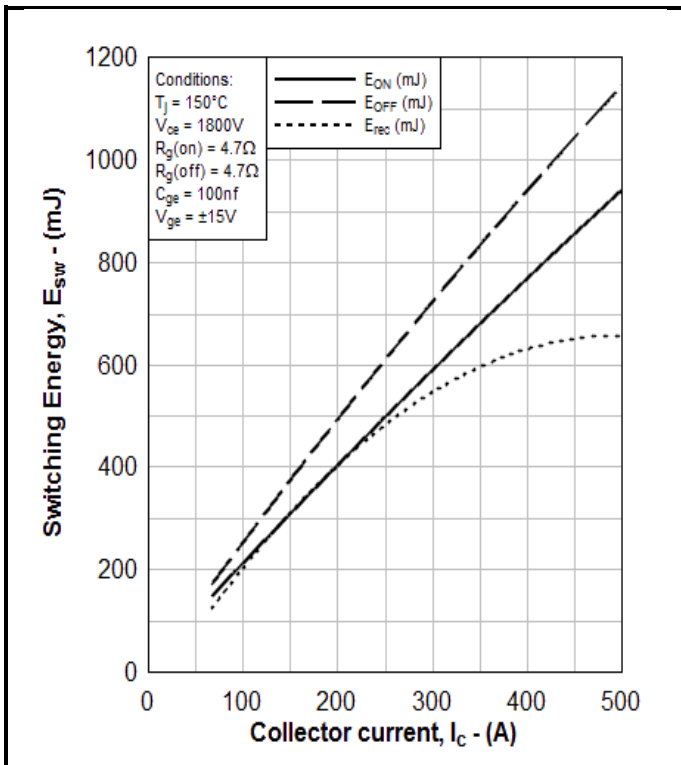


Fig. 5 Typical switching energy vs collector current

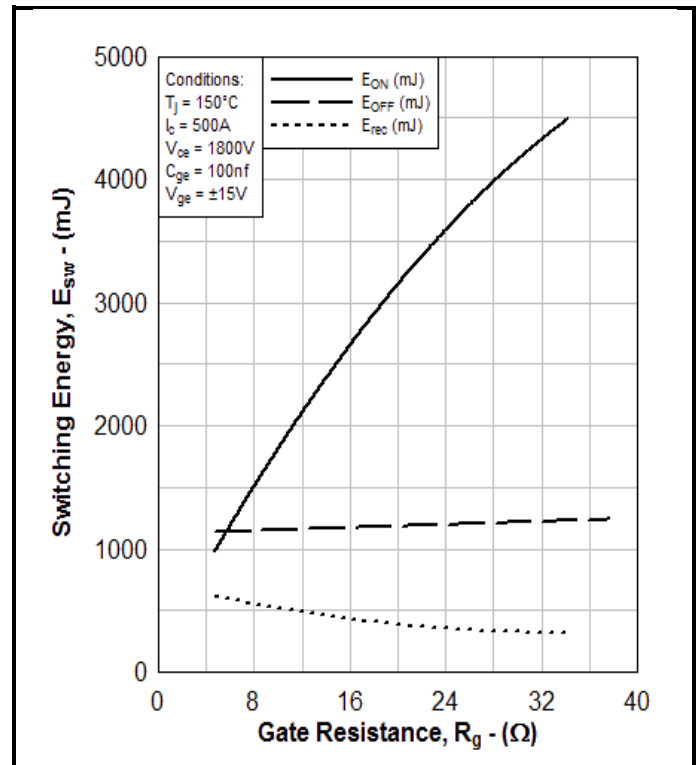


Fig. 6 Typical switching energy vs gate resistance

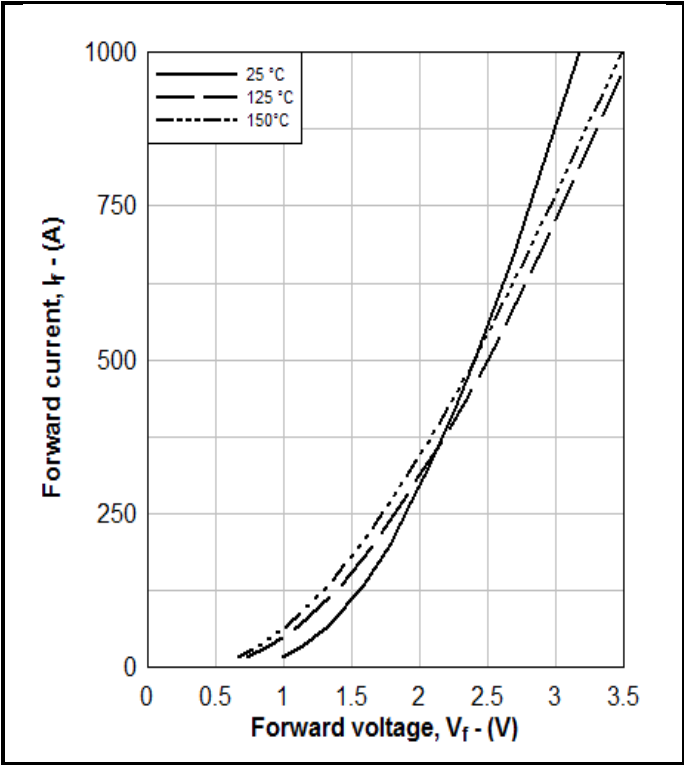


Fig. 7 Diode typical forward characteristics

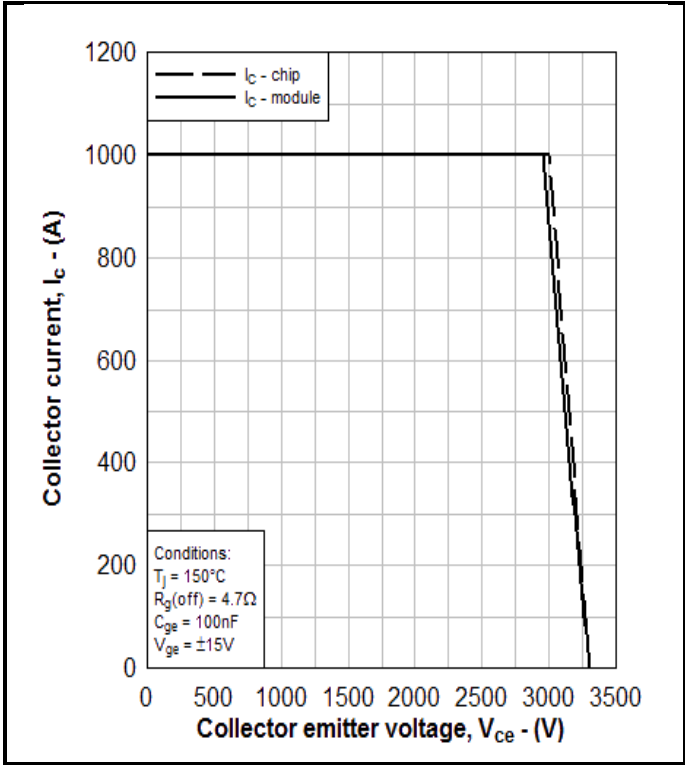


Fig. 8 Reverse bias safe operating area

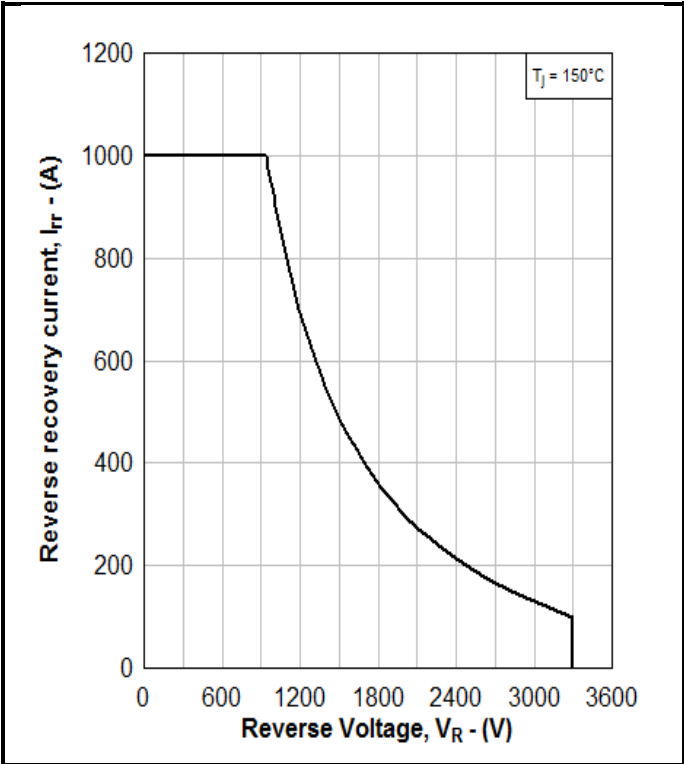


Fig. 9 Diode reverse bias safe operating area

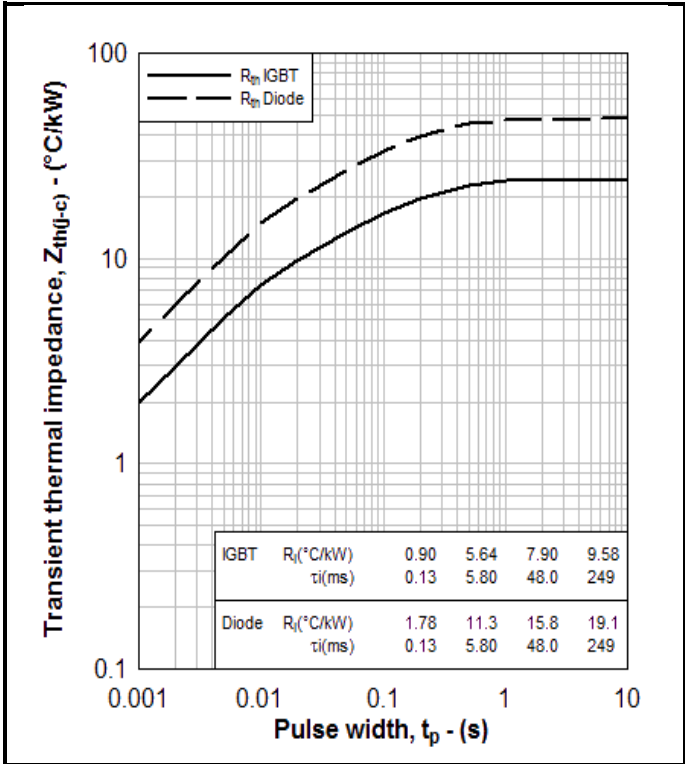
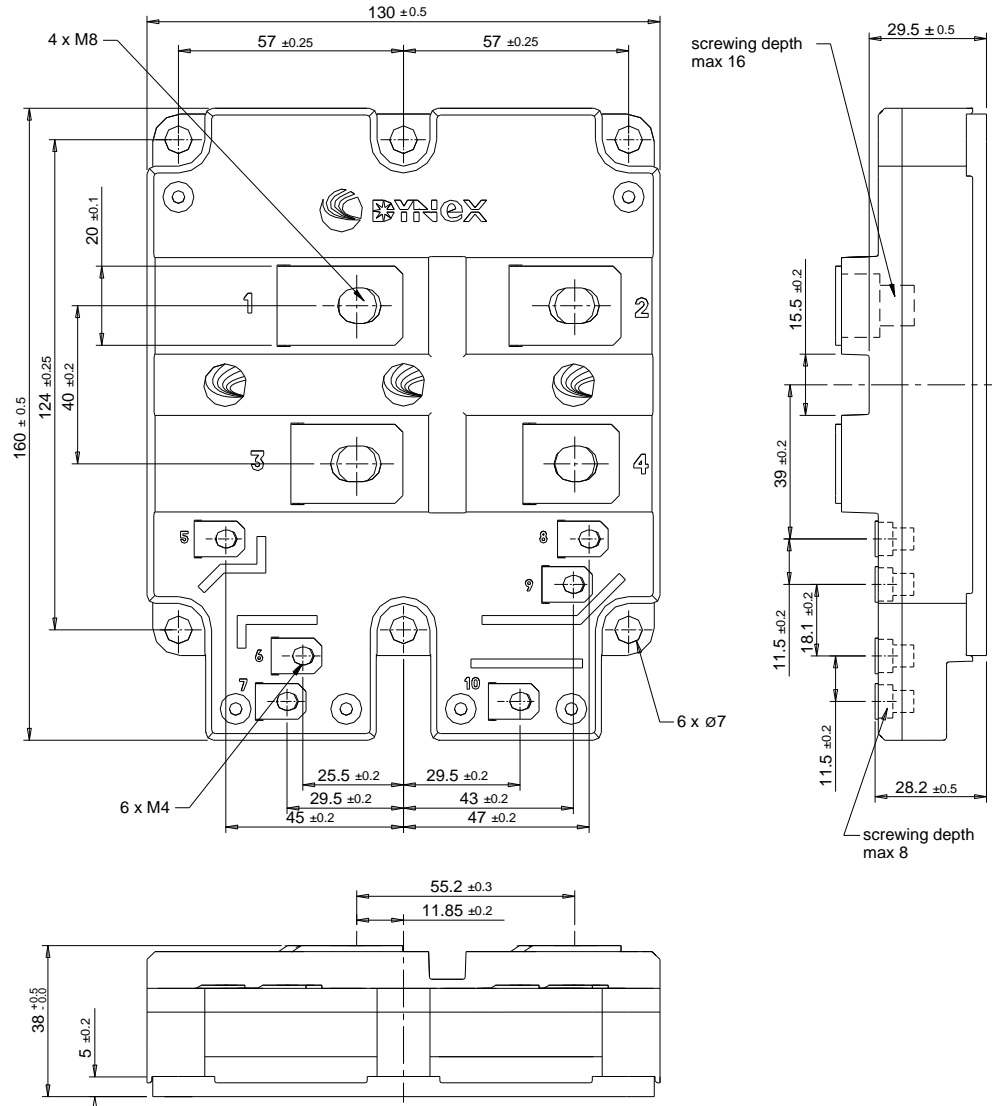


Fig. 10 Transient thermal impedance

PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services.
All dimensions in mm, unless stated otherwise.
DO NOT SCALE.



Nominal Weight: 1000g

Module Outline Type Code: G

Fig. 11 Module outline drawing

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