



DIM400XCM45-TS000

IGBT Chopper Module

DS6111-7 August 2023 (LN42746)

FEATURES

Replaces DS6111-6

- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- High Current Density Enhanced DMOS SPT
- Isolated AlSiC Base With AlN Substrates
- Lead Free Construction

APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- Traction Drives
- Choppers

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM400XCM45-TS000 is a single switch 4500V, n-channel enhancement mode, insulated gate bipolar transistor (IGBT) chopper module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10µs short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM400XCM45-TS000

Note: When ordering, please use the complete part number

KEY PARAMETERS

V _{CES}		4500V
V _{CE(sat)}	* (typ)	2.5V
l _c ` ´	(max)	400A
I _{C(PK)}	(max)	800A

^{*} Measured at the auxiliary terminals

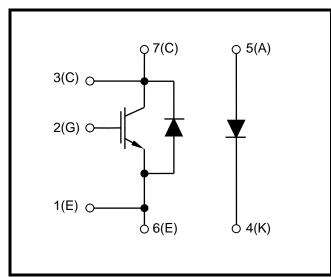


Fig. 1 Circuit configuration

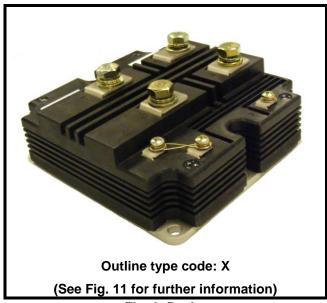


Fig. 2 Package

ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
Vces	Collector-emitter voltage	V _{GE} = 0V	4500	V
V _{GES}	Gate-emitter voltage		±20	V
Ic	Continuous collector current	T _{case} = 90°C	400	Α
I _{C(PK)}	Peak collector current	1ms, T _{case} = 115°C	800	Α
P _{max}	Max. transistor power dissipation	$T_{case} = 25^{\circ}C, T_{j} = 125^{\circ}C$	4.16	kW
l²t	Diode I ² t value	$V_R = 0$, $t_p = 10$ ms, $T_j = 125$ °C	50	kA ² s
V _{isol}	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	7.4	kV
Q_{PD}	Partial discharge – per module	IEC1287, V ₁ = 4800V, V ₂ = 3500V, 50Hz RMS	10	рС

THERMAL AND MECHANICAL RATINGS

Internal insulation material:

Baseplate material:

Creepage distance:

Clearance:

CTI (Comparative Tracking Index):

AIN

ASIC

56mm

26mm

>600

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
R _{th(j-c)}	Thermal resistance – transistor	Continuous dissipation - junction to case		-	24	°C/kW
R _{th(j-c)}	Thermal resistance – diode (IGBT arm)	Continuous dissipation - junction to case	-	-	48	°C/kW
R _{th(j-c)}	Thermal resistance – diode (Diode arm)		-	-	48	°C/kW
R _{th(c-h)}	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)		-	8	°C/kW
T _j	Junction temperature	Transistor	-	-	125	°C
		Diode	-	-	125	°C
T _{stg}	Storage temperature range	-	-40	-	125	°C
	Screw torque	Mounting – M6	-	-	5	Nm
		Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	1	-	10	Nm

ELECTRICAL CHARACTERISTICS

 T_{case} = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Ices	Collector cut-off current	V _{GE} = 0V, V _{CE} = V _{CES}			1	mA
		V _{GE} = 0V, V _{CE} = V _{CES} , T _{case} = 125°C			20	mA
I _{GES}	Gate leakage current	V _{GE} = ± 20V, V _{CE} = 0V			1	μΑ
V _{GE(TH)}	Gate threshold voltage	Ic = 40mA, V _{GE} = V _{CE}		5.8		V
	Collector-emitter saturation voltage	V _{GE} = 15V, I _C = 400A		2.5		V
V _{CE(sat)}		V _{GE} = 15V, I _C = 400A, T _j = 125°C		3.2		V
lF	Diode forward current	DC		400		Α
I _{FM}	Diode maximum forward current	$t_p = 1 ms$		800		Α
	Diode forward voltage	IF = 400A		2.9		V
V _F		I _F = 400A, T _j = 125°C		3.1		V
Cies	Input capacitance	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz		50		nF
Qg	Gate charge	±15V		7.5		μC
Cres	Reverse transfer capacitance	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz		4		nF
L _м	Module inductance – per arm			30		nΗ
RINT	Internal resistance – per arm			270		μΩ
SC _{Data}	Short circuit current, I _{SC}	$T_{j} = 125^{\circ}C$, $V_{CC} = 3400V$ $t_{p} \le 10\mu s$, $V_{GE} \le 15V$ $V_{CE (max)} = V_{CES} - L^{*}x dI/dt$ IEC 60747-9		1600		А

Note:

 $^{^{\}star}$ L is the circuit inductance + L_M

ELECTRICAL CHARACTERISTICS

 $T_{case} = 25$ °C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time	$I_C = 400A$ $V_{GE} = \pm 15V$		2800		ns
t f	Fall time			530		ns
E _{OFF}	Turn-off energy loss	V _{CE} = 2800V		1500		mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = 8.2\Omega$ $R_{G(OFF)} = 8.2\Omega$ $C_{ge} = 68nF$ $L_{S} \sim 165nH$		1000		ns
t _r	Rise time			160		ns
Eon	Turn-on energy loss			1750		mJ
Qrr	Diode reverse recovery charge	I _F = 400A		450		μC
Irr	Diode reverse recovery current	V _{CE} = 2800V		350		Α
E _{rec}	Diode reverse recovery energy	dl _F /dt = 1000A/μs		750		mJ

T_{case} = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time			3100		ns
t _f	Fall time	Ic = 400A V _{GF} = ±15V		560		ns
E _{OFF}	Turn-off energy loss	V _{CE} = 2800V		1300		mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = 8.2\Omega$ $R_{G(OFF)} = 8.2\Omega$ $C_{ge} = 68nF$ $L_{S} \sim 165nH$		900		ns
tr	Rise time			360		ns
Eon	Turn-on energy loss			2400		mJ
Qrr	Diode reverse recovery charge	I _F = 400A		750		μC
I _{rr}	Diode reverse recovery current	V _{CE} = 2800V		380		Α
Erec	Diode reverse recovery energy	dl _F /dt = 1000A/μs		1000		mJ

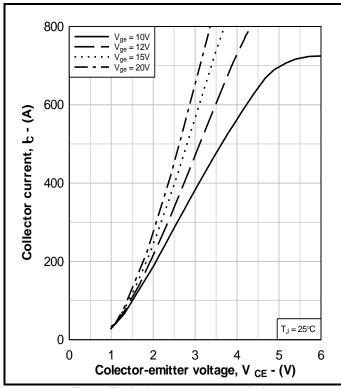


Fig. 3 Typical output characteristics

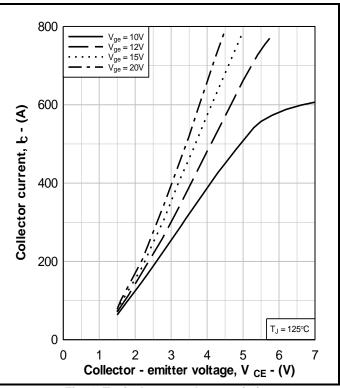


Fig. 4 Typical output characteristics

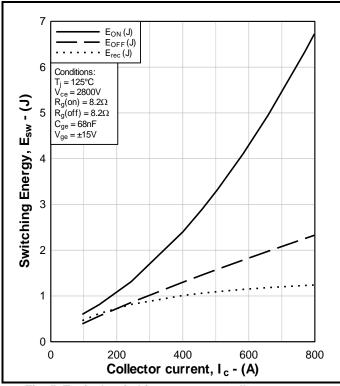


Fig. 5 Typical switching energy vs collector current

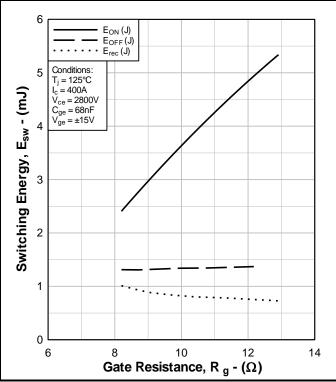


Fig. 6 Typical switching energy vs gate resistance

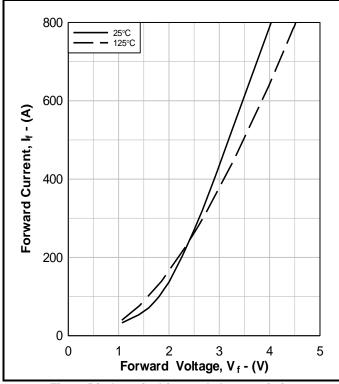


Fig. 7 Diode typical forward characteristics

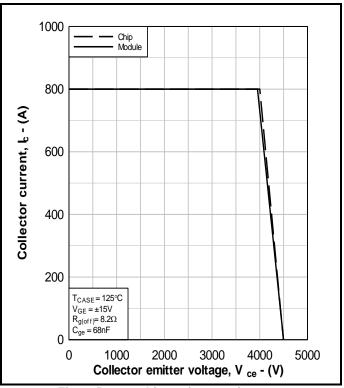


Fig. 8 Reverse bias safe operating area

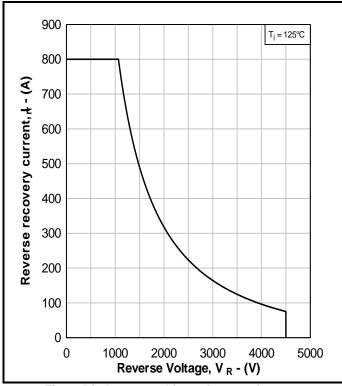


Fig. 9 Diode reverse bias safe operating area

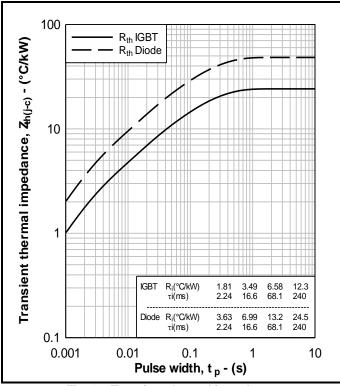


Fig. 10 Transient thermal impedance

PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise.

DO NOT SCALE.

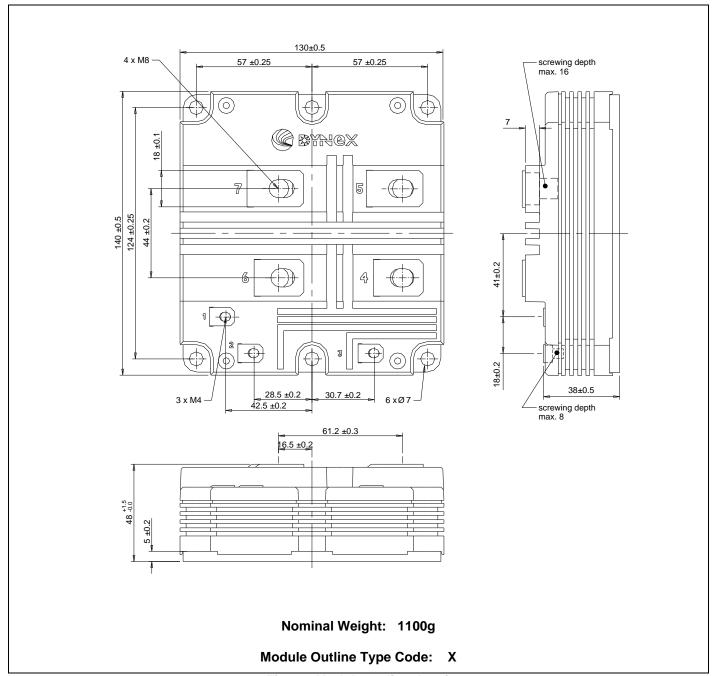


Fig. 11 Module outline drawing

IMPORTANT INFORMATION:

This publication is provided for information only and not for resale.

The products and information in this publication are intended for use by appropriately trained technical personnel.

Due to the diversity of product applications, the information contained herein is provided as a general guide only and does not constitute any guarantee of suitability for use in a specific application. The user must evaluate the suitability of the product and the completeness of the product data for the application. The user is responsible for product selection and ensuring all safety and any warning requirements are met. Should additional product information be needed please contact Customer Service.

Although we have endeavoured to carefully compile the information in this publication it may contain inaccuracies or typographical errors. The information is provided without any warranty or guarantee of any kind.

This publication is an uncontrolled document and is subject to change without notice. When referring to it please ensure that it is the most up to date version and has not been superseded.

The products are not intended for use in applications where a failure or malfunction may cause loss of life, injury or damage to property. The user must ensure that appropriate safety precautions are taken to prevent or mitigate the consequences of a product failure or malfunction.

The products must not be touched when operating because there is a danger of electrocution or severe burning. Always use protective safety equipment such as appropriate shields for the product and wear safety glasses. Even when disconnected any electric charge remaining in the product must be discharged and allowed to cool before safe handling using protective gloves.

Extended exposure to conditions outside the product ratings may affect reliability leading to premature product failure. Use outside the product ratings is likely to cause permanent damage to the product. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture, a large current to flow or high voltage arcing, resulting in fire or explosion. Appropriate application design and safety precautions should always be followed to protect persons and property.

Product Status & Product Ordering:

We annotate datasheets in the top right hand corner of the front page, to indicate product status if it is not yet fully approved for production. The annotations are as follows:-

Target Information: This is the most tentative form of information and represents a very preliminary specification.

No actual design work on the product has been started.

Preliminary Information: The product design is complete and final characterisation for volume production is in progress.

The datasheet represents the product as it is now understood but details may change.

No Annotation: The product has been approved for production and unless otherwise notified by Dynex any

product ordered will be supplied to the current version of the data sheet prevailing at the

time of our order acknowledgement.

All products and materials are sold and services provided subject to Dynex's conditions of sale, which are available on request.

Any brand names and product names used in this publication are trademarks, registered trademarks or trade names of their respective owners.

HEADQUARTERS OPERATIONS

DYNEX SEMICONDUCTOR LTD

Doddington Road, Lincoln, Lincolnshire, LN6 3LF, United Kingdom

Fax: +44(0)1522 500550 Tel: +44(0)1522 500500 Web: http://www.dynexsemi.com

CUSTOMER SERVICE

DYNEX SEMICONDUCTOR LTD

Doddington Road, Lincoln, Lincolnshire, LN6 3LF, United Kingdom

Tel: +44(0)1522 502753 / 502901 Email: <u>powersolutions@dynexsemi.com</u>

© Dynex Semiconductor Ltd. 2013 Technical Documentation – Not for resale.