

#### Replaces DS5863-1.1

# DIM400GCM33-F000

## **IGBT Chopper Module**

DS5863-2 February 2011 (LN28080)

#### **FEATURES**

- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- Soft Punch Through Silicon
- Isolated AISiC Base With AIN Substrates
- Lead Free Construction

## **APPLICATIONS**

- Choppers
- Motor Controllers
- Power Supplies
- Traction Auxiliaries

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM400GCM33-F000 is a 3300V, n-channel enhancement mode, insulated gate bipolar transistor (IGBT) chopper module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10µs short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

#### **ORDERING INFORMATION**

Order As:

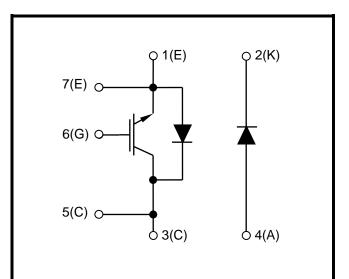
## DIM400GCM33-F000

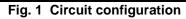
Note: When ordering, please use the complete part number

## **KEY PARAMETERS**

V <sub>CES</sub>		3300V
V <sub>CE(sat)</sub>	* (typ)	2.8V
l <sub>c</sub> `	(max)	400A
I <sub>C(PK)</sub>	(max)	800A

\* Measured at the auxiliary terminals







#### **ABSOLUTE MAXIMUM RATINGS**

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

#### T<sub>case</sub> = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V <sub>CES</sub>	Collector-emitter voltage	V <sub>GE</sub> = 0V	3300	V
$V_{\text{GES}}$	Gate-emitter voltage		±20	V
Ι <sub>C</sub>	Continuous collector current	$T_{case} = 90^{\circ}C$	400	А
I <sub>C(PK)</sub>	Peak collector current	1ms, $T_{case} = 115^{\circ}C$	800	А
P <sub>max</sub>	Max. transistor power dissipation	$T_{case} = 25^{\circ}C, T_j = 150^{\circ}C$	5200	W
l <sup>2</sup> t	Diode I <sup>2</sup> t value (IGBT arm)	V 0 t 10mp T 1250C	80	kA <sup>2</sup> s
11	Diode I <sup>2</sup> t value (Diode arm)	$V_R = 0, t_p = 10ms, T_j = 125^{\circ}C$	80	kA <sup>2</sup> s
V <sub>isol</sub>	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	6000	V
Q <sub>PD</sub>	Partial discharge – per module	IEC1287, $V_1 = 3500V$ , $V_2 = 2600V$ , 50Hz RMS	10	рС

#### THERMAL AND MECHANICAL RATINGS

Internal insulation material:	AIN
Baseplate material:	AlSiC
Creepage distance:	33mm
Clearance:	20mm
CTI (Comparative Tracking Index):	350

Symbol	Parameter	Test Conditions	Min	Тур.	Мах	Units
R <sub>th(j-c)</sub>	Thermal resistance – transistor (per arm)	Continuous dissipation – junction to case	-	-	24	°C/kW
Б	Thermal resistance – diode (IGBT arm)	Continuous dissipation –	-	-	48	°C/kW
R <sub>th(j-c)</sub>	Thermal resistance – diode (Diode arm)	junction to case			48	°C/kW
R <sub>th(c-h)</sub>	Thermal resistance – case to heatsink (per module)Mounting torque 5Nm (with mounting grease)		-	-	8	°C/kW
Tj	Junction temperature	Transistor	-	-	150	°C
		Diode	-	-	125	°C
T <sub>stg</sub>	Storage temperature range	-	-40	-	125	°C
		Mounting – M6	-	-	5	Nm
	Screw torque	Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm

## **ELECTRICAL CHARACTERISTICS**

#### T<sub>case</sub> = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
I <sub>CES</sub>	Collector cut-off current	$V_{GE} = 0V, V_{CE} = V_{CES}$			2	mA
		$V_{GE} = 0V, V_{CE} = V_{CES}, T_{case} = 125^{\circ}C$			30	mA
I <sub>GES</sub>	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$			1	μA
V <sub>GE(TH)</sub>	Gate threshold voltage	$I_{C} = 40 \text{mA}, V_{GE} = V_{CE}$	5.5	6.5	7.0	V
M	Collector-emitter	$V_{GE} = 15V, I_{C} = 400A$		2.8		V
V <sub>CE(sat)</sub>	saturation voltage	V <sub>GE</sub> = 15V, I <sub>C</sub> = 400A, T <sub>j</sub> = 125°C		3.6		V
I <sub>F</sub>	Diode forward current	DC		400		А
I <sub>FM</sub>	Diode maximum forward current	t <sub>p</sub> = 1ms		800		А
	Diode forward voltage (IGBT arm)	— I <sub>F</sub> = 400A		2.9		V
V <sub>F</sub> †	Diode forward voltage (Diode arm)			2.9		V
VF	Diode forward voltage (IGBT arm)	– I <sub>F</sub> = 400A, T <sub>j</sub> = 125°C		3.0		V
	Diode forward voltage (Diode arm)			3.0		V
C <sub>ies</sub>	Input capacitance	$V_{CE}$ = 25V, $V_{GE}$ = 0V, f = 1MHz		72		nF
$Q_{g}$	Gate charge	±15V		10		μC
C <sub>res</sub>	Reverse transfer capacitance	$V_{CE}$ = 25V, $V_{GE}$ = 0V, f = 1MHz				nF
L <sub>M</sub>	Module inductance – per arm			25		nH
R <sub>INT</sub>	Internal resistance – per arm			260		μΩ
SC <sub>Data</sub>	Short circuit current, I <sub>SC</sub>	$\begin{array}{l} T_{j} = 125^{\circ}C, \ V_{CC} = 2500V \\ t_{p} \leq 10\mu s, \ V_{GE} \leq 15V \\ V_{CE\ (max)} = V_{CES} - L^{*}x \ dI/dt \\ IEC\ 60747-9 \end{array}$		1850		A

#### Note:

 $^{\dagger}$  Measured at the power busbars, not the auxiliary terminals  $^{\star}$  L is the circuit inductance + L\_M

## **ELECTRICAL CHARACTERISTICS**

#### T<sub>case</sub> = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t <sub>d(off)</sub>	Turn-off delay time	I <sub>C</sub> = 400A		2100		ns
t <sub>f</sub>	Fall time	$V_{GE} = \pm 15V$		210		ns
E <sub>OFF</sub>	Turn-off energy loss	$V_{CE} = 1800V$		520		mJ
t <sub>d(on)</sub>	Turn-on delay time	$R_{G(ON)} = 5.6\Omega$ $R_{G(OFF)} = 8.2\Omega$		1130		ns
t <sub>r</sub>	Rise time	$C_{ge} = 110 nF$		245		ns
E <sub>ON</sub>	Turn-on energy loss	L <sub>s</sub> ~ 100nH		620		mJ
Q <sub>rr</sub>	Diode reverse recovery charge	I <sub>F</sub> = 400A		160		μC
I <sub>rr</sub>	Diode reverse recovery current	V <sub>CE</sub> = 1800V		330		А
E <sub>rec</sub>	Diode reverse recovery energy	$dI_F/dt = 2000A/\mu s$		150		mJ

#### T<sub>case</sub> = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t <sub>d(off)</sub>	Turn-off delay time	I <sub>C</sub> = 400A		2150		ns
t <sub>f</sub>	Fall time	$V_{GE} = \pm 15V$		220		ns
E <sub>OFF</sub>	Turn-off energy loss	$V_{CE} = 1800V$		600		mJ
t <sub>d(on)</sub>	Turn-on delay time	$R_{G(ON)} = 5.6\Omega$ $R_{G(OFF)} = 8.2\Omega$		1160		ns
t <sub>r</sub>	Rise time	$C_{ge} = 110nF$		285		ns
E <sub>ON</sub>	Turn-on energy loss	L <sub>s</sub> ~ 100nH		870		mJ
Q <sub>rr</sub>	Diode reverse recovery charge	I <sub>F</sub> = 400A		300		μC
I <sub>rr</sub>	Diode reverse recovery current	$V_{CE} = 1800V$		400		А
E <sub>rec</sub>	Diode reverse recovery energy	dI <sub>F</sub> /dt = 2000A/µs		300		mJ

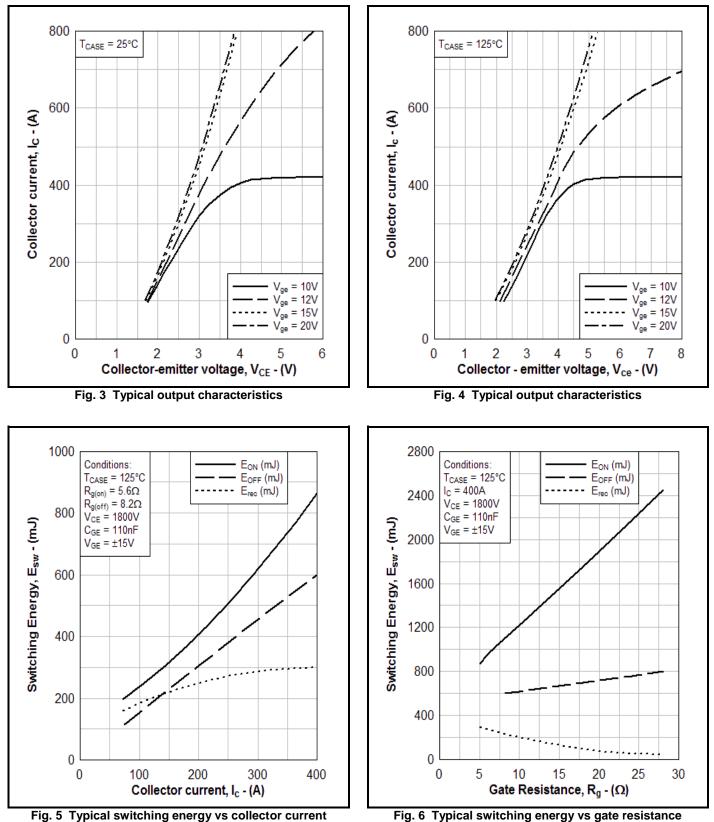


Fig. 6 Typical switching energy vs gate resistance

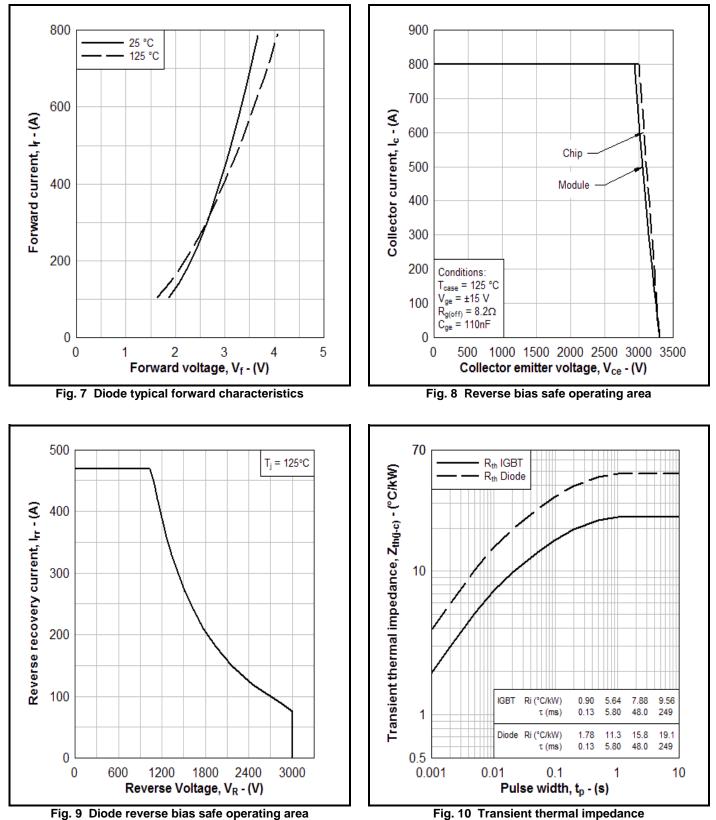
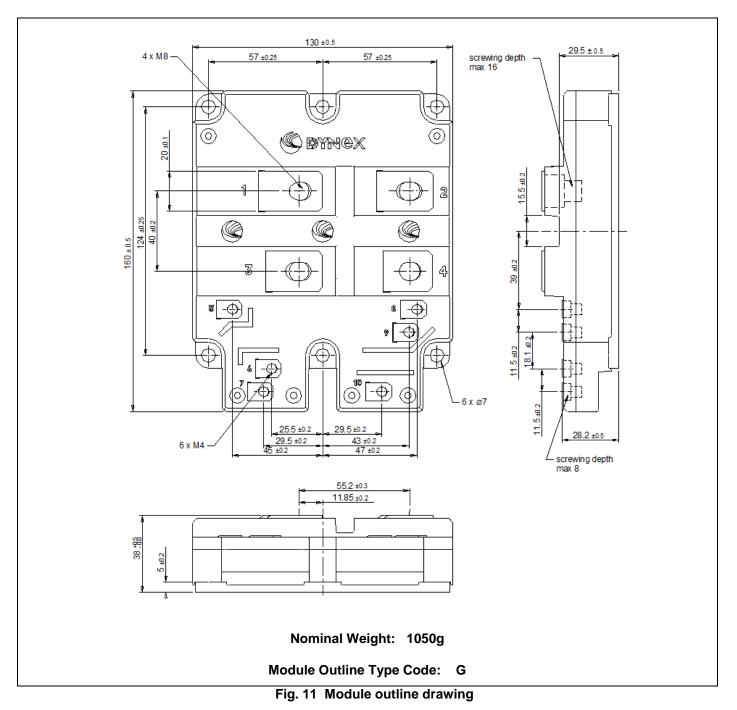


Fig. 10 Transient thermal impedance

#### PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise. **DO NOT SCALE.** 



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