

Replaces DS5447-5

DIM2400ESM17-A000

Single Switch IGBT Module

DS5447-6 June 2012 (LN29603)

FEATURES

- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- Non Punch Through Silicon
- Isolated AISiC Base with AIN Substrates
- Lead Free construction

APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- Traction Drives

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM2400ESM17-A000 is a single switch 1700V, n-channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10µs short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM2400ESM17-A000

Note: When ordering, please use the complete part number

KEY PARAMETERS

V _{CES}		1700V
V _{CE(sat)} *	ʻ (typ)	2.7V
l _c	(max)	2400A
I _{C(PK)}	(max)	4800A

* Measured at the power busbars, not the auxiliary terminals

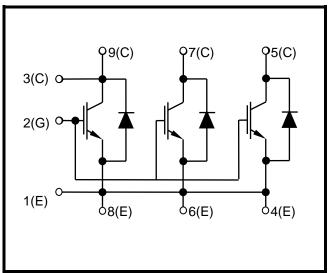


Fig. 1 Circuit configuration



Caution: This device is sensitive to electrostatic discharge. Users should follow ESD handling procedures

ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V _{CES}	Collector-emitter voltage	$V_{GE} = 0V$	1700	V
V _{GES}	Gate-emitter voltage		±20	V
Ι _C	Continuous collector current	$T_{case} = 75^{\circ}C$	2400	А
I _{C(PK)}	Peak collector current	1ms, $T_{case} = 110^{\circ}C$	4800	А
P _{max}	Max. transistor power dissipation	$T_{case} = 25^{\circ}C, T_j = 150^{\circ}C$	20800	W
l ² t	Diode I ² t value	$V_{R} = 0, t_{p} = 10ms, T_{j} = 125^{\circ}C$	1080	kA ² s
V _{isol}	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	4000	V
Q _{PD}	Partial discharge – per module	IEC1287, $V_1 = 1800V$, $V_2 = 1300V$, 50Hz RMS	10	рС

THERMAL AND MECHANICAL RATINGS

Internal insulation material:	AIN
Baseplate material:	AlSiC
Creepage distance:	33mm
Clearance:	20mm
CTI (Comparative Tracking Index):	600

Symbol	Parameter	Test Conditions	Min	Тур.	Мах	Units
R _{th(j-c)}	Thermal resistance – transistor	Continuous dissipation - junction to case	-	-	6	°C/kW
R _{th(j-c)}	Thermal resistance – diode	Continuous dissipation - junction to case	-	-	13	°C/kW
R _{th(c-h)}	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	6	°C/kW
T _j	Junction temperature	Transistor	-	-	150	°C
		Diode	-	-	125	°C
T _{stg}	Storage temperature range	-	-40	-	125	°C
		Mounting – M6	-	-	5	Nm
	Screw torque	Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm

ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Тур	Мах	Units
	0	$V_{GE} = 0V, V_{CE} = V_{CES}$			3	mA
I _{CES}	Collector cut-off current	$V_{GE} = 0V, V_{CE} = V_{CES}, T_{case} = 125^{\circ}C$			75	mA
I _{GES}	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$			12	μA
$V_{\text{GE(TH)}}$	Gate threshold voltage	I_{C} = 120mA, V_{GE} = V_{CE}	4.5	5.5	6.5	V
v t	Collector-emitter	$V_{GE} = 15V, I_C = 2400A$		2.7	3.2	V
V _{CE(sat)} †	saturation voltage	$V_{GE} = 15V, I_C = 2400A, T_j = 125^{\circ}C$		3.4	4.0	V
I _F	Diode forward current	DC			2400	А
I _{FM}	Diode maximum forward current	t _p = 1ms			4800	А
v t	Diada famuard valtage	I _F = 2400A		2.2	2.5	V
VF	V _F [†] Diode forward voltage	I _F = 2400A, T _j = 125°C		2.3	2.6	V
C _{ies}	Input capacitance	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz		180		nF
Qg	Gate charge	±15V		27		μC
C _{res}	Reverse transfer capacitance	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz				nF
L _M	Module inductance			10		nH
R _{INT}	Internal transistor resistance			90		μΩ
SC _{Data}	Short circuit current, I _{SC}	$\begin{split} T_{j} &= 125^{\circ}C, \ V_{CC} &= 1000V \\ t_{p} &\leq 10 \mu s, \ V_{GE} &\leq 15V \\ V_{CE \ (max)} &= V_{CES} - L^{*} x \ dl/dt \\ IEC \ 60747-9 \end{split}$		9600		A

Note:

 † Measured at the power busbars, not the auxiliary terminals $^{\cdot}$ L is the circuit inductance + L_M

ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time			2000		ns
t _f	Fall time	$I_{C} = 2400A$ $V_{GF} = \pm 15V$		200		ns
E _{OFF}	Turn-off energy loss	$V_{CE} = 900V$		900		mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = 1.0\Omega$		800		ns
t _r	Rise time	$R_{G(OFF)} = 1.0\Omega$ $L_{S} \sim 50 \text{nH}$		300		ns
E _{ON}	Turn-on energy loss			475		mJ
Q _{rr}	Diode reverse recovery charge	I _F = 2400A		450		μC
I _{rr}	Diode reverse recovery current	$V_{CE} = 900V$		1200		А
E _{rec}	Diode reverse recovery energy	dI _F /dt = 8500A/µs		300		mJ

T_{case} = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time			2300		ns
t _f	Fall time	I _C = 2400A V _{GE} = ±15V		250		ns
E _{OFF}	Turn-off energy loss	$V_{GE} = \pm 13V$ $V_{CE} = 900V$		1200		mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = 1.0\Omega$		900		ns
t _r	Rise time	R _{G(OFF)} = 1.0Ω L _S ~ 50nH		300		ns
E _{ON}	Turn-on energy loss			750		mJ
Q _{rr}	Diode reverse recovery charge	I _F = 2400A		750		μC
I _{rr}	Diode reverse recovery current	$V_{CE} = 900V$		1400		А
E _{rec}	Diode reverse recovery energy	dI _F /dt = 8000A/µs		600		mJ

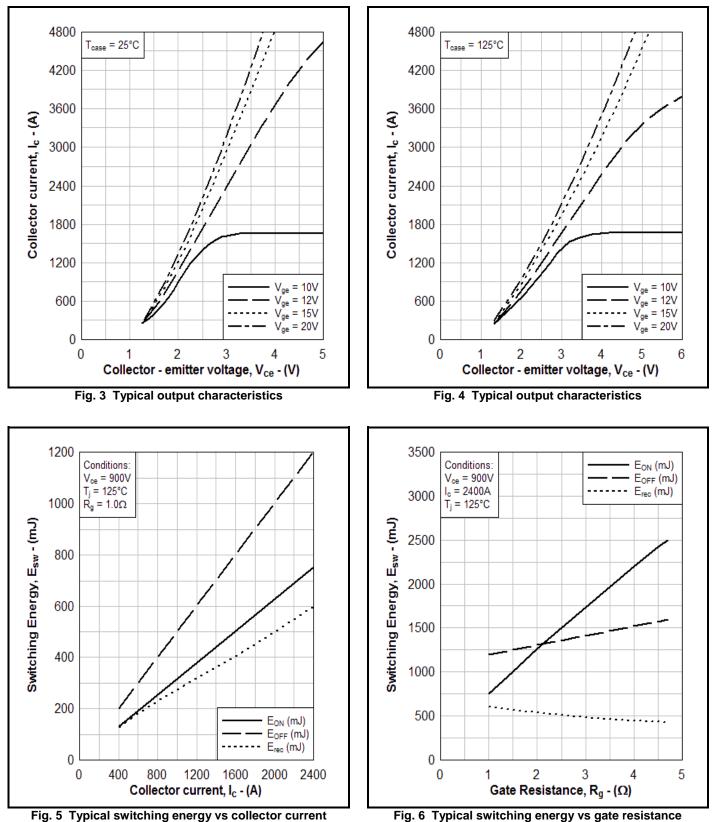


Fig. 6 Typical switching energy vs gate resistance

Caution: This device is sensitive to electrostatic discharge. Users should follow ESD handling procedures

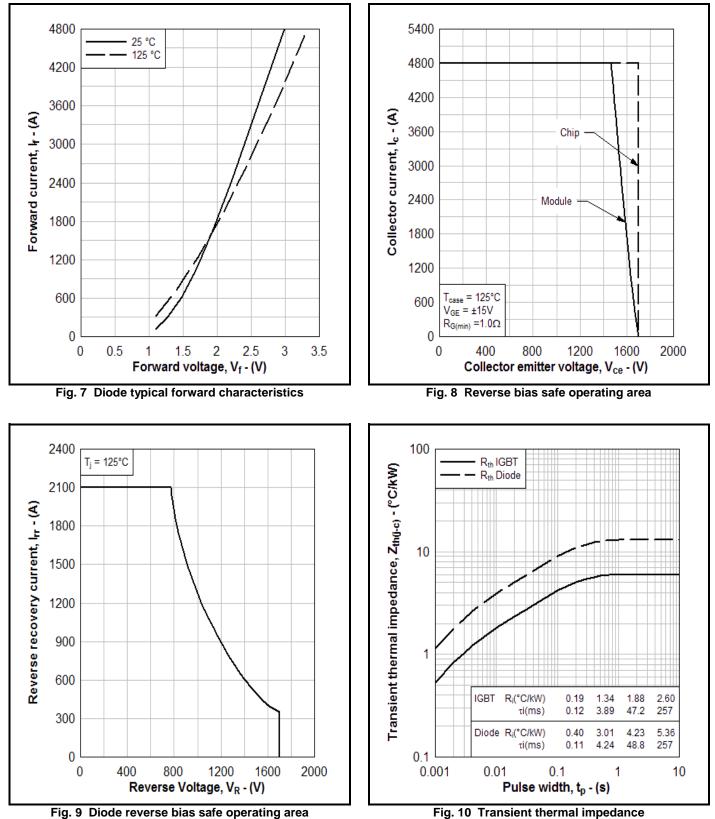
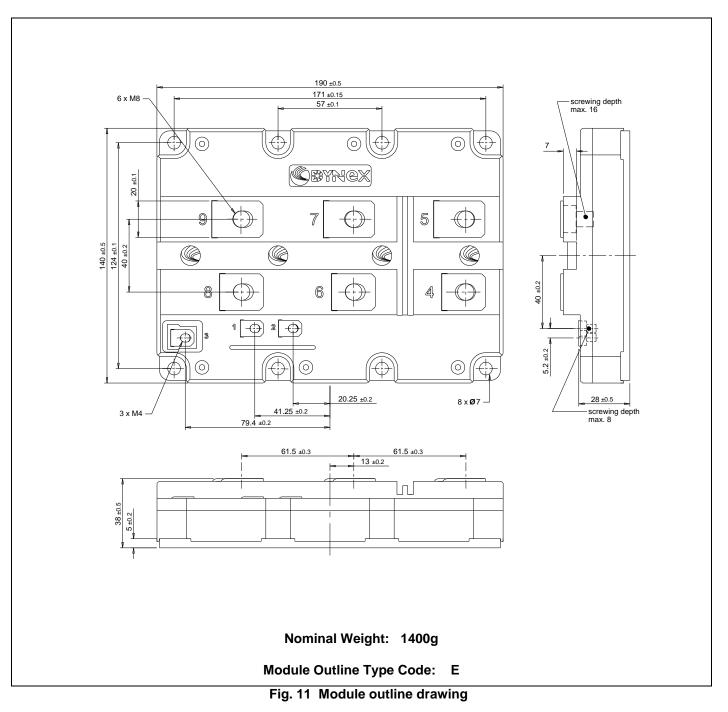


Fig. 10 Transient thermal impedance

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PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise. **DO NOT SCALE.**



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HEADQUARTERS OPERATIONS

DYNEX SEMICONDUCTOR LTD

Doddington Road, Lincoln, Lincolnshire, LN6 3LF, United Kingdom Fax: +44(0)1522 500550 Tel: +44(0)1522 500500 Web: <u>http://www.dynexsemi.com</u>

CUSTOMER SERVICE

DYNEX SEMICONDUCTOR LTD

Doddington Road, Lincoln, Lincolnshire, LN6 3LF, United Kingdom

 Tel:
 +44(0)1522 502753 / 502901

 Email:
 powersolutions@dynexsemi.com

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