

# DMOS+ Gen4 DMOS

# DIM1500ESM33-PR500

# **Single Switch IGBT Module**

DS6321-1 March 2020 (LN39774)

#### **FEATURES**

- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- High Current Density Enhanced DMOS
- Isolated AISiC Base With AIN Substrates
- Low Switching Loss Device

#### **APPLICATIONS**

- High Reliability Inverters
- Motor Controllers
- Smart Grid
- Traction Drives

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM1500ESM33-PR500 is a single switch 3300V, n-channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10µs short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

## ORDERING INFORMATION

Order As:

## DIM1500ESM33-PR500

Note: When ordering, please use the complete part number

## **KEY PARAMETERS**

V <sub>CES</sub>		3300V
V <sub>CE(sat)</sub>	* (typ)	2.4V
l <sub>c</sub>	(max)	1500A
I <sub>C(PK)</sub>	(max)	3000A

<sup>\*</sup> Measured at the auxiliary terminals

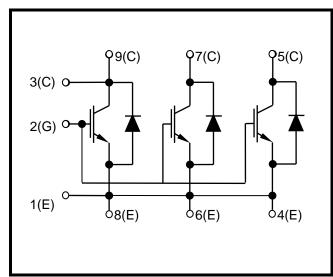


Fig. 1 Circuit configuration



Fig. 2 Package

## **ABSOLUTE MAXIMUM RATINGS**

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T<sub>case</sub> = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
Vces	Collector-emitter voltage	V <sub>GE</sub> = 0V	3300	V
V <sub>GES</sub>	Gate-emitter voltage		±20	V
Ic	Continuous collector current	T <sub>case</sub> = 110°C	1500	Α
IC(PK)	Peak collector current	1ms, T <sub>case</sub> = 140°C	3000	Α
P <sub>max</sub>	Max. transistor power dissipation	$T_{case} = 25^{\circ}C, T_{j} = 150^{\circ}C$	15.6	kW
l²t	Diode I <sup>2</sup> t value	$V_R = 0$ , $t_p = 10$ ms, $T_j = 150$ °C	720	kA <sup>2</sup> s
Visol	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	6000	V
$Q_{PD}$	Partial discharge – per module	IEC1287, V <sub>1</sub> = 3500V, V <sub>2</sub> = 2600V, 50Hz RMS	10	рС

## THERMAL AND MECHANICAL RATINGS

Internal insulation material:

Baseplate material:

Creepage distance:

Clearance:

CTI (Comparative Tracking Index):

AIN

AISiC

33mm

20mm

>600

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
R <sub>th(j-c)</sub>	Thermal resistance – transistor	Continuous dissipation - junction to case	-	-	8	°C/kW
R <sub>th(j-c)</sub>	Thermal resistance – diode	Continuous dissipation - junction to case	-	-	16	°C/kW
R <sub>th(c-h)</sub>	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	6	°C/kW
Tj	Junction temperature	Transistor	-	-	150	°C
		Diode	-	-	150	°C
T <sub>stg</sub>	Storage temperature range	-	-40	-	150	°C
		Mounting – M6	-	-	5	Nm
	Screw torque	Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm

## **ELECTRICAL CHARACTERISTICS**

 $T_{case}$  = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
		V <sub>GE</sub> = 0V, V <sub>CE</sub> = V <sub>CES</sub>			1	mA
Ices	Collector cut-off current	V <sub>GE</sub> = 0V, V <sub>CE</sub> = V <sub>CES</sub> , T <sub>case</sub> = 125°C			90	mA
		V <sub>GE</sub> = 0V, V <sub>CE</sub> = V <sub>CES</sub> , T <sub>case</sub> = 150°C			150	mA
I <sub>GES</sub>	Gate leakage current	V <sub>GE</sub> = ± 20V, V <sub>CE</sub> = 0V			1	μA
V <sub>GE(TH)</sub>	Gate threshold voltage	Ic = 120mA, V <sub>GE</sub> = V <sub>CE</sub>	5.50	6.10	7.00	V
		V <sub>GE</sub> = 15V, I <sub>C</sub> = 1500A		2.40	2.90	V
V <sub>CE(sat)</sub>	Collector-emitter saturation voltage	V <sub>GE</sub> = 15V, I <sub>C</sub> = 1500A, T <sub>j</sub> = 125°C		2.95	3.40	V
	3	V <sub>GE</sub> = 15V, I <sub>C</sub> = 1500A, T <sub>j</sub> = 150°C		3.10	3.60	V
l <sub>F</sub>	Diode forward current	DC		1500		Α
I <sub>FM</sub>	Diode maximum forward current	$t_p = 1 ms$		3000		Α
	Diode forward voltage	I <sub>F</sub> = 1500A		2.10	2.60	V
$V_{F}$		I <sub>F</sub> = 1500A, T <sub>j</sub> = 125°C		2.25	2.70	V
		I <sub>F</sub> = 1500A, T <sub>j</sub> = 150°C		2.25	2.70	V
Cies	Input capacitance	V <sub>CE</sub> = 25V, V <sub>GE</sub> = 0V, f = 1MHz		260		nF
Qg	Gate charge	±15V Including external C <sub>ge</sub>		25		μC
Cres	Reverse transfer capacitance	V <sub>CE</sub> = 25V, V <sub>GE</sub> = 0V, f = 1MHz		6		nF
L <sub>M</sub>	Module inductance			10		nΗ
RINT	Internal transistor resistance			110		μΩ
SC <sub>Data</sub>	Short circuit current, Isc	$T_{j} = 150^{\circ}C$ , $V_{CC} = 2500V$ $t_{p} \le 10\mu s$ , $V_{GE} \le 15V$ $V_{CE (max)} = V_{CES} - L^{*} x dI/dt$ IEC 60747-9		5800		А

Note:  $^{\star}$  L is the circuit inductance +  $L_{\text{M}}$ 

## **ELECTRICAL CHARACTERISTICS**

 $T_{case} = 25$ °C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t <sub>d(off)</sub>	Turn-off delay time	I <sub>C</sub> = 1500A		2100		ns
<b>t</b> f	Fall time	$V_{GE} = \pm 15V$		540		ns
Eoff	Turn-off energy loss	$V_{CE} = 1800V$		2400		mJ
t <sub>d(on)</sub>	Turn-on delay time	$R_{g(ON)} = 1.0\Omega$ $R_{g(OFF)} = 1.5\Omega$		750		ns
t <sub>r</sub>	Rise time	$C_{GE} = 330nF$		340		ns
E <sub>ON</sub>	Turn-on energy loss	Ls ~ 150nH		1450		mJ
Qrr	Diode reverse recovery charge	I <sub>F</sub> = 1500A		1150		μC
Irr	Diode reverse recovery current	V <sub>CE</sub> = 1800V		1250		Α
Erec	Diode reverse recovery energy	dI <sub>F</sub> /dt = 4800A/μs		1550		mJ

# T<sub>case</sub> = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t <sub>d(off)</sub>	Turn-off delay time	I <sub>C</sub> = 1500A		2250		ns
<b>t</b> f	Fall time	$V_{GE} = \pm 15V$		570		ns
E <sub>OFF</sub>	Turn-off energy loss	$V_{CE} = 1800V$		2950		mJ
t <sub>d(on)</sub>	Turn-on delay time	$\begin{array}{c} R_{g(\text{ON})} = 1.0\Omega \\ R_{g(\text{OFF})} = 1.5\Omega \\ C_{\text{GE}} = 330 \text{nF} \\ L_{\text{S}} \sim 150 \text{nH} \end{array}$		730		ns
tr	Rise time			350		ns
Eon	Turn-on energy loss			1900		mJ
Qrr	Diode reverse recovery charge	I <sub>F</sub> = 1500A V <sub>CE</sub> = 1800V		1800		μC
Irr	Diode reverse recovery current			1420		Α
Erec	Diode reverse recovery energy	dI <sub>F</sub> /dt = 4800A/μs		2450		mJ

## $T_{case} = 150$ °C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t <sub>d(off)</sub>	Turn-off delay time	I <sub>C</sub> = 1500A		2290		ns
t <sub>f</sub>	Fall time	$V_{GE} = \pm 15V$		580		ns
Eoff	Turn-off energy loss	$V_{CE} = 1800V$		3200		mJ
t <sub>d(on)</sub>	Turn-on delay time	$R_{g(ON)} = 1.0\Omega$ $R_{g(OFF)} = 1.5\Omega$		730		ns
t <sub>r</sub>	Rise time	C <sub>GE</sub> = 330nF		360		ns
Eon	Turn-on energy loss	Ls ~ 150nH		2100		mJ
Qrr	Diode reverse recovery charge	I <sub>F</sub> = 1500A		1980		μC
Irr	Diode reverse recovery current	V <sub>CE</sub> = 1800V		1450		Α
Erec	Diode reverse recovery energy	dl <sub>F</sub> /dt = 5000A/µs		2720		mJ

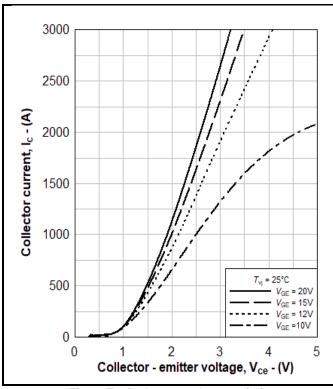


Fig. 3 Typical output characteristics

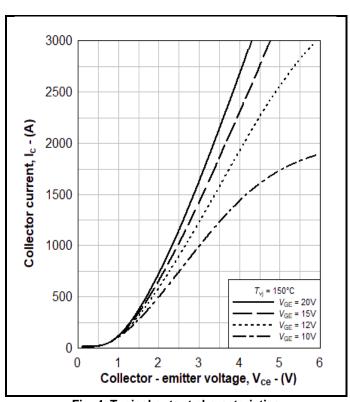


Fig. 4 Typical output characteristics

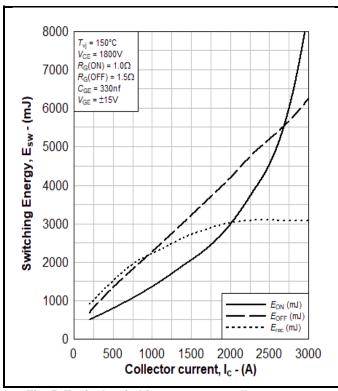


Fig. 5 Typical switching energy vs collector current

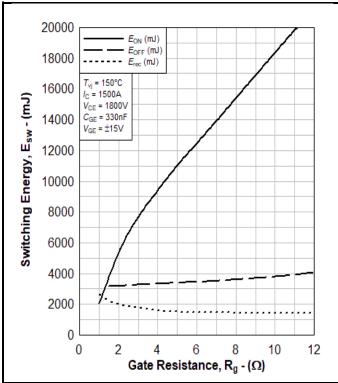


Fig. 6 Typical switching energy vs gate resistance

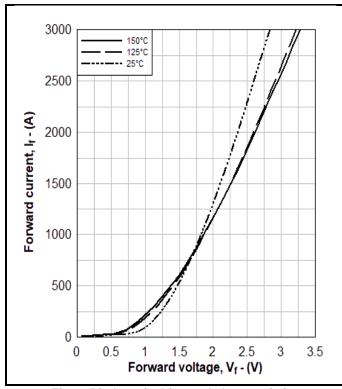


Fig. 7 Diode typical forward characteristics

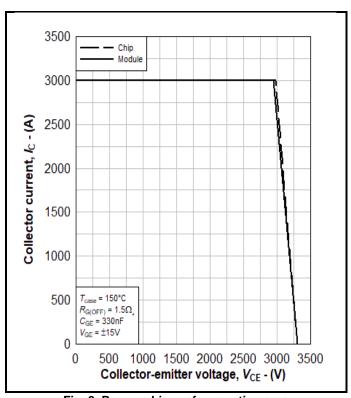


Fig. 8 Reverse bias safe operating area

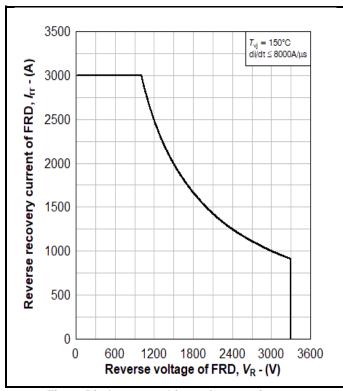


Fig. 9 Diode reverse bias safe operating area

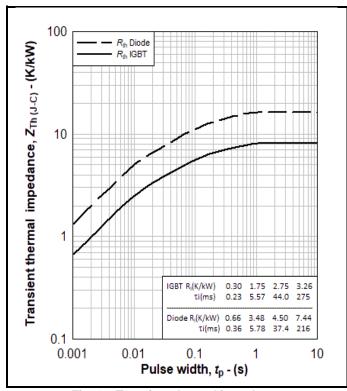


Fig. 10 Transient thermal impedance

## **PACKAGE DETAILS**

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise.

## DO NOT SCALE.

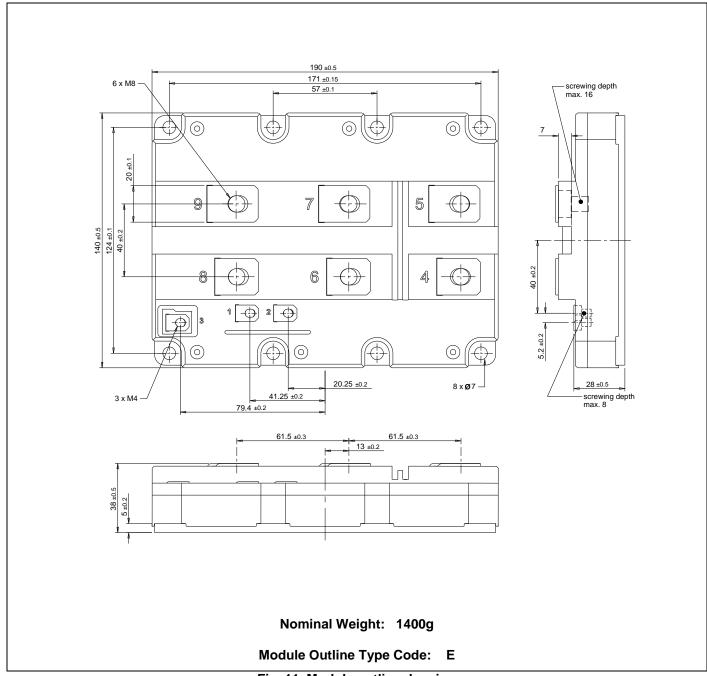


Fig. 11 Module outline drawing

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## DYNEX SEMICONDUCTOR LTD

Doddington Road, Lincoln, Lincolnshire, LN6 3LF, United Kingdom

Tel: +44(0)1522 500500 Web: http://www.dynexsemi.com

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## DYNEX SEMICONDUCTOR LTD

Doddington Road, Lincoln, Lincolnshire, LN6 3LF, United Kingdom

Tel: +44(0)1522 502753 / 502901 Email: powersolutions@dynexsemi.com

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