



DIM125PHM33-TL000

Half Bridge IGBT Module

DS6306-3 February 2023 (LN42405)

FEATURES

Replaces DS6306-2

- Low V_{CE(sat)} Device
- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- High Current Density Enhanced DMOS SPT
- Isolated AISiC Base with AIN Substrates
- Lead Free Construction

APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- **Traction Auxiliaries**
- Choppers

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM125PHM33-TL000 is a Low $V_{\text{CE(sat)}}$ half bridge 3300V, soft punch through n-channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA). This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM125PHM33-TL000

Note: When ordering, please use the complete part number

KEY PARAMETERS

V_{CES}		3300\
V _{CE(sat)}	* (typ)	2.0V
l _c	(max)	125A
I _{C(PK)}	(max)	250A

^{*} Measured at the auxiliary terminals

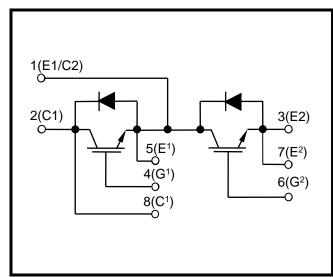


Fig. 1 Circuit configuration

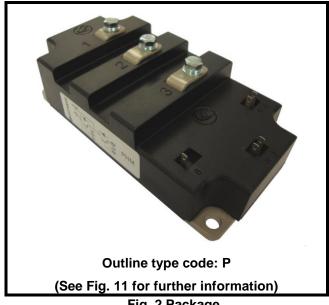


Fig. 2 Package

ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
Vces	Collector-emitter voltage	V _{GE} = 0V	3300	V
V _{GES}	Gate-emitter voltage		±20	V
Ic	Continuous collector current	T _{case} = 115°C	125	Α
I _{C(PK)}	Peak collector current	1ms, T _{case} = 140°C	250	Α
P _{max}	Max. transistor power dissipation	$T_{case} = 25^{\circ}C, T_{j} = 150^{\circ}C$	1.3	kW
l²t	Diode I ² t value	$V_R = 0$, $t_p = 10$ ms, $T_j = 125$ °C	5	kA ² s
Visol	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	6000	V
Q _{PD}	Partial discharge – per module	IEC1287, V ₁ = 3500V, V ₂ = 2600V, 50Hz RMS	10	рC

THERMAL AND MECHANICAL RATINGS

Internal insulation material:

Baseplate material:

Creepage distance:

Clearance:

CTI (Comparative Tracking Index):

AIN

AISiC

33mm

20mm

>600

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
R _{th(j-c)}	Thermal resistance – transistor	Continuous dissipation - junction to case	-	-	96	°C/kW
R _{th(j-c)}	Thermal resistance – Diode	Continuous dissipation - junction to case	-	-	192	°C/kW
R _{th(c-h)}	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	16	°C/kW
т.	Junction temperature	Transistor	-	-	150	°C
Tj		Diode	-	-	150	°C
T _{stg}	Storage temperature range	-	-40	-	125	°C
	Screw torque	Mounting – M6	-	-	5	Nm
		Electrical connections – M5	-	-	4	Nm

ELECTRICAL CHARACTERISTICS

 T_{case} = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Ices	Collector cut-off current	V _{GE} = 0V, V _{CE} = V _{CES}			1	mA
		V _{GE} = 0V, V _{CE} = V _{CES} , T _{case} = 125°C			7.5	mA
		V _{GE} = 0V, V _{CE} = V _{CES} , T _{case} = 150°C			12.5	mA
I _{GES}	Gate leakage current	V _{GE} = ± 20V, V _{CE} = 0V			1	μΑ
V _{GE(TH)}	Gate threshold voltage	Ic = 10mA, V _{GE} = V _{CE}		5.7		V
		V _{GE} = 15V, I _C = 125A		2.0		V
V _{CE(sat)}	Collector-emitter saturation voltage	V _{GE} = 15V, I _C = 125A, T _j = 125°C		2.6		V
	g-	V _{GE} = 15V, I _C = 125A, T _j = 150°C		2.8		V
lF	Diode forward current	DC		125		Α
I _{FM}	Diode maximum forward current	$t_p = 1 ms$		250		А
	Diode forward voltage	I _F = 125A		2.4		V
V _F		I _F = 125A, T _j = 125°C		2.5		V
		I _F = 125A, T _j = 150°C		2.4		V
Cies	Input capacitance	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz		22.5		nF
Qg	Gate charge	±15V Including external C _{ge}		2.5		μC
Cres	Reverse transfer capacitance	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz		0.5		nF
L _M	Module inductance			40		nΗ
RINT	Internal transistor resistance			500		μΩ
SC _{Data}	Short circuit current, I _{SC}	$\begin{split} T_{j} &= 150^{\circ}\text{C}, \ V_{CC} = 2500\text{V} \\ t_{p} &\leq 10\mu\text{s}, \ V_{GE} \leq 15\text{V} \\ V_{CE \ (max)} &= V_{CES} - L^{*} \ x \ dI/dt \\ IEC \ 60747-9 \end{split}$		480		А

Note: * L is the circuit inductance + L_{M}

ELECTRICAL CHARACTERISTICS

 $T_{case} = 25$ °C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time	I _C = 125A		2700		ns
t _f	Fall time	$V_{GE} = \pm 15V$		610		ns
Eoff	Turn-off energy loss	V _{CE} = 1800V		313		mJ
t _{d(on)}	Turn-on delay time	$R_{g(ON)} = 20\Omega$ $R_{g(OFF)} = 20\Omega$		960		ns
t _r	Rise time	$C_{GE} = 27nF$		430		ns
Eon	Turn-on energy loss	Ls ~ 150nH		200		mJ
Qrr	Diode reverse recovery charge	I _F = 125A V _{CE} = 1800V dI _F /dt = 350A/μs		70		μC
Irr	Diode reverse recovery current			75		Α
Erec	Diode reverse recovery energy			85		mJ

T_{case} = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time	I _C = 125A		2750		ns
t f	Fall time	$V_{GE} = \pm 15V$		590		ns
E _{OFF}	Turn-off energy loss	$V_{CE} = 1800V$		340		mJ
t _{d(on)}	Turn-on delay time	$R_{g(ON)} = 20\Omega$ $R_{g(OFF)} = 20\Omega$		1000		ns
t_r	Rise time	C _{GE} = 27nF L _S ~ 150nH		460		ns
Eon	Turn-on energy loss			260		mJ
Qrr	Diode reverse recovery charge	I _F = 125A V _{CE} = 1800V dI _F /dt = 350A/μs		115		μC
Irr	Diode reverse recovery current			97		Α
Erec	Diode reverse recovery energy			140		mJ

T_{case} = 150°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time	I _C = 125A		2760		ns
t _f	Fall time	$V_{GE} = \pm 15V$		590		ns
Eoff	Turn-off energy loss	$V_{CE} = 1800V$		366		mJ
t _{d(on)}	Turn-on delay time	$\begin{array}{l} R_{g(\text{ON})} = 20\Omega \\ R_{g(\text{OFF})} = 20\Omega \\ C_{\text{GE}} = 27 \text{nF} \\ L_{\text{S}} \sim 150 \text{nH} \end{array}$		1040		ns
t _r	Rise time			460		ns
Eon	Turn-on energy loss			283		mJ
Qrr	Diode reverse recovery charge	I _F = 125A V _{CE} = 1800V dI _F /dt = 350A/µs		135		μC
Irr	Diode reverse recovery current			100		Α
Erec	Diode reverse recovery energy			165		mJ

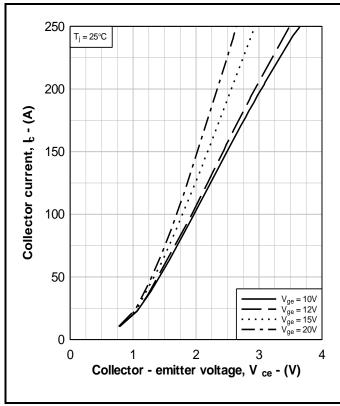


Fig. 3 Typical output characteristics

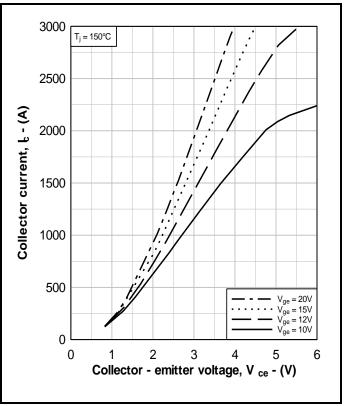


Fig. 4 Typical output characteristics

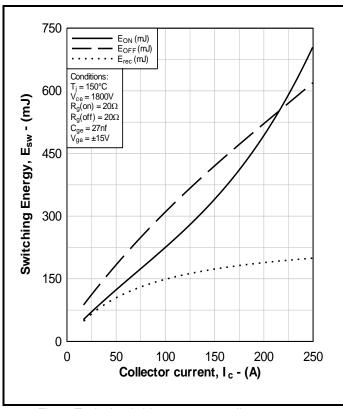


Fig. 5 Typical switching energy vs collector current

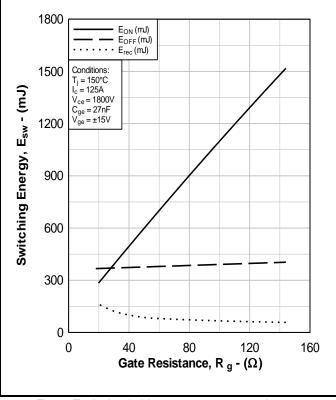


Fig. 6 Typical switching energy vs gate resistance

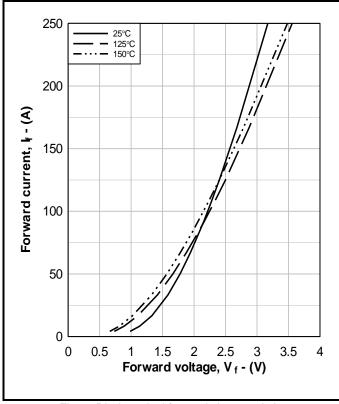


Fig. 7 Diode typical forward characteristics

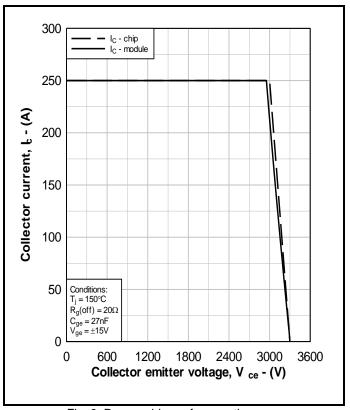


Fig. 8 Reverse bias safe operating area

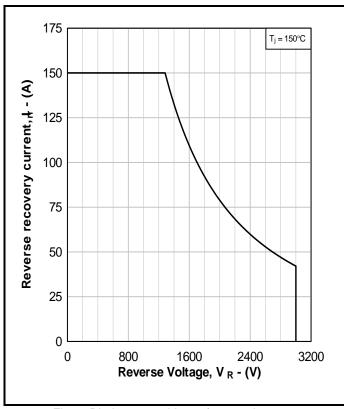


Fig. 9 Diode reverse bias safe operating area

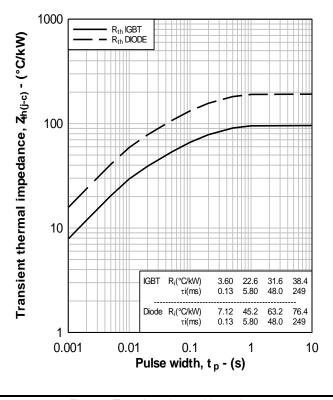


Fig. 10 Transient thermal impedance

PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise.

DO NOT SCALE.

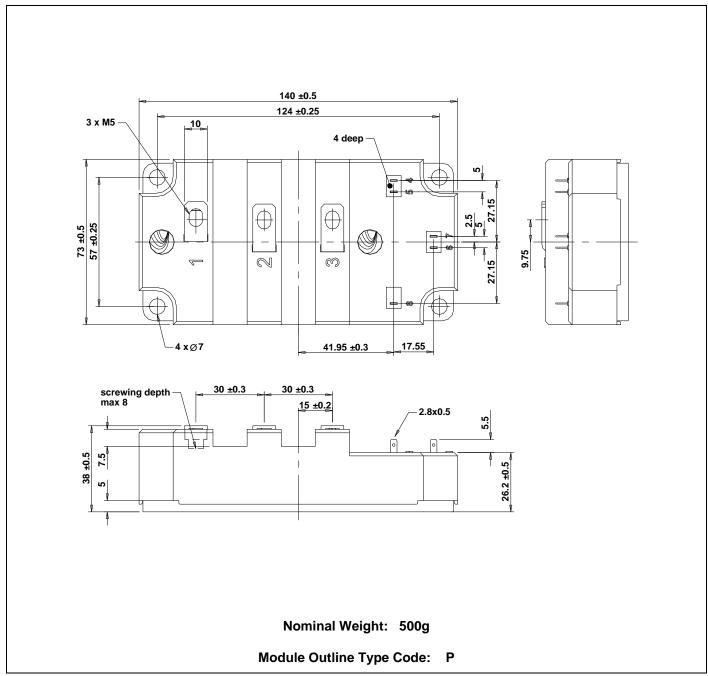


Fig. 11 Module outline drawing

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