



DIM1200ASM45-TF000

Replaces DS6248-2

Single Switch IGBT Module

DS6248-3 August 2023 (LN42736)

FEATURES

- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- High Current Density Enhanced DMOS SPT
- Isolated AISiC Base With AIN Substrates
- Lead Free Construction

APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- Traction Drives
- Choppers

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM1200ASM45-TF000 is a single switch 4500V, n-channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10µs short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM1200ASM45-TF000

Note: When ordering, please use the complete part number

KEY PARAMETERS

VCES		4500V
V _{CE(sat)}	* (typ)	3.5V
lc	(max)	1200A
I _{C(PK)}	(max)	2400A

* Measured at the auxiliary terminals

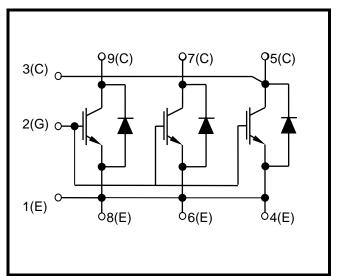


Fig. 1 Circuit configuration



ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
Vces	Collector-emitter voltage	V _{GE} = 0V	4500	V
V _{GES}	Gate-emitter voltage		±20	V
lc	Continuous collector current	T _{case} = 80°C	1200	А
I _{C(PK)}	Peak collector current	1ms, $T_{case} = 110^{\circ}C$	2400	А
P _{max}	Max. transistor power dissipation	$T_{case} = 25^{\circ}C, T_j = 125^{\circ}C$	12.5	kW
l²t	Diode I ² t value	$V_R = 0, t_p = 10ms, T_j = 125^{\circ}C$	460	kA ² s
Visol	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	7.4	kV
Q _{PD}	Partial discharge – per module	IEC1287, $V_1 = 4800V$, $V_2 = 3500V$, 50Hz RMS	10	рС

THERMAL AND MECHANICAL RATINGS

Internal insulation material:	AIN
Baseplate material:	AISiC
Creepage distance:	56mm
Clearance:	26mm
CTI (Comparative Tracking Index):	>600

Symbol	Parameter	Test Conditions	Min	Тур.	Мах	Units
R _{th(j-c)}	Thermal resistance – transistor	Continuous dissipation - junction to case	-	-	8	°C/kW
Rth(j-c)	Thermal resistance – diode	Continuous dissipation - junction to case	-	-	16	°C/kW
Rth(c-h)	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	6	°C/kW
Tj	Junction temperature	Transistor	-	-	125	°C
		Diode	-	-	125	°C
T _{stg}	Storage temperature range	-	-40	-	125	°C
		Mounting – M6	-	-	5	Nm
	Screw torque	Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm

ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Тур	Мах	Units
	0 H + + + # + +	$V_{GE} = 0V, V_{CE} = V_{CES}$			4	mA
ICES	Collector cut-off current	$V_{GE} = 0V, V_{CE} = V_{CES}, T_{case} = 125^{\circ}C$			90	mA
Iges	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$			1	μA
V _{GE(TH)}	Gate threshold voltage	I_{C} = 120mA, V_{GE} = V_{CE}		5.8		V
M	Collector-emitter	V _{GE} = 15V, I _C = 1200A		3.5		V
V _{CE(sat)}	saturation voltage	V _{GE} = 15V, I _C = 1200A, T _j = 125°C		4.4		V
lF	Diode forward current	DC		1200		А
Іғм	Diode maximum forward current	t _p = 1ms		2400		А
VF	Diode forward voltage	IF = 1200A		2.9		V
		I _F = 1200A, T _j = 125°C		3.1		V
Cies	Input capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		150		nF
Qg	Gate charge	±15V		17		μC
Cres	Reverse transfer capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		12		nF
Lм	Module inductance			10		nH
RINT	Internal transistor resistance			90		μΩ
SC _{Data}	Short circuit current, I _{SC}	$\begin{split} T_{j} &= 125^{\circ}C, \ V_{CC} &= 3400V \\ t_{p} &\leq 10 \mu s, \ V_{GE} &\leq 15V \\ V_{CE \ (max)} &= V_{CES} - L^{*} \ x \ dI/dt \\ IEC \ 60747-9 \end{split}$		4800		A

Note:

 $^{\ast}\,$ L is the circuit inductance + L_{M}

Caution: This device is sensitive to electrostatic discharge. Users should follow ESD handling procedures

ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time			2350		ns
t _f	Fall time	I _C = 1200A V _{GE} = ±15V		400		ns
Eoff	Turn-off energy loss	$V_{CE} = 2800V$		2000		mJ
t _{d(on)}	Turn-on delay time	$\begin{array}{l} R_{G(ON)} = 2.4\Omega \\ R_{G(OFF)} = 2.7\Omega \end{array}$		850		ns
tr	Rise time	$C_{ge} = 220 nF$		290		ns
Eon	Turn-on energy loss	Ls ~ 165nH		5400		mJ
Qrr	Diode reverse recovery charge	IF = 1200A		1050		μC
Irr	Diode reverse recovery current	$V_{CE} = 2800V$		1100		А
E _{rec}	Diode reverse recovery energy	dl _F /dt = 3000A/µs		1470		mJ

T_{case} = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time			2450		ns
t _f	Fall time	$I_{C} = 1200A$ $V_{GE} = \pm 15V$		350		ns
EOFF	Turn-off energy loss	$V_{CE} = 2800V$		2250		mJ
t _{d(on)}	Turn-on delay time	$\begin{array}{l} R_{G(ON)} = 2.4\Omega \\ R_{G(OFF)} = 2.7\Omega \end{array}$		850		ns
tr	Rise time	$C_{ge} = 220 nF$		300		ns
Eon	Turn-on energy loss	L _S ~ 165nH		6700		mJ
Qrr	Diode reverse recovery charge	I _F = 1200A		1900		μC
Irr	Diode reverse recovery current	$V_{CE} = 2800V$		1175		А
Erec	Diode reverse recovery energy	dl⊧/dt = 3000A/µs		3100		mJ

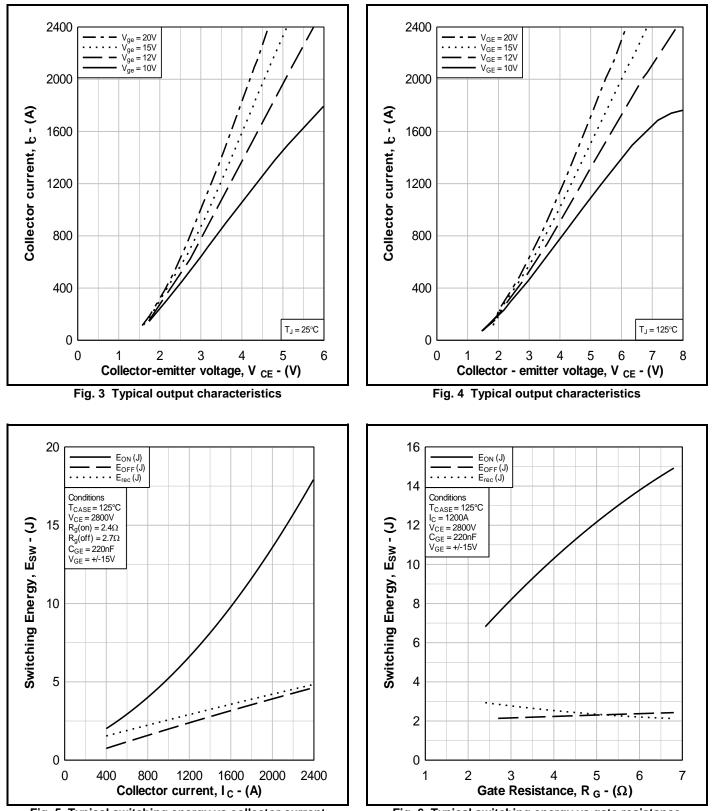


Fig. 5 Typical switching energy vs collector current Fig. 6 Typical switching energy vs gate resistance

Caution: This device is sensitive to electrostatic discharge. Users should follow ESD handling procedures

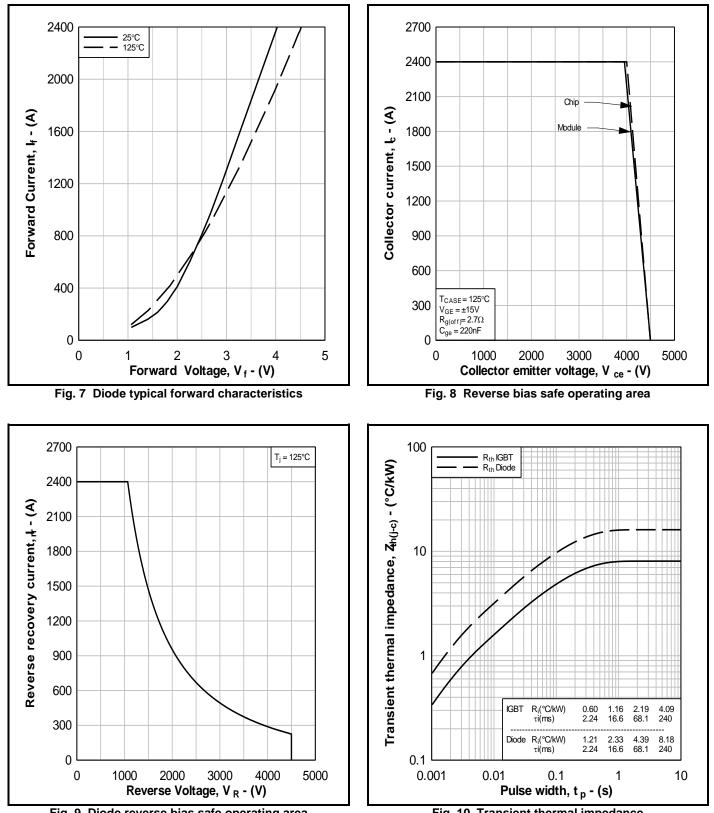


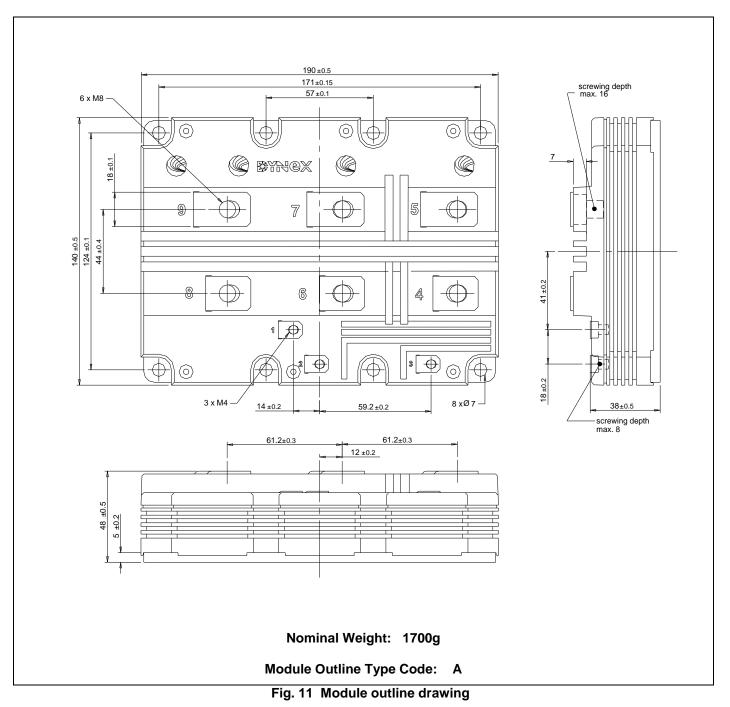
Fig. 9 Diode reverse bias safe operating area

Fig. 10 Transient thermal impedance

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PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise. **DO NOT SCALE.**



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