DCR690G52

Phase Control Thyristor

Replaces DS5830-4 DS5830-5 September 2024 (LN43593)

FEATURES

- Double Side Cooling
- High Surge Capability

APPLICATIONS

- High Power Drives
- High Voltage Power Supplies
- Static Switches

KEY PARAMETERS

V_{DRM}	**5200V**
$I_{T(AV)}$	**690A**
I_{TSM}	**9450A**
dV/dt*	**1500V/µs**
dI/dt	**300A/µs**

***Higher dV/dt selections are available on request**

VOLTAGE RATINGS

Part and Ordering Number	Repetitive Peak Voltages V_{DRM} and V_{RRM} (V)	Conditions
DCR690G52* DCR690G50 DCR690G48	5200 5000 4800	T_{vj} = -40°C to 125°C, I_{DRM} = I_{RRM} = 100mA, V_{DRM}, V_{RRM} t_p = 10ms V_{DSM} & V_{RSM} = V_{DRM} & V_{RRM} + 100V respectively

Lower voltage grades available.

*5000V @ -40°C, 5200V @ 0°C

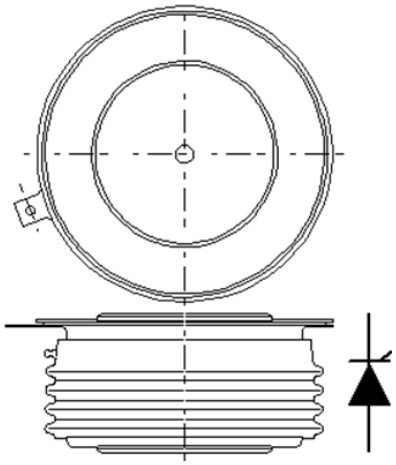
ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

DCR690G52

Note: Please use the complete part number when ordering and quote this number in any future correspondence relating to your order.



Outline type code: G

(See Package Details for further information)

Fig. 1 Package outline

CURRENT RATINGS

T_{case} = 60°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
Double Side Cooled				
$I_{T(AV)}$	Mean on-state current	Half wave resistive load	690	A
$I_{T(RMS)}$	RMS value	-	1080	A
I_T	Continuous (direct) on-state current	-	1060	A

SURGE RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
I_{TSM}	Surge (non-repetitive) on-state current	10ms half sine, T_{case} = 125°C V_R = 0	9.45	kA
I^2t	I^2t for fusing		0.45	MA^2s

THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions		Min.	Max.	Units
$R_{th(j-c)}$	Thermal resistance - junction to case	Double side cooled	DC	-	26.8	°C/kW
		Single side cooled	Anode DC	-	52.7	°C/kW
			Cathode DC	-	65.2	°C/kW
$R_{th(c-h)}$	Thermal resistance - case to heatsink	Clamping force 11.5kN (with mounting compound)	Double side	-	7.2	°C/kW
			Single side	-	14.4	°C/kW
T_{vj}	Virtual junction temperature	Blocking V_{DRM} / V_{RRM}		-	125	°C
T_{stg}	Storage temperature range			-55	125	°C
F_m	Clamping force			10	13	kN

DYNAMIC CHARACTERISTICS

Symbol	Parameter	Test Conditions		Min.	Max.	Units
I_{RRM}/I_{DRM}	Peak reverse and off-state current	At V_{RRM}/V_{DRM}, T_{case} = 125°C		-	100	mA
V_{TM}	Instantaneous forward voltage	At 1600A peak, T_j = 25°C		2.10	2.40	V
dV/dt	Max. linear rate of rise of off-state voltage	To 67% V_{DRM}, T_j = 125°C, gate open		-	1500	V/µs
dI/dt	Rate of rise of on-state current	From 67% V_{DRM} to 2x $I_{T(AV)}$ Gate source 30V, 10Ω t_r < 0.5µs, T_j = 125°C	Repetitive 50Hz	-	150	A/µs
			Non-repetitive	-	300	A/µs
$V_{T(TO)}$	Threshold voltage - Low level	100A to 500A at T_{case} = 125°C		-	0.92	V
	Threshold voltage - High level	500A to 3000A at T_{case} = 125°C		-	1.10	V
r_T	On-state slope resistance - Low level	100A to 500A at T_{case} = 125°C		-	1.45	mΩ
	On-state slope resistance - High level	500A to 3000A at T_{case} = 125°C		-	1.12	mΩ
t_{gd}	Delay time	V_D = 67% V_{DRM}, gate source 30V, 10Ω t_r = 0.5µs, T_j = 25°C		-	3	µs
t_q	Turn-off time	T_j = 125°C, V_R = 200V, dI/dt = 5A/µs, dV_{DR}/dt = 20V/µs linear		400	800	µs
Q_S	Stored charge [LEM]	I_T = 2000A, T_j = 125°C, dI/dt = 5A/µs		1200	2400	µC
Q_S	Stored charge	T_j = 125°C, dI/dt = 1A/µs, $V_{R\ peak}$ ~ 3200V, V_R ~ 2600V		-	2170	µC
I_{RR}	Reverse recovery current			-	37	A
I_L	Latching current	T_j = 25°C, V_D = 5V		-	3	A
I_H	Holding current	T_j = 25°C, R_{G-K} = ∞, I_{TM} = 500A, I_T = 5A		-	300	mA

GATE TRIGGER CHARACTERISTICS AND RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
V_{GT}	Gate trigger voltage	V_{DRM} = 5V, T_{case} = 25°C	1.5	V
V_{GD}	Gate non-trigger voltage	At 50% V_{DRM}, T_{case} = 125°C	0.4	V
I_{GT}	Gate trigger current	V_{DRM} = 5V, T_{case} = 25°C	350	mA
I_{GD}	Gate non-trigger current	At 50% V_{DRM}, T_{case} = 125°C	10	mA

CURVES

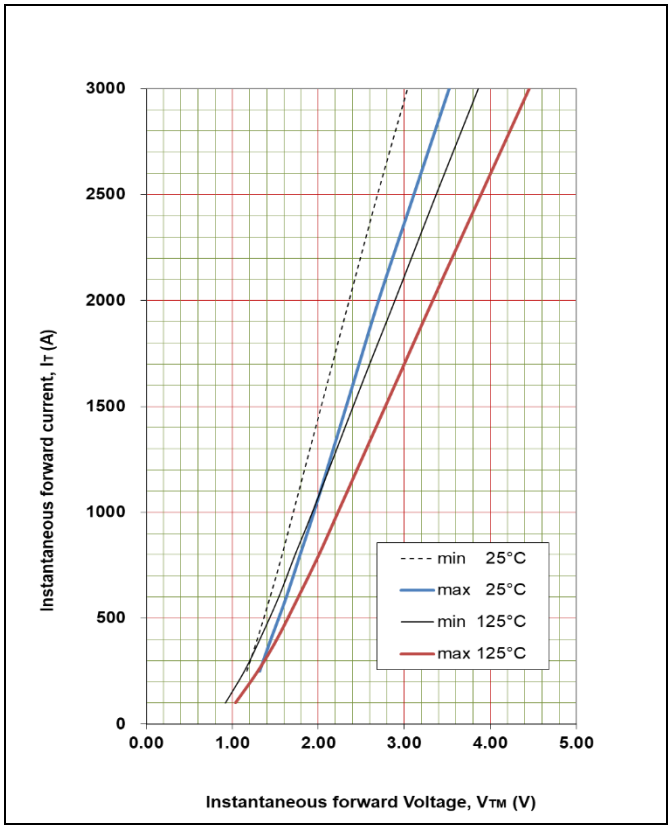


Fig. 2 Maximum & minimum on state characteristics

V_{TM} EQUATION

$V_{TM} = A + B.\ln(I_T) + C.I_T + D.\sqrt{I_T}$

Where A = 0.207302
B = 0.178990
C = 0.001121
D = -0.010408

These values are valid for T_j = 125°C for I_T 100A to 3000A

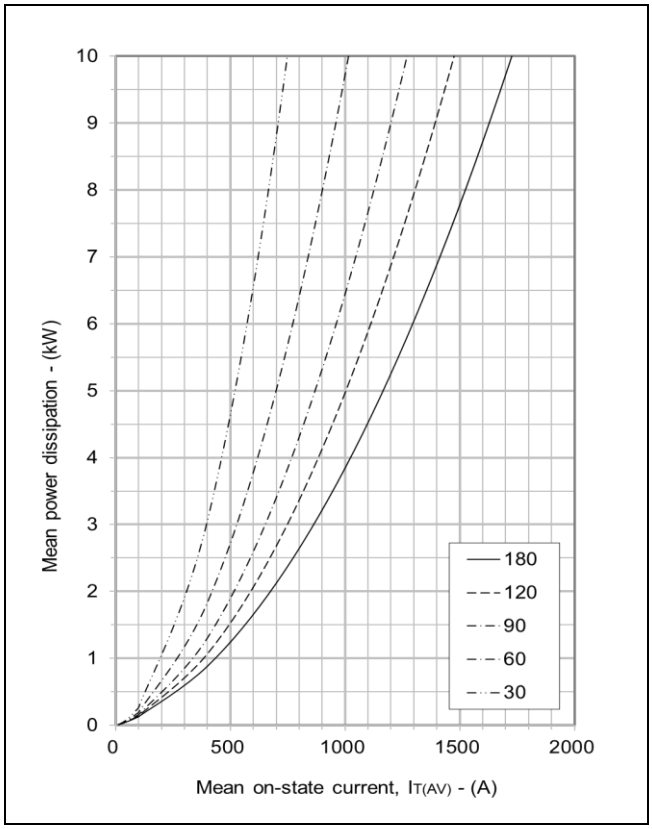


Fig. 3 On-state power dissipation - sine wave

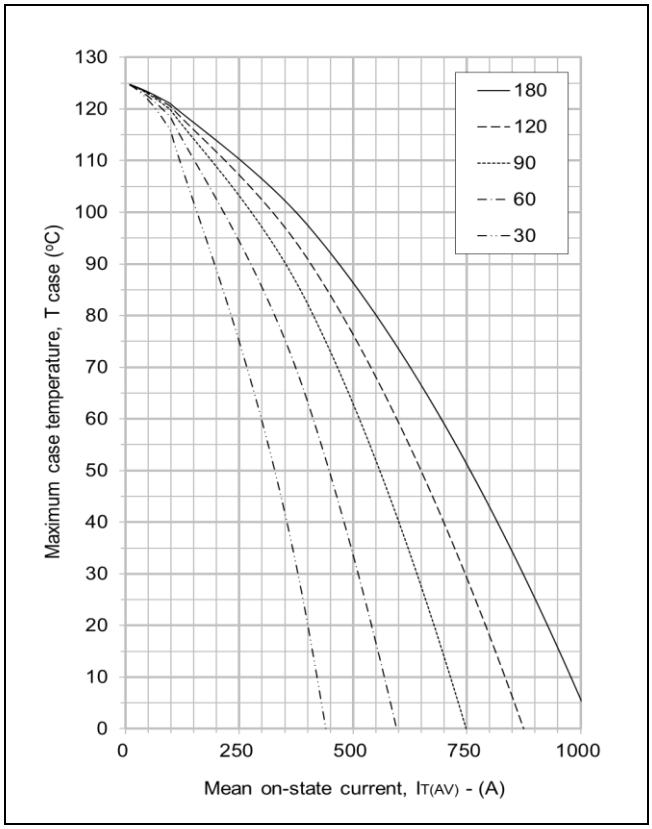


Fig. 4 Maximum permissible case temperature, double side cooled - sine wave

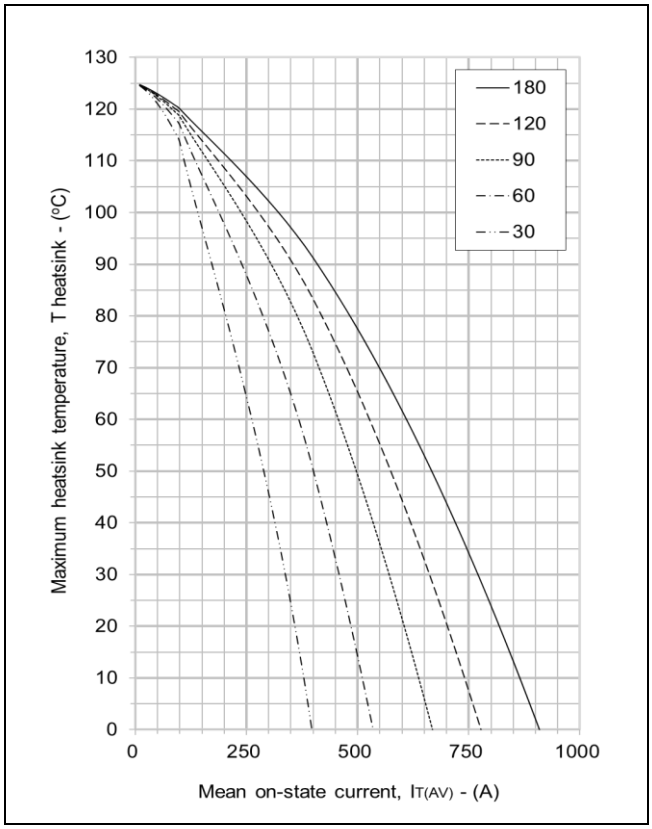


Fig. 5 Maximum permissible heatsink temperature, double side cooled - sine wave

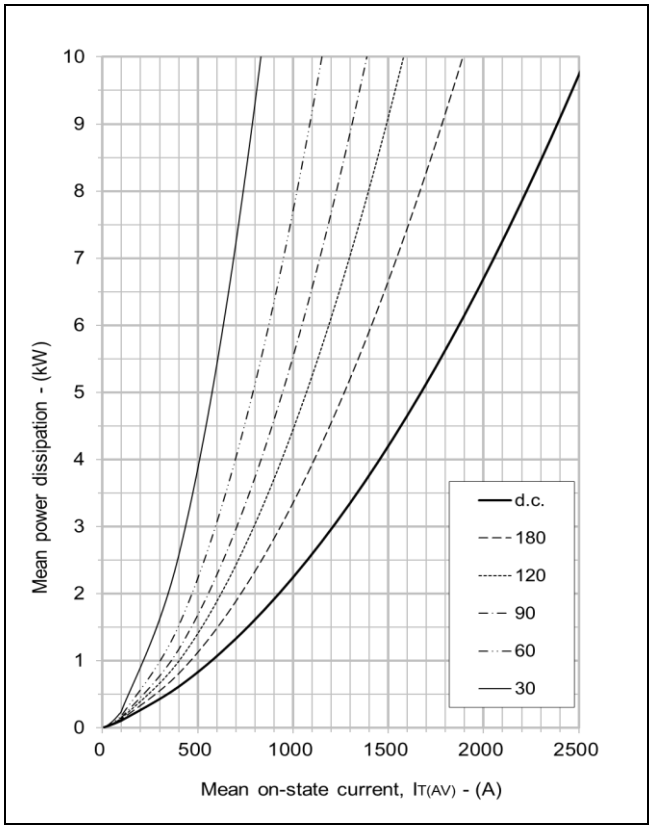


Fig. 6 On-state power dissipation - rectangular wave

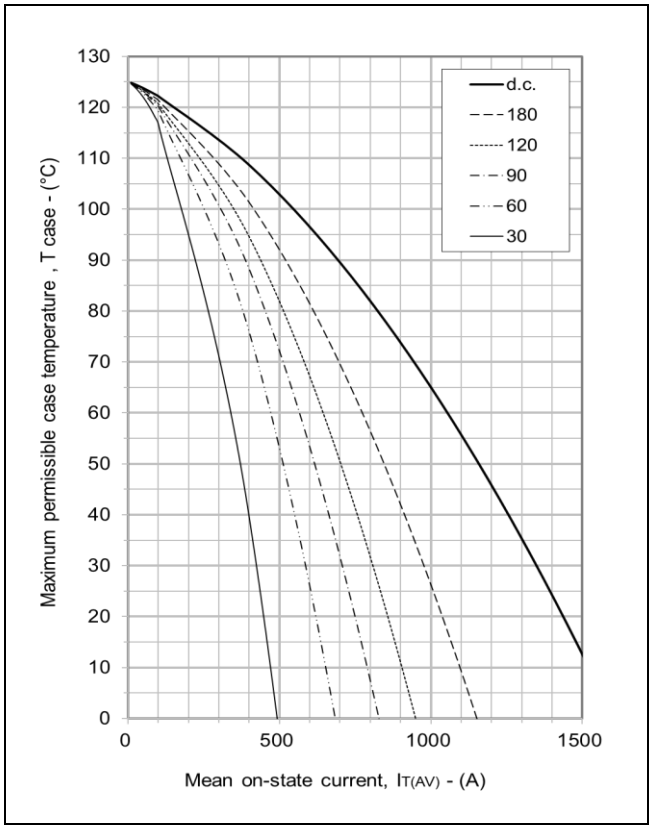


Fig. 7 Maximum permissible case temperature, double side cooled - rectangular wave

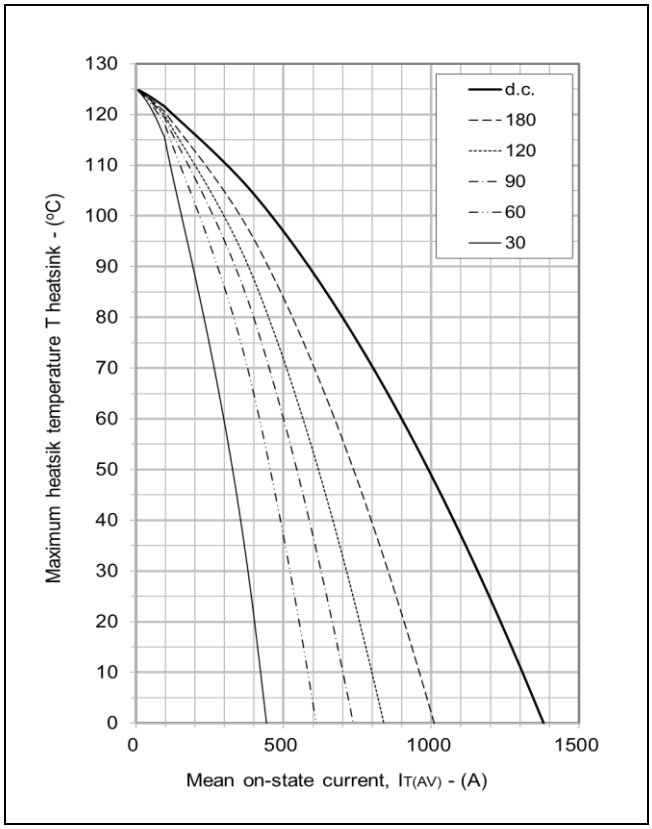
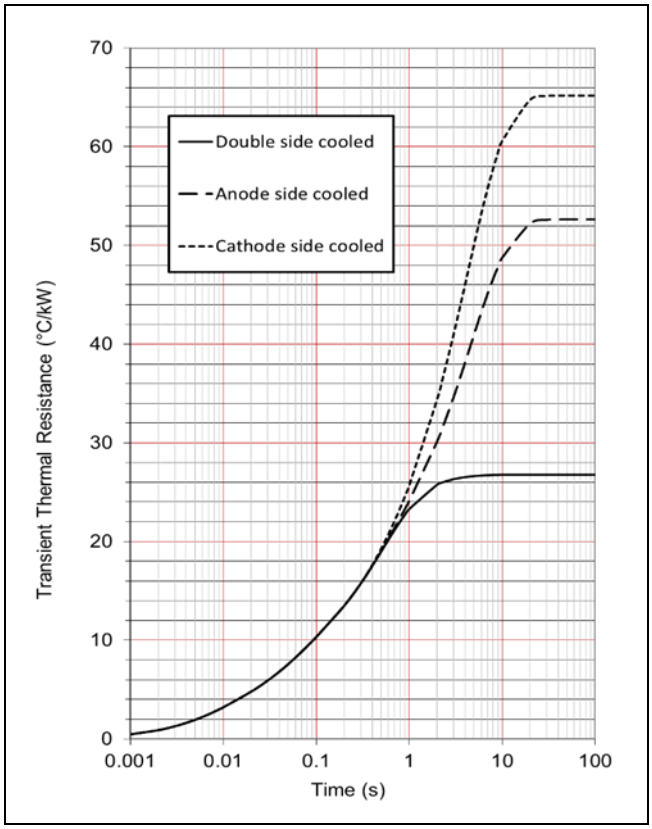


Fig. 8 Maximum permissible heatsink temperature, double side cooled - rectangular wave



		1	2	3	4
Double side cooled	Ri(°C/kW)	2.300	5.423	16.907	2.149
	Ti(s)	0.007	0.046	0.496	1.825
Anode side cooled	Ri(°C/kW)	2.321	5.266	10.269	34.803
	Ti(s)	0.007	0.046	0.348	4.582
Cathode side cooled	Ri(°C/kW)	2.490	5.911	7.426	49.343
	Ti(s)	0.007	0.053	0.393	4.230

$$Z_{th} = \sum_{i=1}^{i=4} R_i \cdot \left(1 - \exp\left(-\frac{T}{T_i}\right)\right)$$

$\Delta R_{th(j-c)}$ Conduction

Tables show the increments of thermal resistance $R_{th(j-c)}$ when the device operates at conduction angles other than d.c.

Double side cooling		
	ΔZ_{th} (z)	
θ°	sine	rect
180	4.15	2.72
120	4.90	4.02
90	5.74	4.79
60	6.53	5.65
30	7.16	6.64
15	7.46	7.18

Anode Side Cooling		
	ΔZ_{th} (z)	
θ°	sine	rect
180	4.15	2.72
120	4.89	4.02
90	5.73	4.78
60	6.52	5.65
30	7.15	6.62
15	7.44	7.16

Cathode Sided Cooling		
	ΔZ_{th} (z)	
θ°	sine	rect
180	4.13	2.71
120	4.87	4.00
90	5.69	4.76
60	6.46	5.60
30	7.07	6.56
15	7.36	7.09

Fig. 9 Maximum (limit) transient thermal impedance – junction to case (degC/kW)

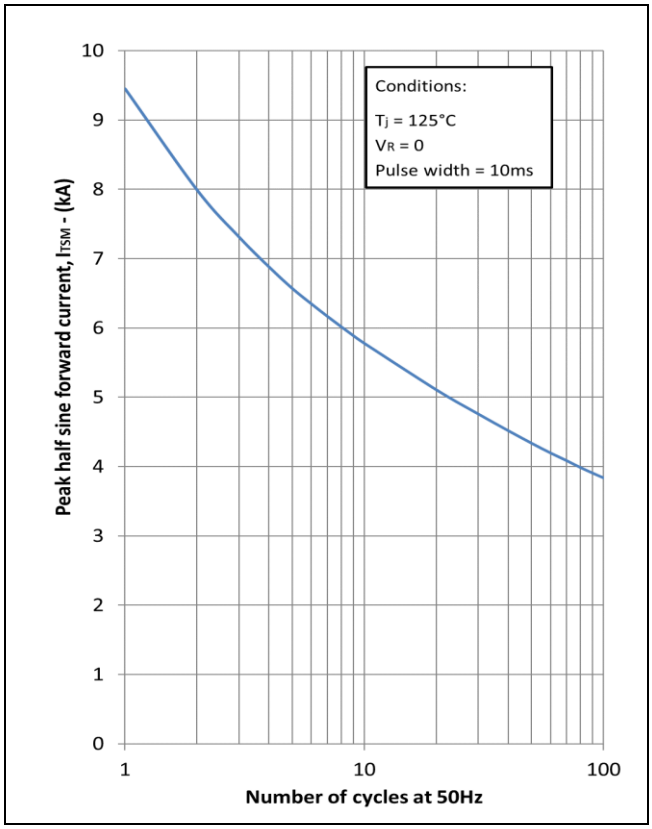


Fig. 10 Multi-cycle surge current

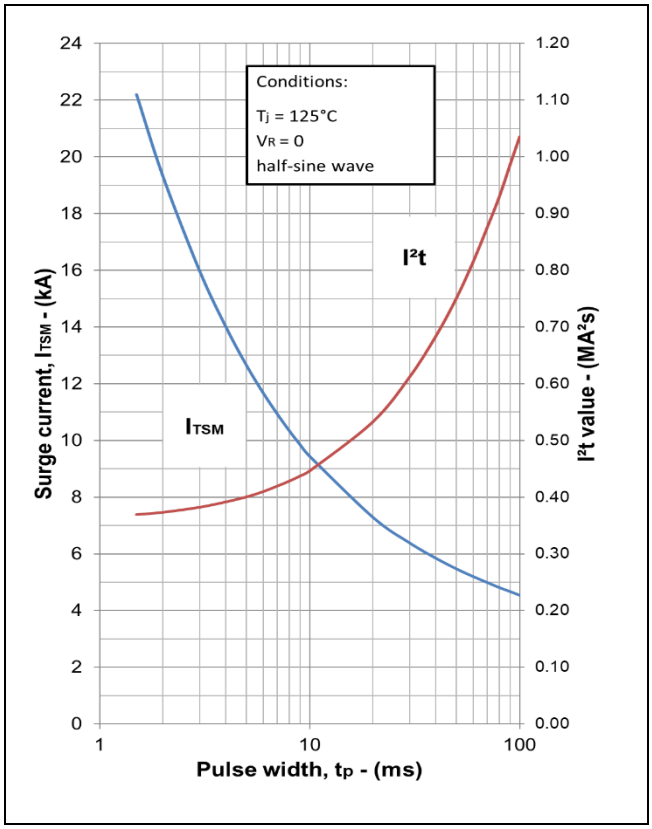


Fig. 11 Single-cycle surge current

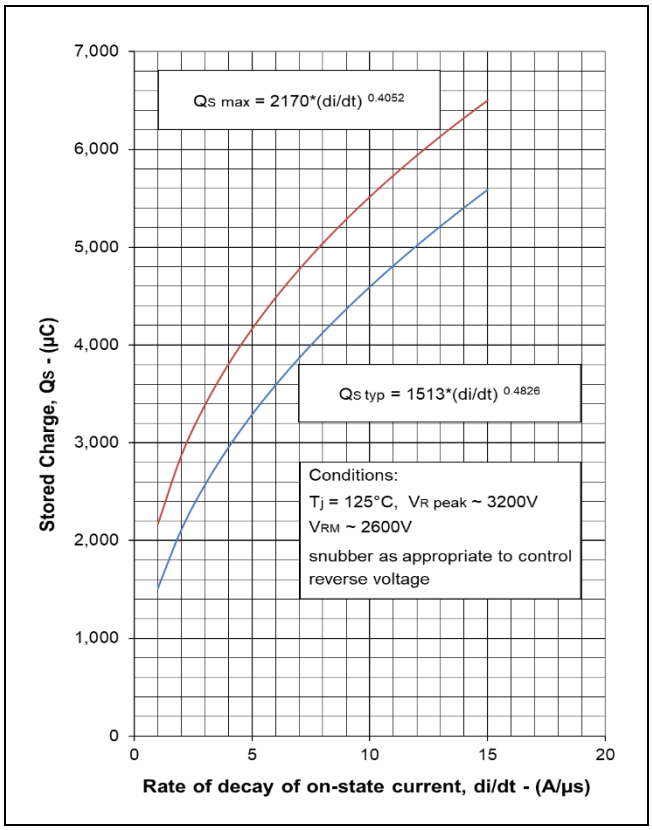


Fig. 12 Stored charge

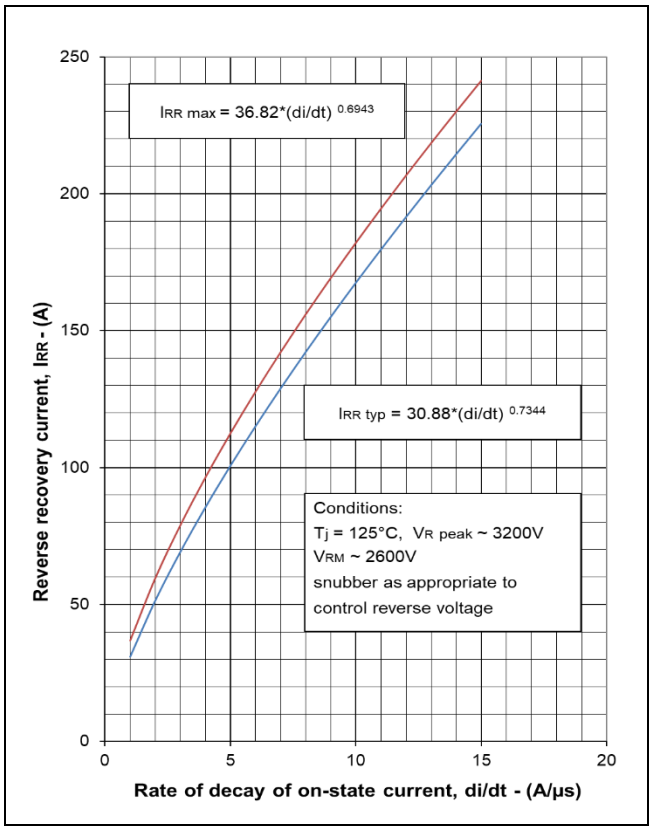


Fig. 13 Reverse recovery current

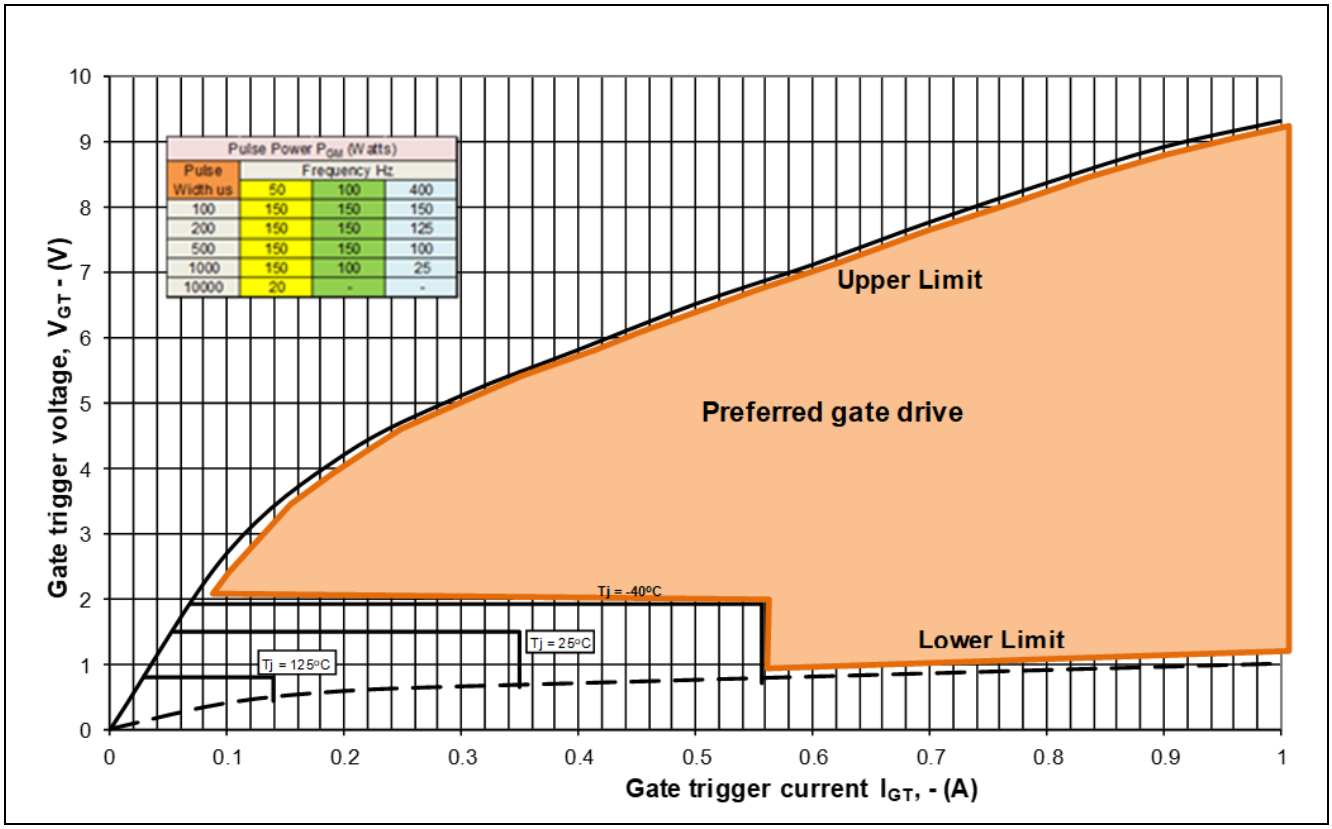


Fig. 14 Gate characteristics

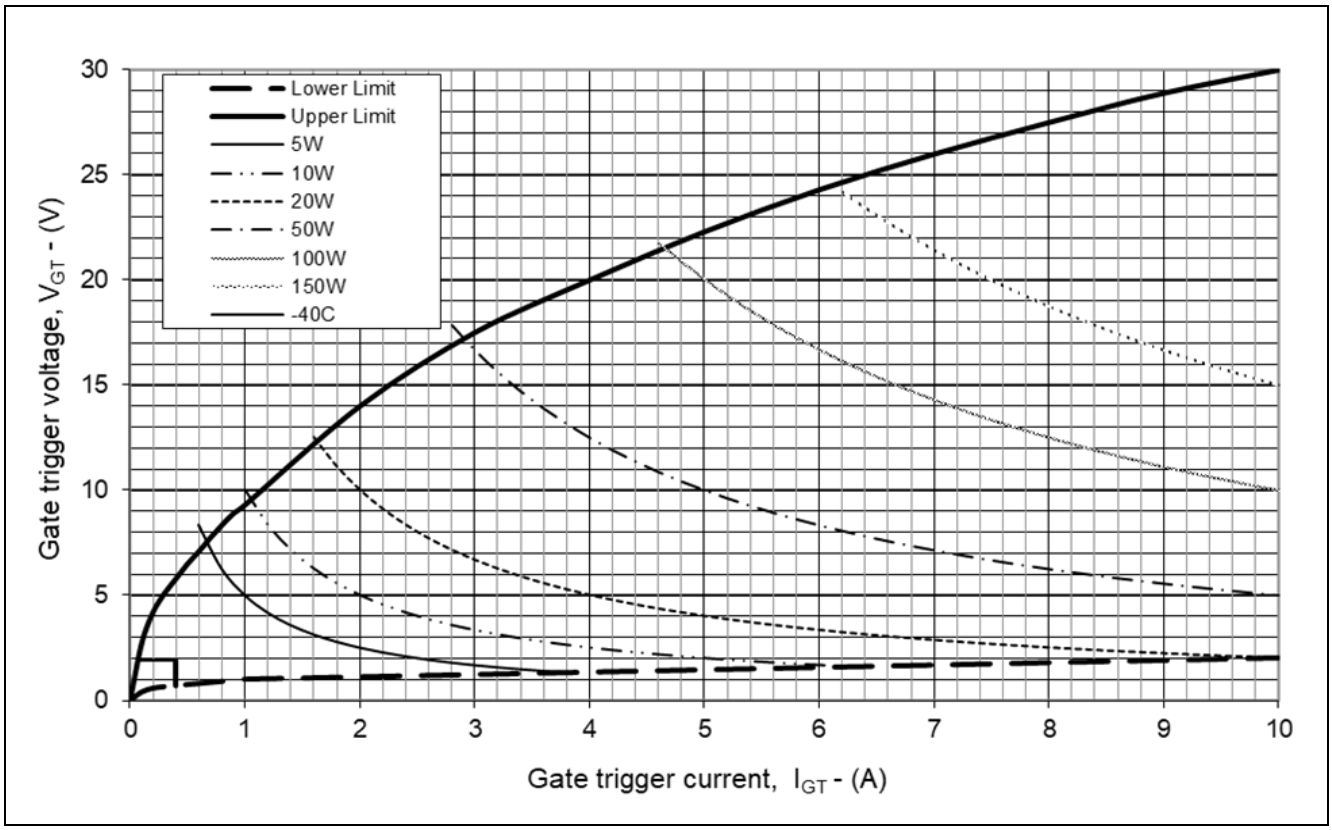


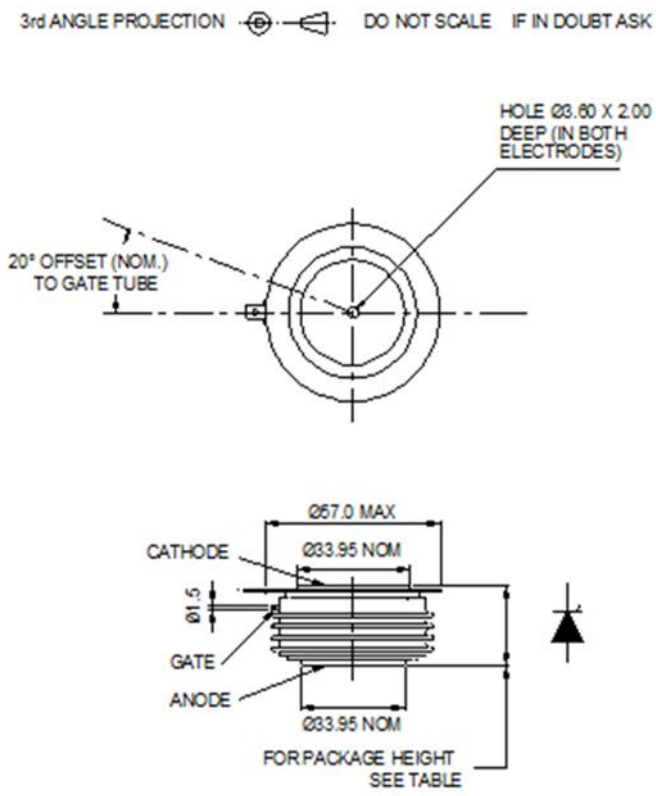
Fig. 15 Gate characteristics

PACKAGE DETAILS

For further package information, please contact Customer services.

All dimensions in mm, unless stated otherwise.

DO NOT SCALE



Device	Maximum Thickness (mm)	Minimum Thickness (mm)
DCR470G85	27.4	26.9
DCR590G65	27.1	26.6
DCR690G52	**26.9**	**26.3**
DCR780G42	26.7	26.2
DCR960G28	26.5	26.0
DCR1080G22	26.4	25.9

Lead length: 420mm

Lead terminal connector: M4 ring

Package outline type code: G

Fig. 16 Package outline

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