

FEATURES

- Double Side Cooling
- High Surge Capability
- Low Inductance Internal Construction

KEY PARAMETERS

V_{DRM}	1800V
$I_{T(AV)}$	6987A
I_{TSM}	98000A
dV/dt^*	1000V/μs
dI/dt	250A/μs

APPLICATIONS

- High Voltage Power Converters
- DC Motor Control
- High Voltage Power Supplies

VOLTAGE RATINGS

Part and Ordering Number	Repetitive Peak Voltages V_{DRM} and V_{RRM} V	Conditions
DCR6990M18	1800	$T_{vj} = 0^{\circ}\text{C to } 125^{\circ}\text{C}$, $I_{DRM} = I_{RRM} = 500\text{mA}$, $V_{DRM}, V_{RRM} t_p = 10\text{ms}$, $V_{DSM} \& V_{RSM} =$ $V_{DRM} \& V_{RRM} + 100\text{V}$ respectively
DCR6990M16	1600	
DCR6990M14	1400	
DCR6990M12	1200	

Lower voltage grades available.

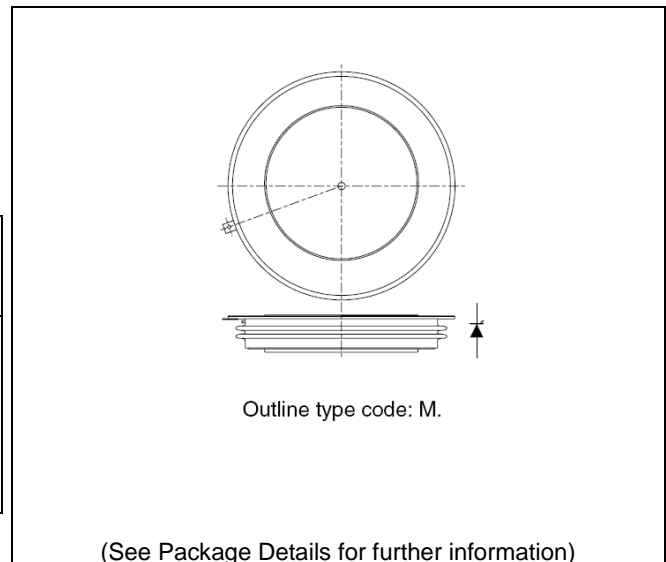


Fig. 1 Package outline

ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

DCR6990M14

Note: Please use the complete part number when ordering and quote this number in any future correspondence relating to your order.

CURRENT RATINGS

T_{case} = 60°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
Double Side Cooled				
I _{T(AV)}	Mean on-state current	Half wave resistive load	6987	A
I _{T(RMS)}	RMS value	-	10976	A
I _T	Continuous (direct) on-state current	-	10169	A
Single Side Cooled (Anode side)				
I _{T(AV)}	Mean on-state current	Half wave resistive load	4559	A
I _{T(RMS)}	RMS value	-	7161	A
I _T	Continuous (direct) on-state current	-	6058	A

T_{case} = 80°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
Double Side Cooled				
I _{T(AV)}	Mean on-state current	Half wave resistive load	5446	A
I _{T(RMS)}	RMS value	-	8586	A
I _T	Continuous (direct) on-state current	-	7769	A
Single Side Cooled (Anode side)				
I _{T(AV)}	Mean on-state current	Half wave resistive load	3500	A
I _{T(RMS)}	RMS value	-	5497	A
I _T	Continuous (direct) on-state current	-	4518	A

SURGE RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
I_{TSM}	Surge (non-repetitive) on-state current	10ms half sine, $T_{case} = 125^{\circ}C$ $V_R = 50\%V_{RRM} - \frac{1}{4}$ Sine	78.0	kA
I^2t	I^2t for fusing		30.4×10^6	A^2s
I_{TSM}	Surge (non-repetitive) on-state current	10ms half sine, $T_{case} = 125^{\circ}C$ $V_R = 0$	98.0	kA
I^2t	I^2t for fusing		48×10^6	A^2s

THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions	Min.	Max.	Units	
$R_{th(j-c)}$	Thermal resistance – junction to case	Double side cooled	DC	-	0.005	$^{\circ}C/W$
		Single side cooled	Anode DC	-	0.01	$^{\circ}C/W$
			Cathode DC	-	0.01	$^{\circ}C/W$
$R_{th(c-h)}$	Thermal resistance – case to heatsink	Clamping force 83.0kN (with mounting compound)	Double side	-	0.001	$^{\circ}C/W$
			Single side	-	0.002	$^{\circ}C/W$
T_{vj}	Virtual junction temperature	Blocking V_{DRM} / V_{RRM}	-	125	$^{\circ}C$	
T_{stg}	Storage temperature range		-55	125	$^{\circ}C$	
F_m	Clamping force		74.0	91.0	kN	

DYNAMIC CHARACTERISTICS

Symbol	Parameter	Test Conditions	Min.	Max.	Units	
I_{RRM}/I_{DRM}	Peak reverse and off-state current	At V_{RRM}/V_{DRM} , $T_{case} = 125^{\circ}C$	-	500	mA	
dV/dt	Max. linear rate of rise of off-state voltage	To 67% V_{DRM} , $T_j = 125^{\circ}C$, gate open	-	1000	V/ μ s	
dl/dt	Rate of rise of on-state current	From 67% V_{DRM} to 1000A Gate source 20V, 20 Ω , $t_r = 0.5\mu$ s to 1A, $T_j = 125^{\circ}C$	Repetitive 50Hz	-	250	A/ μ s
			Non-repetitive	-	500	A/ μ s
$V_{T(TO)}$	Threshold voltage – Low level	At $T_{vj} = 125^{\circ}C$	-	0.77	V	
r_T	On-state slope resistance – Low level	At $T_{vj} = 125^{\circ}C$	-	0.05	m Ω	
t_{gd}	Delay time	$V_D = 67\% V_{DRM}$, gate source 30V, 15 Ω $t_r = 0.5\mu$ s, $T_j = 25^{\circ}C$	-	2	μ s	
I_L	Latching current	$T_j = 25^{\circ}C$, $V_D = 5V$	150	750	mA	
I_H	Holding current	$T_j = 25^{\circ}C$, $V_{G-K} = \infty$	40	200	mA	

GATE TRIGGER CHARACTERISTICS AND RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
V_{GT}	Gate trigger voltage	$V_{DRM} = 5V$, $T_{case} = 25^{\circ}C$	3.5	V
V_{GD}	Gate non-trigger voltage	At V_{DRM} , $T_{case} = 125^{\circ}C$	0.25	V
I_{GT}	Gate trigger current	$V_{DRM} = 5V$, $T_{case} = 25^{\circ}C$	500	mA
V_{FGM}	Peak forward gate voltage	Anode positive with respect to cathode	30	V
V_{FGN}	Peak forward gate voltage	Anode negative with respect to cathode	0.25	V
V_{RGM}	Peak forward gate voltage	-	5	V
I_{FGM}	Peak forward gate current	Anode positive with respect to cathode	30	A
P_{GM}	Peak gate power	See Gate Characteristics curve/table	150	W
$P_{G(AV)}$	Mean gate power	-	10	W

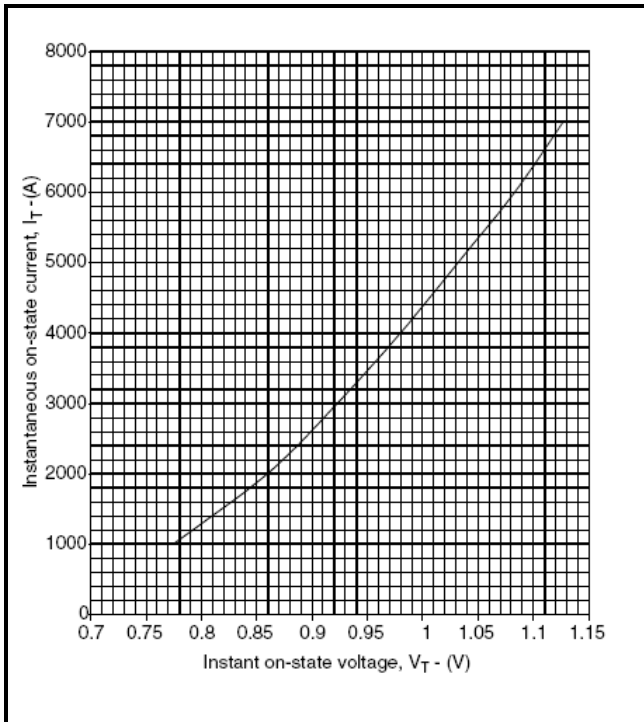


Fig.2 Maximum (limit) on-state characteristics

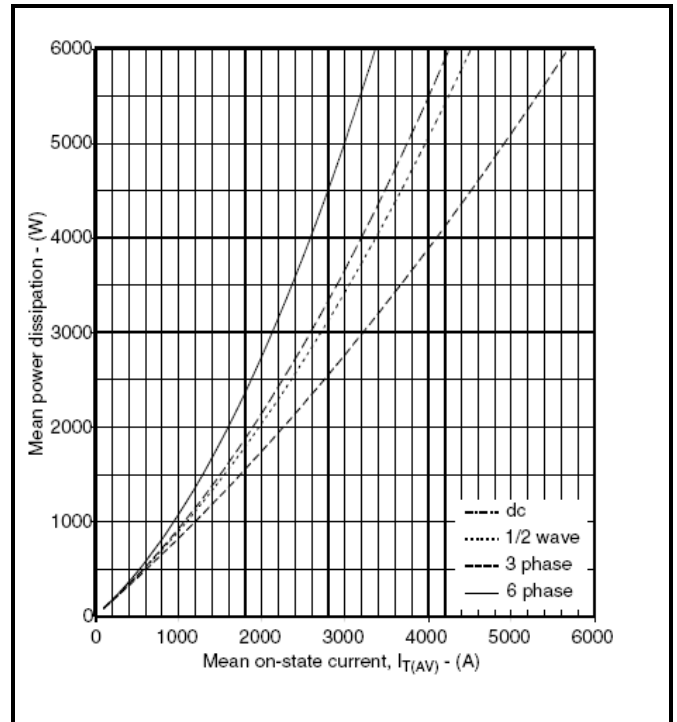


Fig.3 Power dissipation curves

V_{TM} Equation:-

$$V_{TM} = A + B \ln(I_T) + C \cdot I_T + D \cdot \sqrt{I_T}$$

Where

A = 0.4624

B = 0.0275

C = 2.2501×10^{-5}

D = 0.0032

these values are valid for $T_j = 125^\circ\text{C}$ for I_T 500A to 7000A

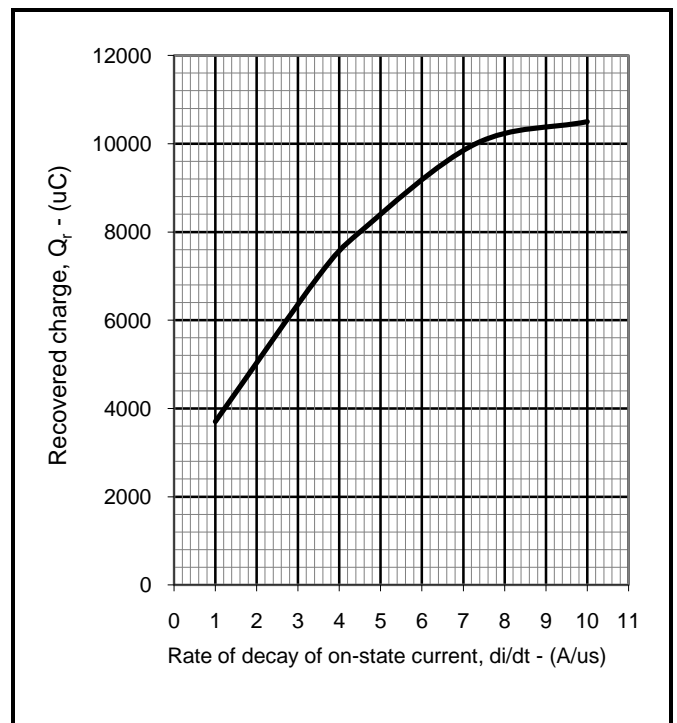


Fig.4 Recovered charge

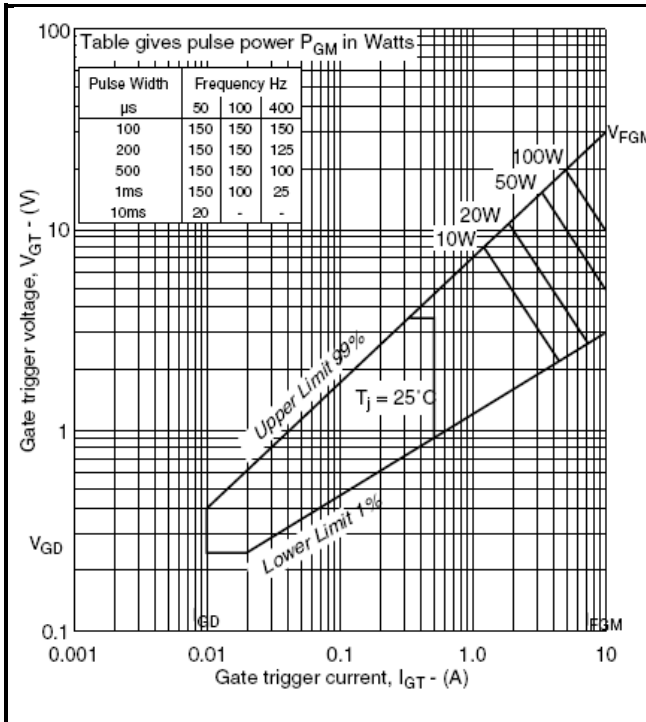


Fig.5 Gate characteristics

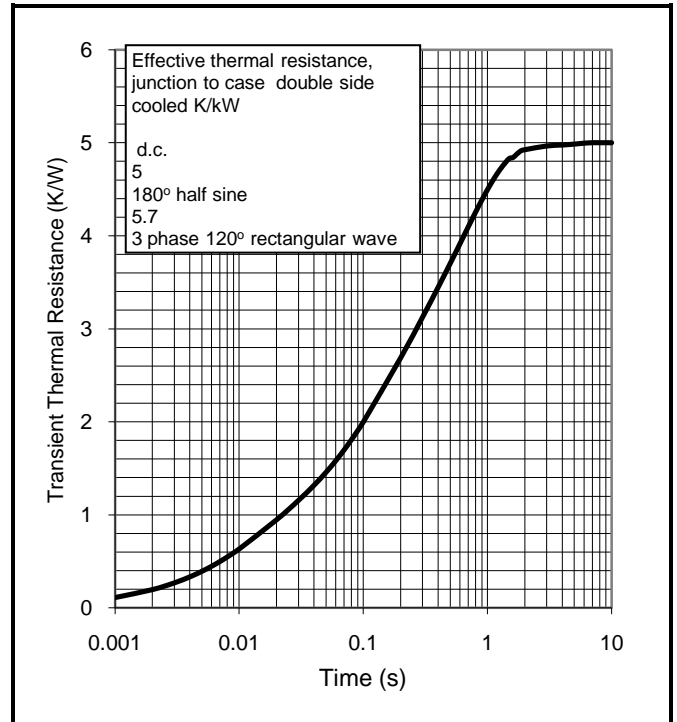


Fig.6 Maximum (limit) transient thermal impedance- junction to case ($^\circ C/W$)

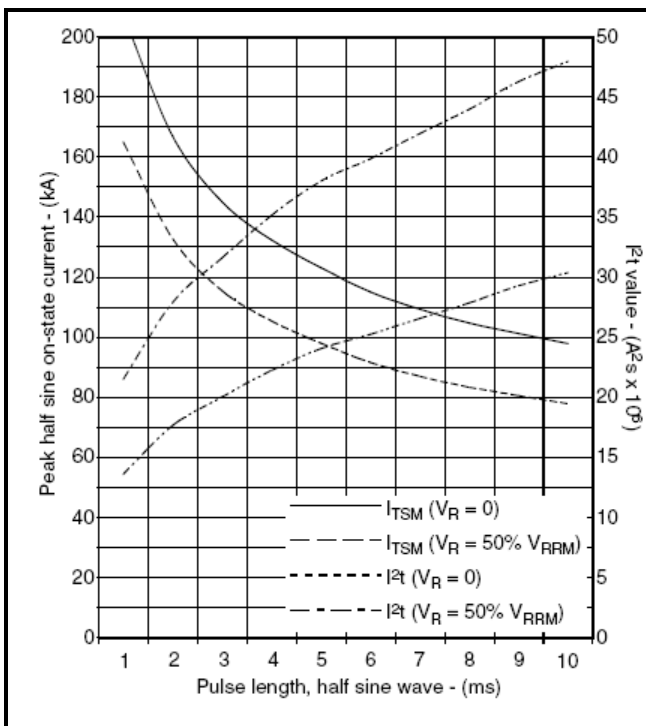


Fig.7 Sub-cycle surge current

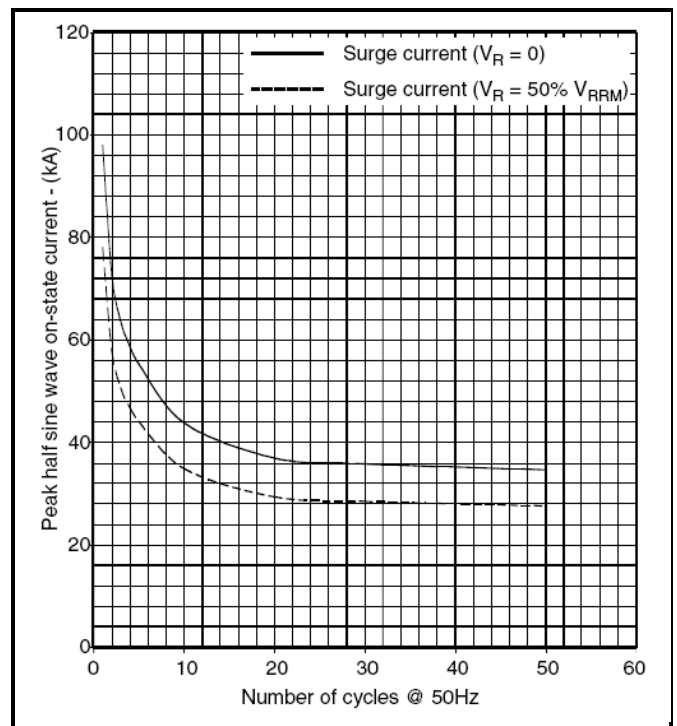


Fig.8 Multi-cycle surge current

PACKAGE DETAILS

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.

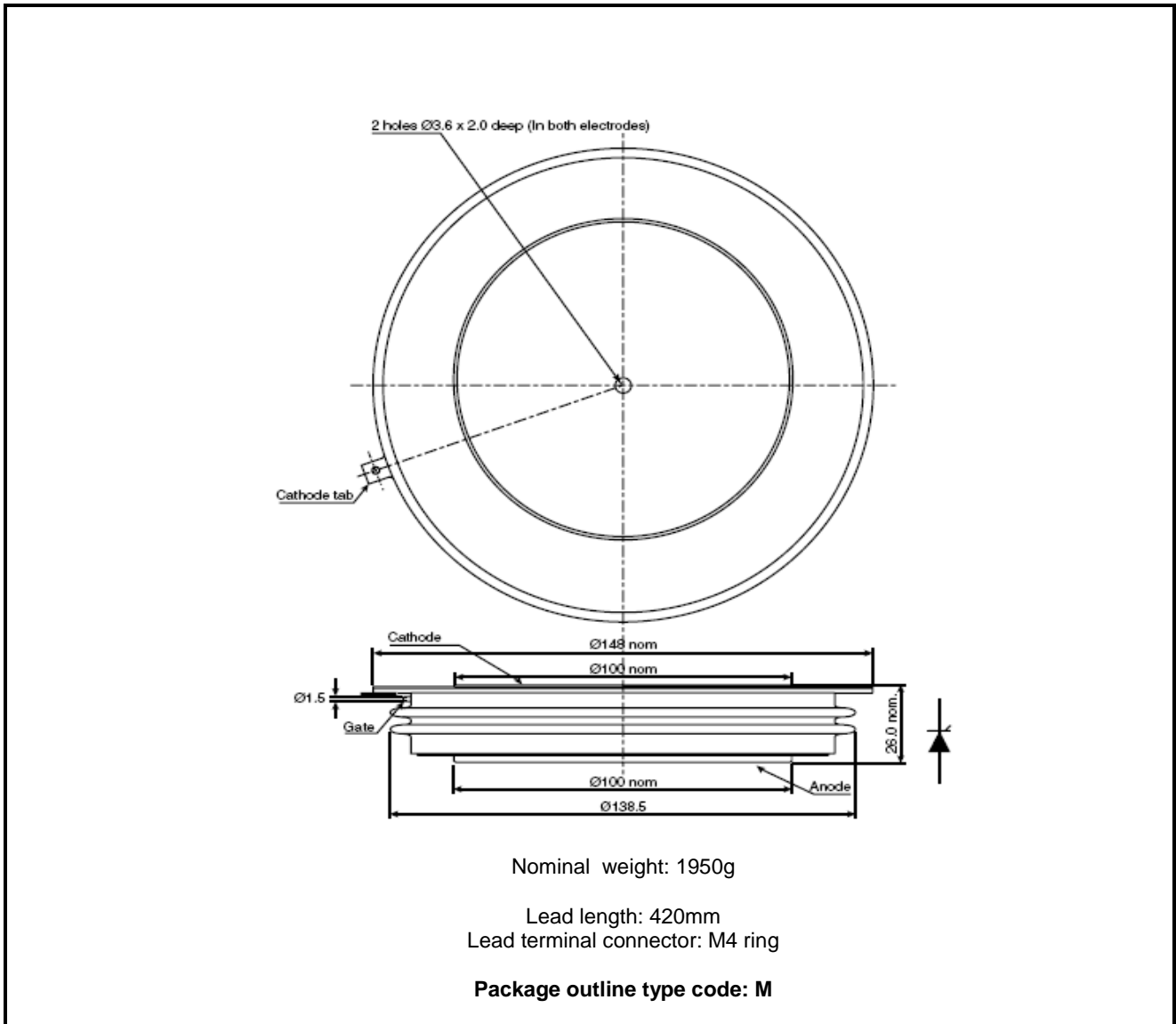


Fig.9 Package outline

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Due to the diversity of product applications, the information contained herein is provided as a guide only and does not constitute any guarantee of suitability for use in a specific application. The user must evaluate the suitability of the product and the completeness of the product data for the application. The user is responsible for product selection and ensuring all safety and any warning requirements are met. Should additional product information be needed please contact Customer Service.

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The products must not be touched when operating because there is a danger of electrocution or severe burning. Always use protective safety equipment such as appropriate shields for the product and wear safety glasses. Even when disconnected any electric charge remaining in the product must be discharged and allowed to cool before safe handling using protective gloves.

Extended exposure outside the product ratings may affect reliability leading to premature product failure. Use outside the product ratings is likely to cause permanent damage to the product. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture, a large current to flow or high voltage arcing, resulting in fire or explosion. Appropriate application design and safety precautions should always be followed to protect persons and property.

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No Annotation: The product has been approved for production and unless otherwise notified by Dynex any product ordered will be supplied to the **current version of the data sheet prevailing at the time of our order acknowledgement.**

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