

## FEATURES

- Trench Gate IGBT
- Cu Base with Enhanced Al<sub>2</sub>O<sub>3</sub> Substrates
- High Thermal Cycling Capability
- 10µs Short Circuit Withstand
- Low E<sub>ON</sub> E<sub>OFF</sub> Variant
- IGBT T<sub>vj(max)</sub> = 175°C

## APPLICATIONS

- Motor Drives
- Power Charging Equipment
- Renewable Energy Power Conversion
- Electric Vehicles

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM600M1HS12-PC500 is a half bridge 1200V, trench gate, insulated gate bipolar transistor (IGBT) module with enhanced field stop and implantation technology. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10µs short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

## ORDERING INFORMATION

Order As:

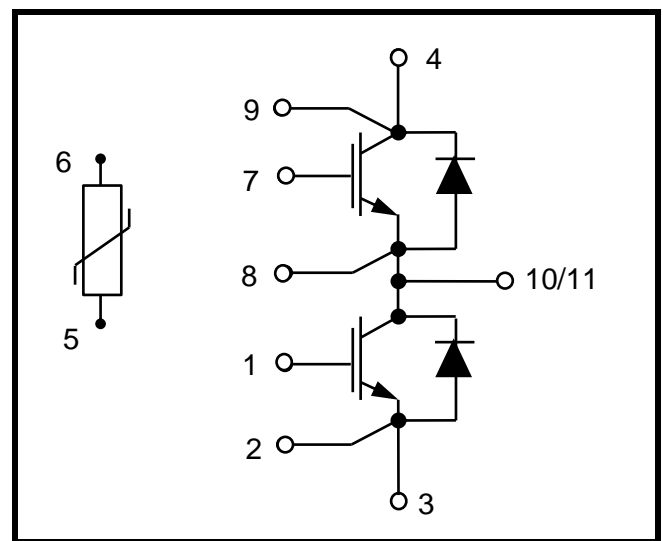
### DIM600M1HS12-PC500

Note: When ordering, please use the complete part number

## KEY PARAMETERS

V <sub>CE(S)</sub>	<b>1200V</b>
V <sub>CE(sat)</sub> * (typ)	<b>1.85V</b>
I <sub>C</sub> (max)	<b>600A</b>
I <sub>C(PK)</sub> (max)	<b>1200A</b>

\* Measured at the auxiliary terminals



**Fig. 1 Circuit configuration**



**Fig. 2 Package**

**ABSOLUTE MAXIMUM RATINGS**

Stresses above those listed under ‘Absolute Maximum Ratings’ may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

**T<sub>case</sub> = 25°C unless stated otherwise**

Symbol	Parameter	Test Conditions	Max.	Units
V <sub>CES</sub>	Collector-emitter voltage	V <sub>GE</sub> = 0V, T <sub>C</sub> = 25°C	1200	V
V <sub>GES</sub>	Gate-emitter voltage	T <sub>C</sub> = 25°C	±20	V
I <sub>C</sub>	Continuous collector current	T <sub>C</sub> = 100 °C, T <sub>vj</sub> max = 175°C	600	A
I <sub>C(PK)</sub>	Peak collector current	t <sub>p</sub> = 1ms, T <sub>C</sub> = 133°C	1200	A
P <sub>max</sub>	Max. transistor power dissipation	T <sub>C</sub> = 25°C, T <sub>vj</sub> = 175°C	3.0	kW
I <sup>2</sup> t	Diode I <sup>2</sup> t value	V <sub>R</sub> = 0, t <sub>p</sub> = 10ms, T <sub>vj</sub> = 150°C	21.6	kA <sup>2</sup> s
V <sub>isol</sub>	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	3400	V

**THERMAL AND MECHANICAL RATINGS**

Internal insulation material:	Al <sub>2</sub> O <sub>3</sub>
Baseplate material:	Cu
Creepage distance – Terminal to heatsink:	14.5mm
Creepage distance – Terminal to terminal:	13.0mm
Clearance – Terminal to heatsink:	12.5mm
Clearance – Terminal to terminal:	10mm
CTI (Comparative Tracking Index):	>200

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
R <sub>th(j-c)</sub>	Thermal resistance – IGBT	Continuous dissipation - junction to case	-	-	49	°C/kW
R <sub>th(j-c)</sub>	Thermal resistance – diode		-	-	77	°C/kW
R <sub>th(c-h)</sub>	Thermal resistance – case to heatsink (IGBT)	Mounting torque 5Nm (with mounting grease 1W/m °C)	-	-	34	°C/kW
R <sub>th(c-h)</sub>	Thermal resistance – case to heatsink (Diode)		-	-	40	°C/kW
T <sub>j</sub>	Junction temperature	IGBT	-40	-	150	°C
		Diode	-40	-	150	°C
T <sub>stg</sub>	Storage temperature range	-	-40	-	125	°C
	Screw torque	Mounting – M5	3	-	6	Nm
		Electrical connections – M6	3	-	6	Nm

## ELECTRICAL CHARACTERISTICS

$T_{case} = 25^{\circ}C$  unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
I <sub>CES</sub>	Collector cut-off current	$V_{GE} = 0V, V_{CE} = V_{CES}$			1	mA
		$V_{GE} = 0V, V_{CE} = V_{CES}, T_C = 125^{\circ}C$			10	mA
		$V_{GE} = 0V, V_{CE} = V_{CES}, T_C = 150^{\circ}C$			20	mA
I <sub>GES</sub>	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$			0.5	$\mu A$
V <sub>GE(TH)</sub>	Gate threshold voltage	$I_C = 15mA, V_{GE} = V_{CE}$	5.50	6.10	6.70	V
V <sub>CE(sat)</sub>	Collector-emitter saturation voltage	$V_{GE} = 15V, I_C = 600A$		1.85	2.25	V
		$V_{GE} = 15V, I_C = 600A, T_j = 125^{\circ}C$		2.15	2.55	V
		$V_{GE} = 15V, I_C = 600A, T_j = 150^{\circ}C$		2.25	2.65	V
I <sub>F</sub>	Diode forward current	DC		600		A
I <sub>FM</sub>	Diode maximum forward current	$t_p = 1ms$		1200		A
V <sub>F</sub>	Diode forward voltage	$I_F = 600A$		1.9	2.3	V
		$I_F = 600A, T_j = 125^{\circ}C$		2.1	2.5	V
		$I_F = 600A, T_j = 150^{\circ}C$		2.1	2.5	V
C <sub>ies</sub>	Input capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 100kHz$		93		nF
Q <sub>g</sub>	Gate charge	$\pm 15V$		6.9		$\mu C$
C <sub>res</sub>	Reverse transfer capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		1.0		nF
L <sub>M</sub>	Module inductance			22		nH
R <sub>INT</sub>	Internal transistor resistance			1		m $\Omega$
SC <sub>Data</sub>	Short circuit current, I <sub>sc</sub>	$T_j = 150^{\circ}C, V_{CC} = 800V$ $t_p \leq 10\mu s, V_{GE} \leq 15V$ $V_{CE(max)} = V_{CES} - L^* \times di/dt$ IEC 60747-9		2800		A

**Note:**

\* L is the circuit inductance + L<sub>M</sub>

## NTC-Thermistor Data

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
R <sub>25</sub>	Rated resistance	$T_C = 25^{\circ}C$		5		k $\Omega$
$\Delta R/R$	Deviation of R <sub>100</sub>	$T_C = 100^{\circ}C, R_{100} = 493\Omega$	-5		5	%
P <sub>25</sub>	Power dissipation	$T_C = 25^{\circ}C$			20	m/W
B <sub>25/50</sub>	B-value	$R_2 = R_{25} \exp [B_{25/50}(1/T_2 - 1/(298.15K))]$		3375		K
B <sub>25/80</sub>		$R_2 = R_{25} \exp [B_{25/80}(1/T_2 - 1/(298.15K))]$		3411		K
B <sub>25/100</sub>		$R_2 = R_{25} \exp [B_{25/100}(1/T_2 - 1/(298.15K))]$		3433		K

**ELECTRICAL CHARACTERISTICS**

**T<sub>case</sub> = 25°C unless stated otherwise**

Symbol	Parameter	Test Conditions		Min	Typ.	Max	Units
t <sub>d(off)</sub>	Turn-off delay time	I <sub>C</sub> = 600A V <sub>CE</sub> = 600V V <sub>GE</sub> = ±15V R <sub>G(OFF)</sub> = 1.5Ω R <sub>G(ON)</sub> = 1.5Ω L <sub>S</sub> ~ 60nH	dv/dt = 5000V/μs		725		ns
t <sub>f</sub>	Fall time				120		ns
E <sub>OFF</sub>	Turn-off energy loss				66		mJ
t <sub>d(on)</sub>	Turn-on delay time		di/dt = 6100A/μs		310		ns
t <sub>r</sub>	Rise time				110		ns
E <sub>ON</sub>	Turn-on energy loss				22		mJ
Q <sub>rr</sub>	Diode reverse recovery charge	I <sub>F</sub> = 600A V <sub>CE</sub> = 600V di/dt = 6100A/μs			62		μC
I <sub>rr</sub>	Diode reverse recovery current				405		A
E <sub>rec</sub>	Diode reverse recovery energy				34		mJ

**T<sub>case</sub> = 125°C unless stated otherwise**

Symbol	Parameter	Test Conditions		Min	Typ.	Max	Units
t <sub>d(off)</sub>	Turn-off delay time	I <sub>C</sub> = 600A V <sub>CE</sub> = 600V V <sub>GE</sub> = ±15V R <sub>G(OFF)</sub> = 1.5Ω R <sub>G(ON)</sub> = 1.5Ω L <sub>S</sub> ~ 60nH	dv/dt = 5000V/μs		770		ns
t <sub>f</sub>	Fall time				205		ns
E <sub>OFF</sub>	Turn-off energy loss				80		mJ
t <sub>d(on)</sub>	Turn-on delay time		di/dt = 6100A/μs		330		ns
t <sub>r</sub>	Rise time				115		ns
E <sub>ON</sub>	Turn-on energy loss				29		mJ
Q <sub>rr</sub>	Diode reverse recovery charge	I <sub>F</sub> = 600A V <sub>CE</sub> = 600V di/dt = 6100A/μs			95		μC
I <sub>rr</sub>	Diode reverse recovery current				460		A
E <sub>rec</sub>	Diode reverse recovery energy				48		mJ

**T<sub>case</sub> = 150°C unless stated otherwise**

Symbol	Parameter	Test Conditions		Min	Typ.	Max	Units
t <sub>d(off)</sub>	Turn-off delay time	I <sub>C</sub> = 600A V <sub>CE</sub> = 600V V <sub>GE</sub> = ±15V R <sub>G(OFF)</sub> = 1.5Ω R <sub>G(ON)</sub> = 1.5Ω L <sub>S</sub> ~ 60nH	dv/dt = 5000V/μs		785		ns
t <sub>f</sub>	Fall time				225		ns
E <sub>OFF</sub>	Turn-off energy loss				84		mJ
t <sub>d(on)</sub>	Turn-on delay time		di/dt = 6100A/μs		335		ns
t <sub>r</sub>	Rise time				115		ns
E <sub>ON</sub>	Turn-on energy loss				31		mJ
Q <sub>rr</sub>	Diode reverse recovery charge	I <sub>F</sub> = 600A V <sub>CE</sub> = 600V di/dt = 6100A/μs			110		μC
I <sub>rr</sub>	Diode reverse recovery current				490		A
E <sub>rec</sub>	Diode reverse recovery energy				56		mJ

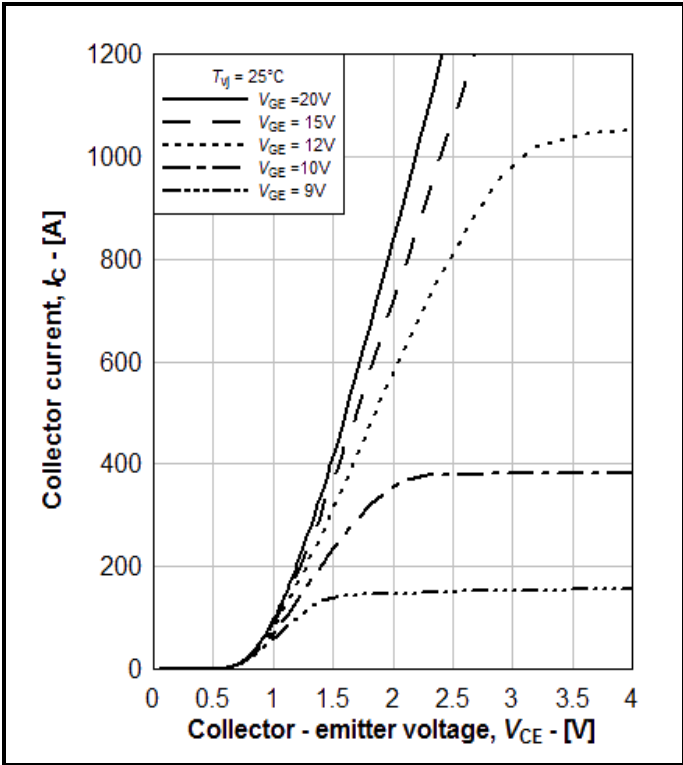


Fig. 3 Typical IGBT output characteristics,  $I_c = f(V_{CE})$

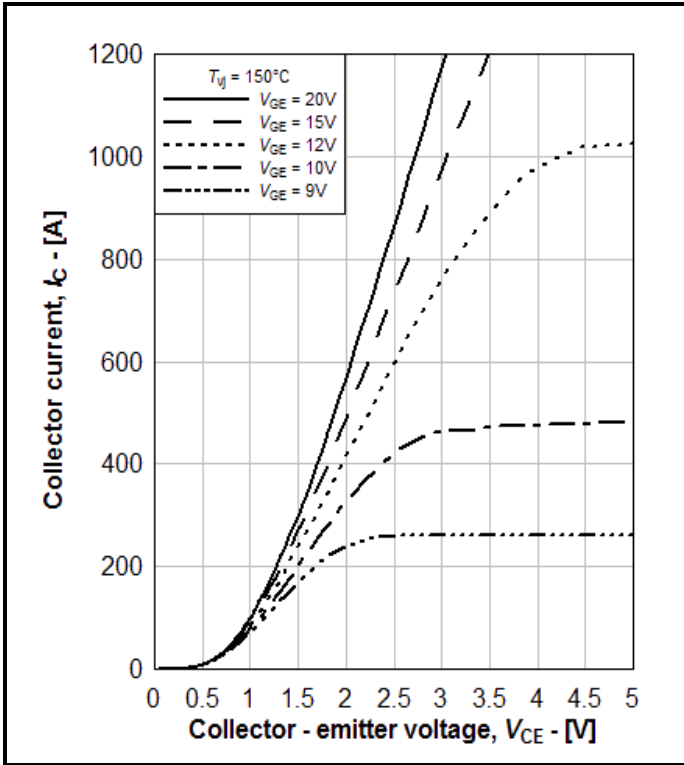


Fig. 4 Typical IGBT output characteristics,  $I_c = f(V_{CE})$

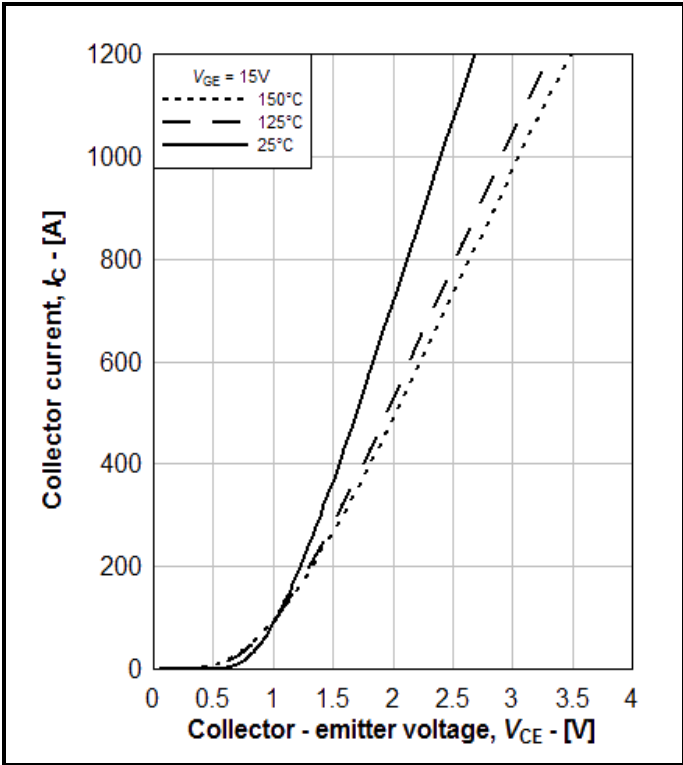


Fig. 5 Typical IGBT output characteristics,  $I_c = f(V_{CE})$

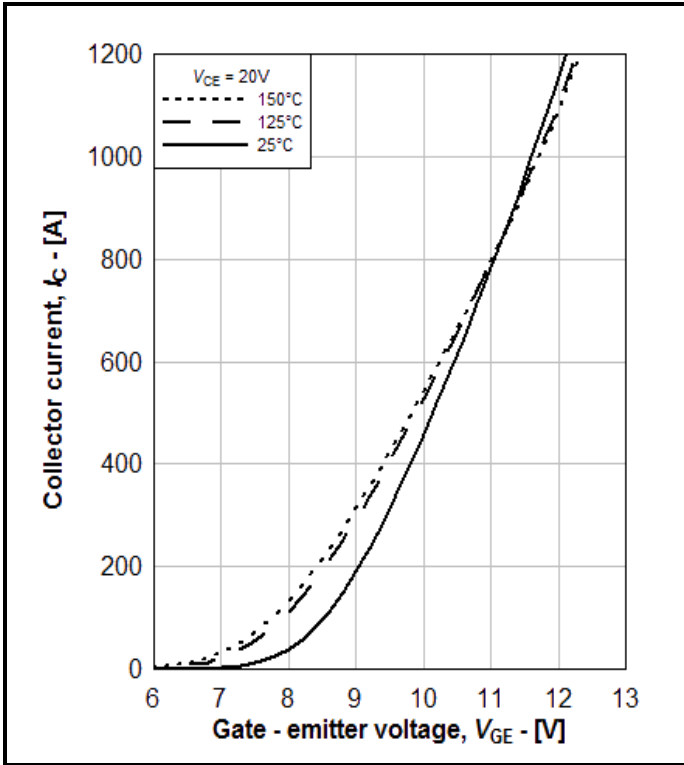


Fig. 6 Typical IGBT transfer characteristics,  $I_c = f(V_{GE})$

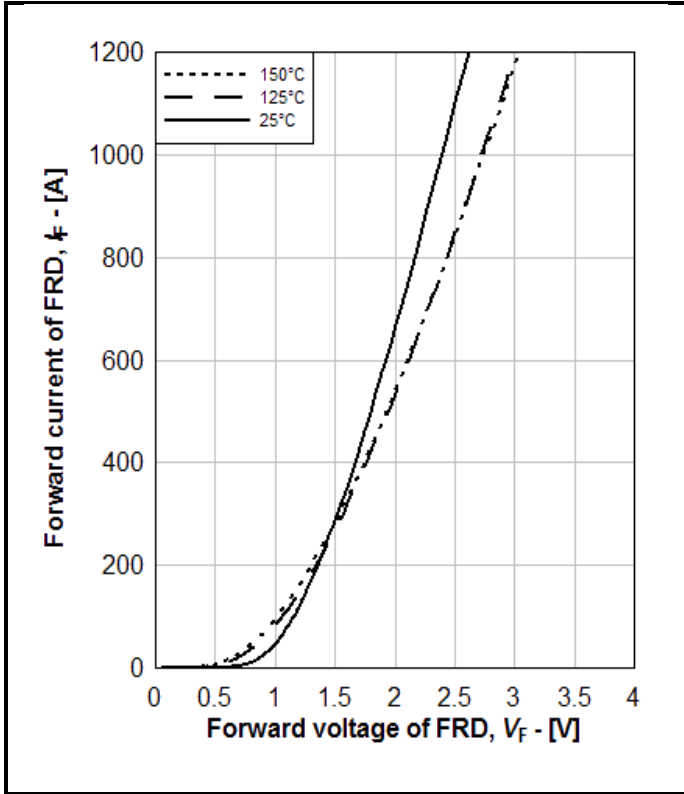


Fig. 7 Diode typical forward characteristics,  $I_F = f(V_F)$

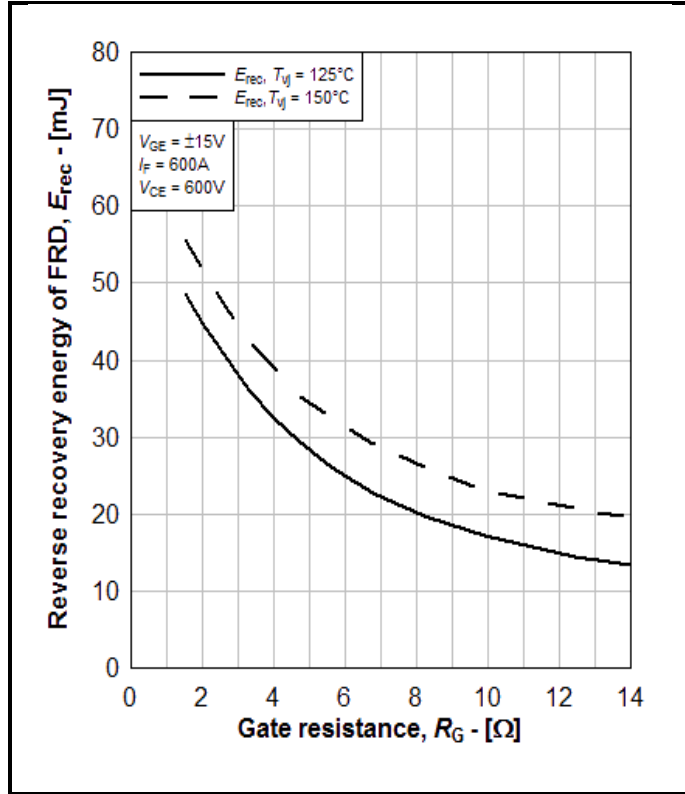


Fig. 8 Typical FRD  $E_{rec}$ ,  $E_{rec} = f(R_G)$

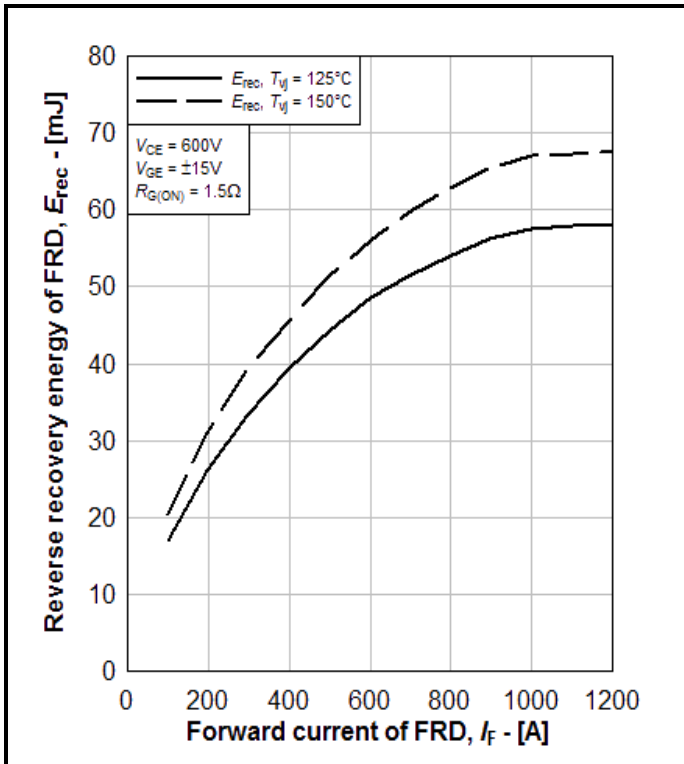


Fig. 9 Typical FRD  $E_{rec}$ ,  $E_{rec} = f(I_F)$

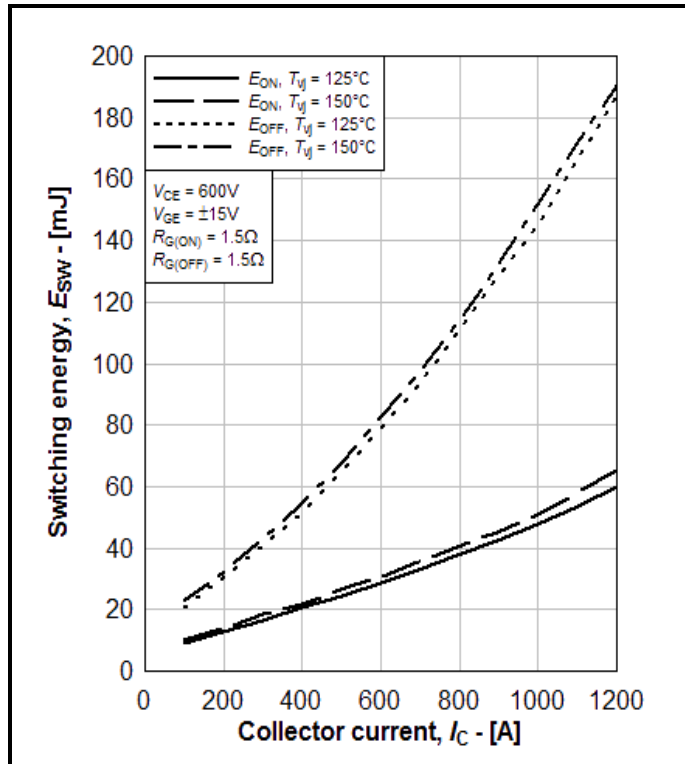


Fig. 10 Typical IGBT switching energy,  $E_{ON} = f(I_C)$ ,  $E_{OFF} = f(I_C)$

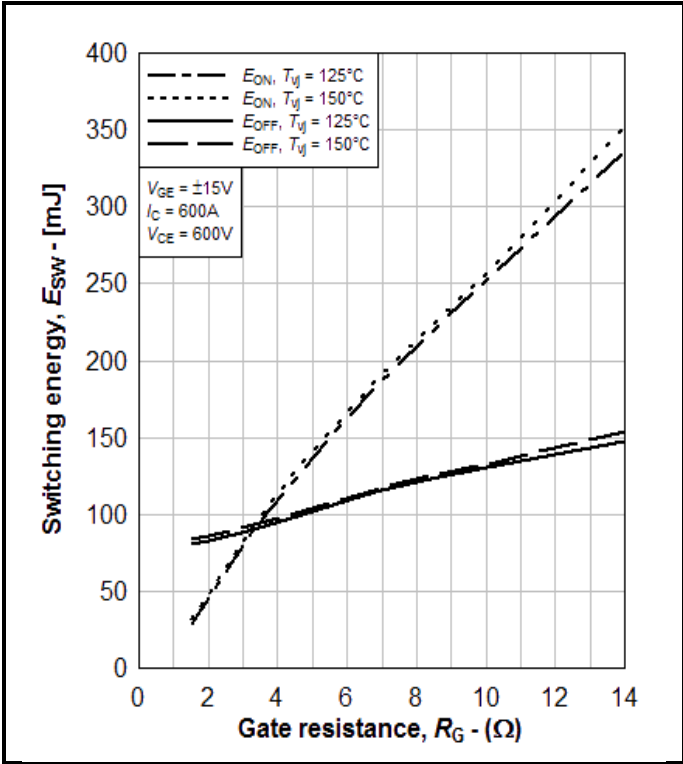


Fig. 11 Typical IGBT switching energy  
 $E_{ON} = f(R_G)$ ,  $E_{OFF} = f(R_G)$

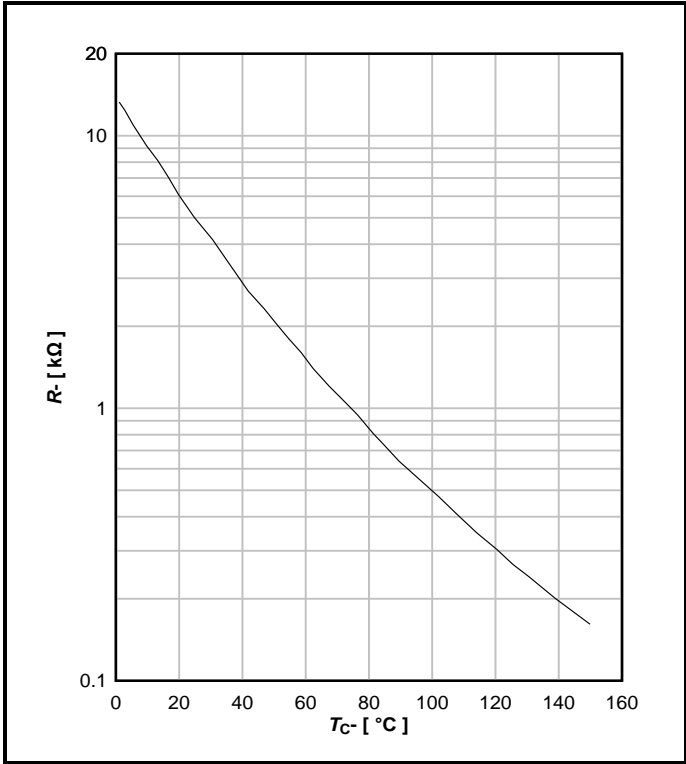


Fig. 12 Typical NTC thermistor characteristic,  $R = f(T_C)$

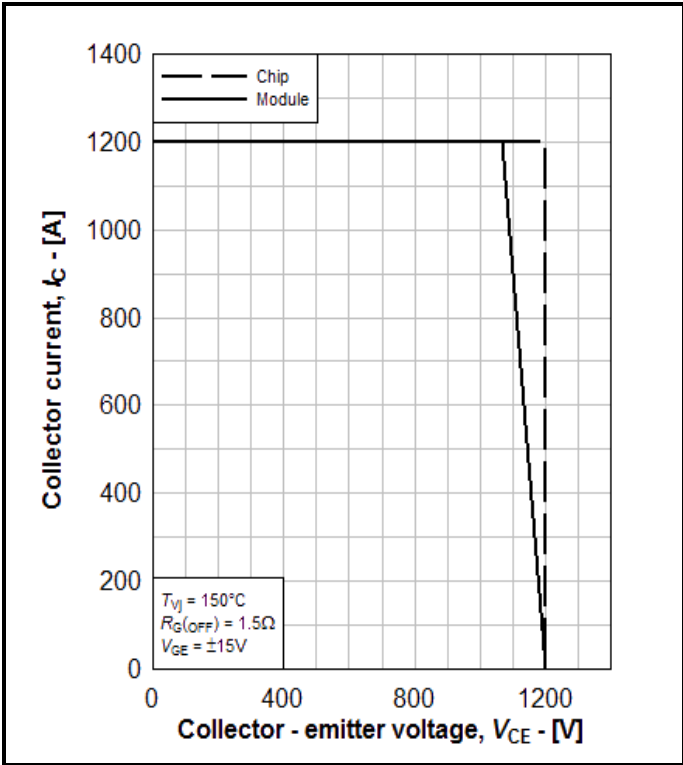


Fig. 13 Reverse bias safe operating area of IGBT,  
 $I_C = f(V_{CE})$

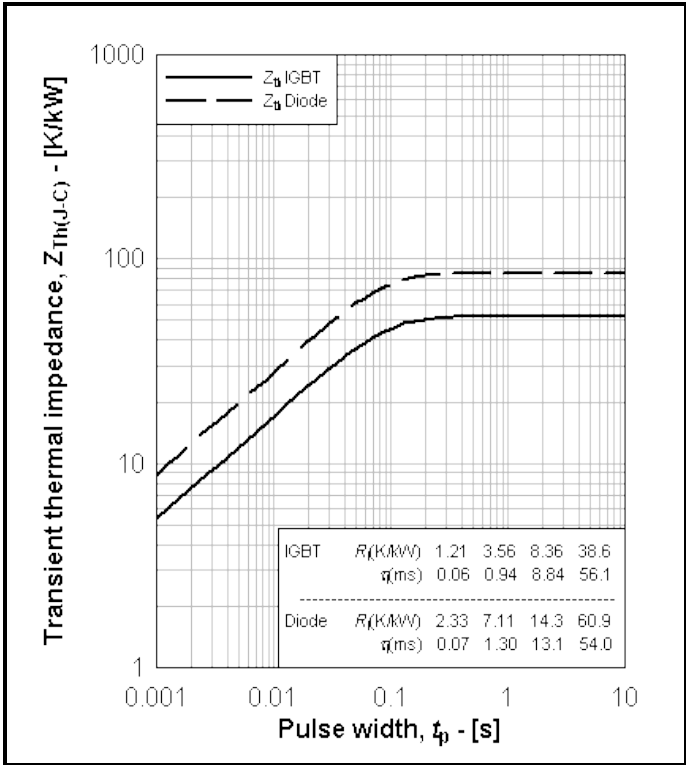
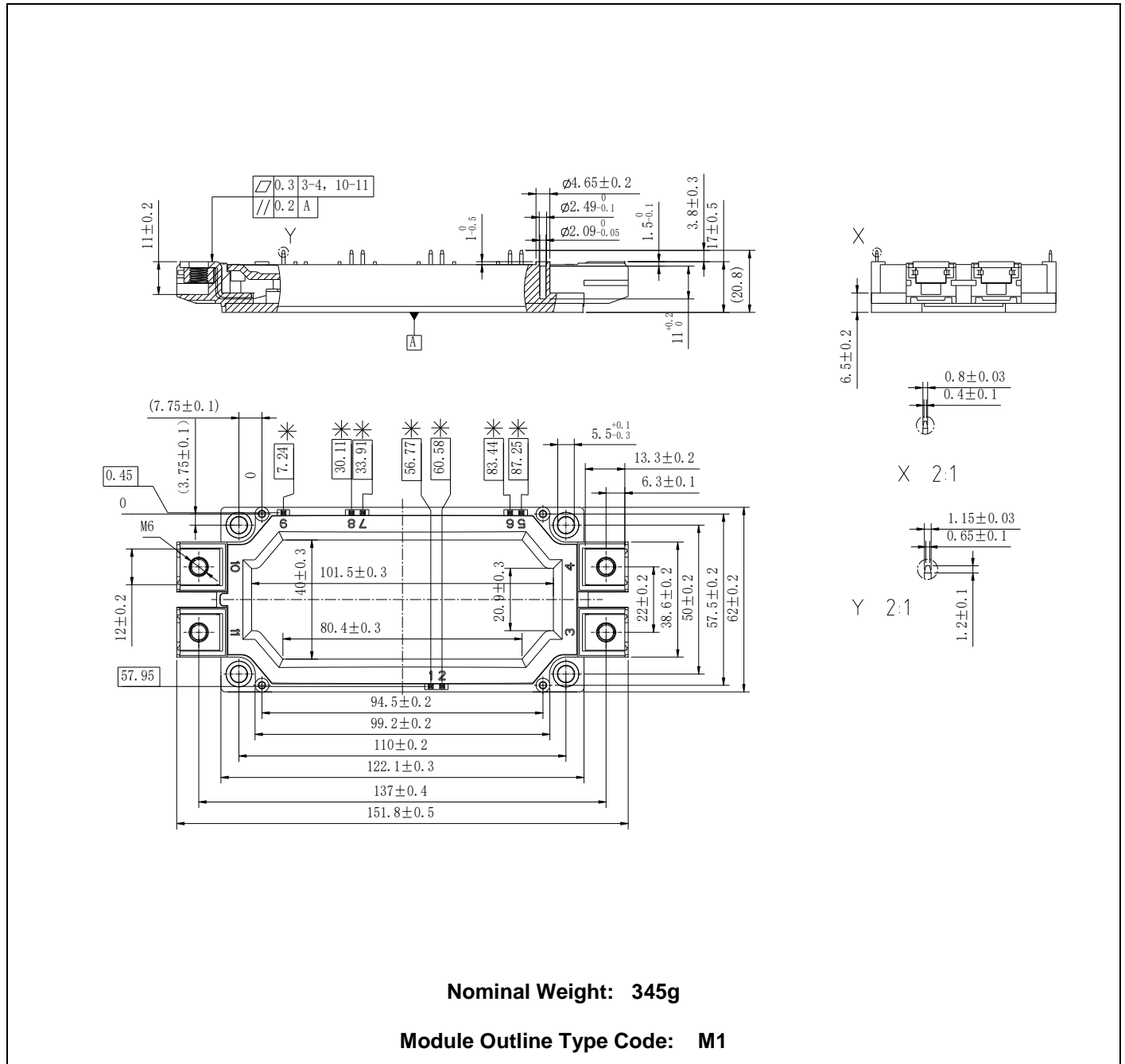


Fig. 14 Transient thermal impedance,  $Z_{th(J-C)} = f(t)$

**PACKAGE DETAILS**

For further package information, please visit our website or contact Customer Services.  
 All dimensions in mm, unless stated otherwise.  
**DO NOT SCALE.**



**Fig. 15 Module outline drawing**



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Extended exposure to conditions outside the product ratings may affect reliability leading to premature product failure. Use outside the product ratings is likely to cause permanent damage to the product. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture, a large current to flow or high voltage arcing, resulting in fire or explosion. Appropriate application design and safety precautions should always be followed to protect persons and property.

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